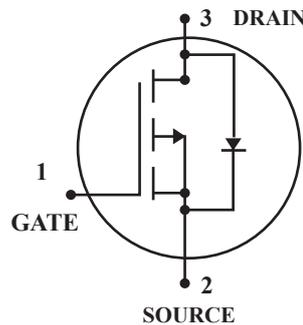


## P-Channel Enhancement Mode Power MOSFET

 Lead(Pb)-Free

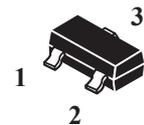


**DRAIN CURRENT**  
-3.2 AMPERES

**DRAIN SOURCE VOLTAGE**  
-30 VOLTAGE

### Features:

- \*Super High Dense Cell Design For Low  $R_{DS(ON)}$   
 $R_{DS(ON)} < 60m\Omega @ V_{GS} = -10V$
- \*Rugged and Reliable
- \*Capable of 2.5V Gate Drive
- \*Simple Drive Requirement
- \*SOT-23 Package



**SOT-23**

### Maximum Ratings ( $T_A = 25^\circ C$ Unless Otherwise Specified)

Rating	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	
Continuous Drain Current <sup>3</sup> , ( $T_A = 25^\circ C$ ) $I_D$ , ( $T_A = 70^\circ C$ )	$I_D$	-3.2	A
		-2.6	
Pulsed Drain Current <sup>1,2</sup>	$I_{DM}$	-10	
Total Power Dissipation ( $T_A = 25^\circ C$ )	$P_D$	1.38	W
Maximum Thermal Resistance Junction-ambient <sup>3</sup>	$R_{\theta JA}$	90	$^\circ C/W$
Operating Junction and Storage Temperature Range <sup>3</sup>	$T_J, T_{stg}$	-55~+150	$^\circ C$

### Device Marking

WTC2305A=2305A

## Electrical Characteristics ( $T_A = 25^\circ\text{C}$ Unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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### Static

Drain-Source Breakdown Voltage $V_{GS}=0, I_D=-250\mu\text{A}$	$V_{(BR)DSS}$	-30	-	-	V
Gate-Source Threshold Voltage $V_{DS}=V_{GS}, I_D=-250\mu\text{A}$	$V_{GS(Th)}$	-0.5	-	-1.2	
Gate-Source Leakage Current $V_{GS} = \pm 12\text{V}$	$I_{GSS}$	-	-	$\pm 100$	nA
Drain- Source Leakage Current( $T_j=25^\circ\text{C}$ ) $V_{DS}=-30\text{V}, V_{GS}=0$	$I_{DSS}$	-	-	-1	$\mu\text{A}$
Drain- Source Leakage Current( $T_j=70^\circ\text{C}$ ) $V_{DS}=-24\text{V}, V_{GS}=0$		-	-	-25	
Drain-Source On-Resistance <sup>2</sup> $V_{GS}=-10\text{V}, I_D=-3.2\text{A}$ $V_{GS}=-4.5\text{V}, I_D=-3.0\text{A}$ $V_{GS}=-2.5\text{V}, I_D=-2.0\text{A}$ $V_{GS}=-1.8\text{V}, I_D=-1.0\text{A}$	$R_{DS(on)}$	-	-	60 80 150 250	$\text{m}\Omega$
Forward Transconductance $V_{DS}=-5\text{V}, I_D=-3\text{A}$	$g_{fs}$	-	9	-	S

### Dynamic

Input Capacitance $V_{GS}=0\text{V}, V_{DS}=-25\text{V}, f=1.0\text{MHz}$	$C_{iss}$	-	735	1325	$\mu\text{F}$
Output Capacitance $V_{GS}=0\text{V}, V_{DS}=-25\text{V}, f=1.0\text{MHz}$	$C_{oss}$	-	100	-	
Reverse Transfer Capacitance $V_{GS}=0\text{V}, V_{DS}=-25\text{V}, f=1.0\text{MHz}$	$C_{rss}$	-	80	-	

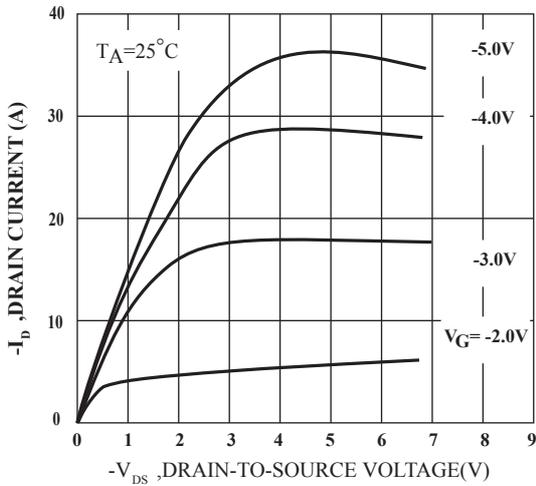
## Switching

Turn-on Delay Time <sup>2</sup> $V_{DS}=-15V, V_{GS}=-10V, I_D=-3.2A, R_D=4.6\Omega, R_G=3.3\Omega$	$t_{d(on)}$	-	7	-	ns
Rise Time $V_{DS}=-15V, V_{GS}=-10V, I_D=-3.2A, R_D=4.6\Omega, R_G=3.3\Omega$	$t_r$	-	15	-	
Turn-off Delay Time $V_{DS}=-15V, V_{GS}=-10V, I_D=-3.2A, R_D=4.6\Omega, R_G=3.3\Omega$	$t_{d(off)}$	-	21	-	
Fall Time $V_{DS}=-15V, V_{GS}=-10V, I_D=-3.2A, R_D=4.6\Omega, R_G=3.3\Omega$	$t_f$	-	15	-	
Total Gate Charge <sup>2</sup> $V_{DS}=-24V, V_{GS}=-4.5V, I_D=-3.2A$	$Q_g$	-	10	18	nC
Gate-Source Charge $V_{DS}=-24V, V_{GS}=-4.5V, I_D=-3.2A$	$Q_{gs}$	-	1.8	-	
Gate-Drain Charge $V_{DS}=-24V, V_{GS}=-4.5V, I_D=-3.2A$	$Q_{gd}$	-	3.6	-	

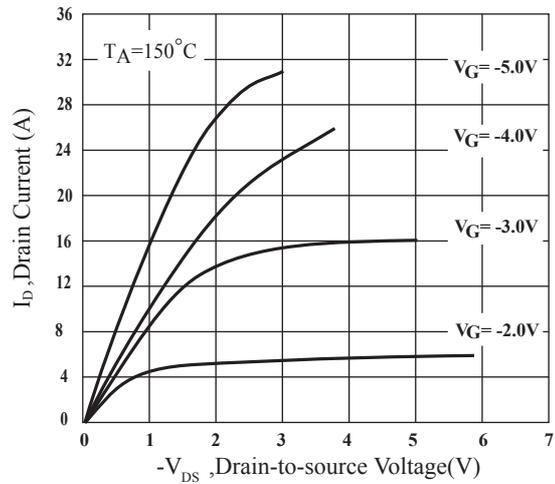
## Source-Drain Diode Characteristics

Forward On Voltage <sup>2</sup> $V_{GS}=0V, I_S=-1.2A$	$V_{SD}$	-	-	-1.2	V
Reverse Recovery Time <sup>2</sup> $V_{GS}=0V, I_S=-3.2A, di/dt=100A/\mu s$	$T_{rr}$	-	24	-	ns
Reverse Recovery Charge $V_{GS}=0V, I_S=-3.2A, di/dt=100A/\mu s$	$Q_{rr}$	-	19	-	nC

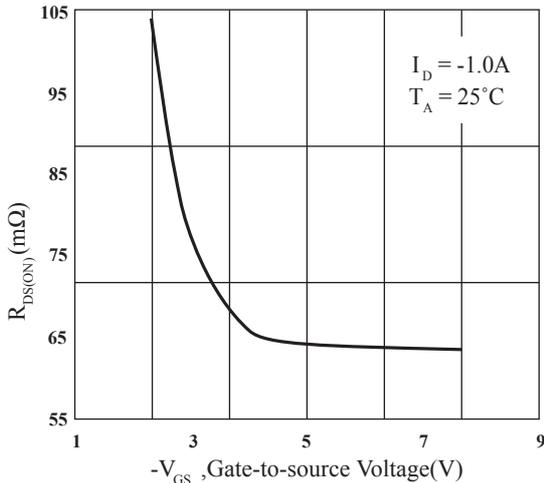
- Note: 1. Pulse width limited by Max, junction temperature.  
 2. pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .  
 3. Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board; 270°C/W when mounted on min, copper pad.



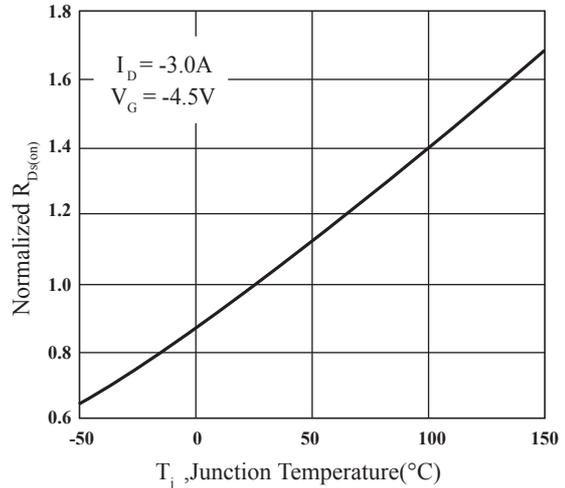
**FIG.1 Typical Output Characteristics**



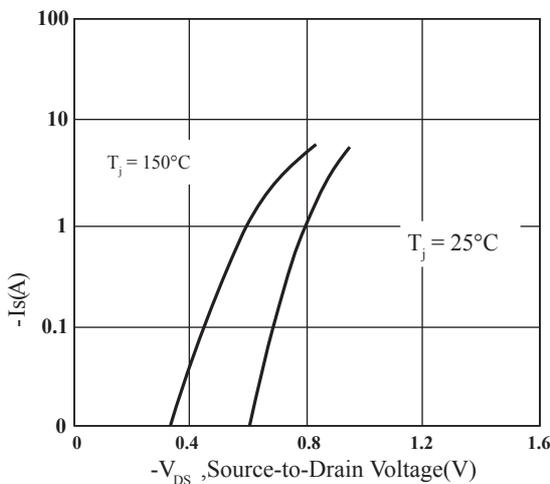
**Fig.2 Typical Output Characteristics**



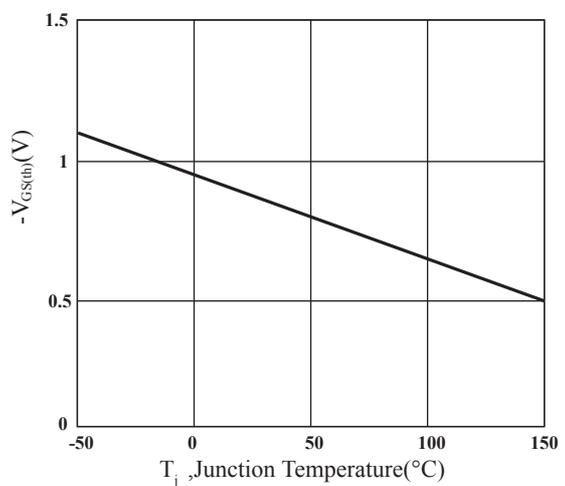
**FIG.3 On-Resistance v.s. Gate Voltage**



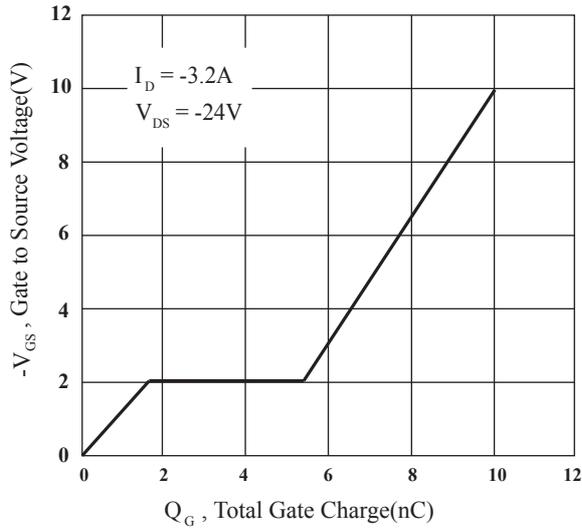
**Fig.4 Normalized On-Resistance**



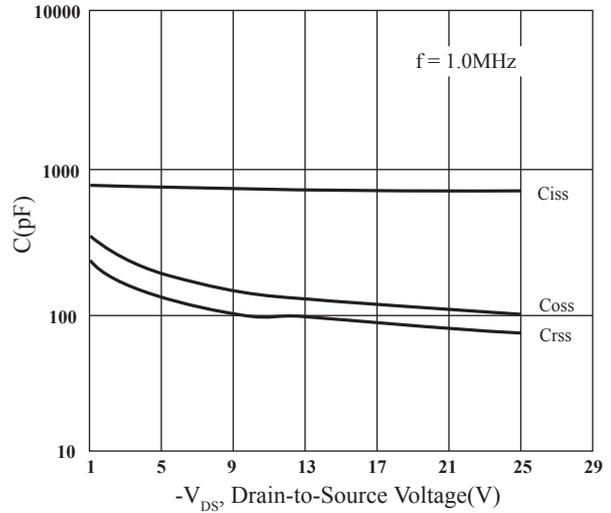
**Fig.5 Forward Characteristics of Reverse Diode**



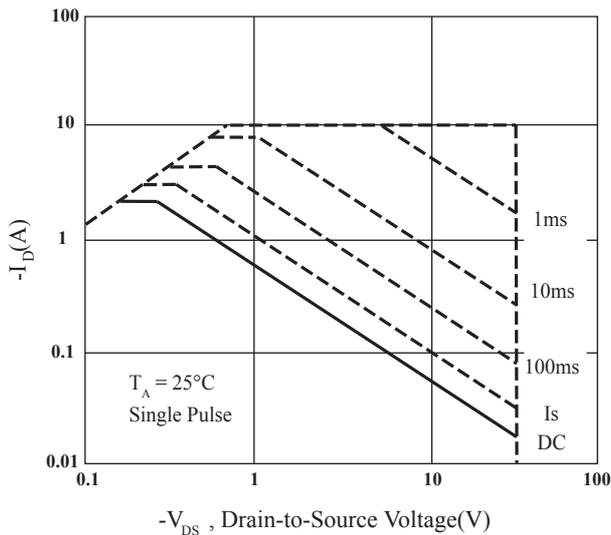
**Fig.6 Gate Threshold Voltage v.s. Junction Temperature**



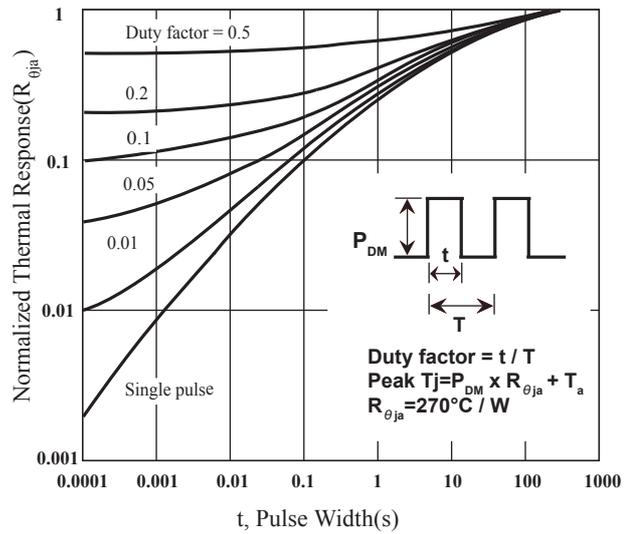
**Fig 7. Gate Charge Characteristics**



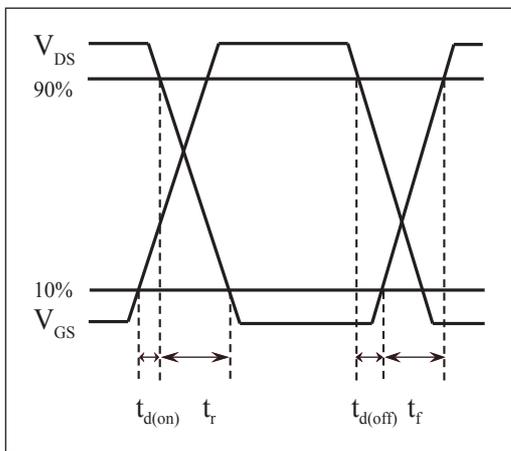
**Fig 8. Typical Capacitance Characteristics**



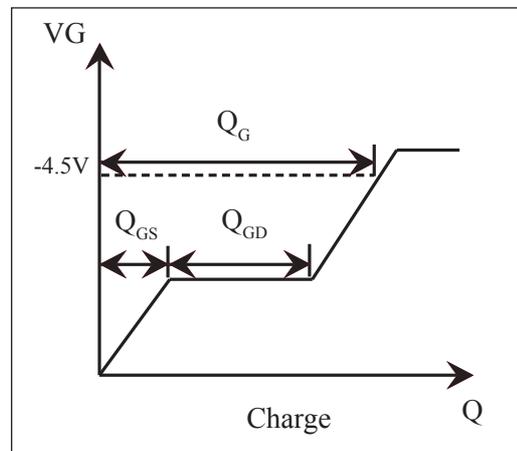
**Fig 9. Maximum Safe Operation Area**



**Fig 10. Effective Transient Thermal Impedance**



**Fig.11 Switching Time Waveform**



**Fig.12 Gate Charge Waveform**

**SOT-23 Outline Dimension**

