SUR560J

Epitaxial planar PNP silicon transistor

Descriptions

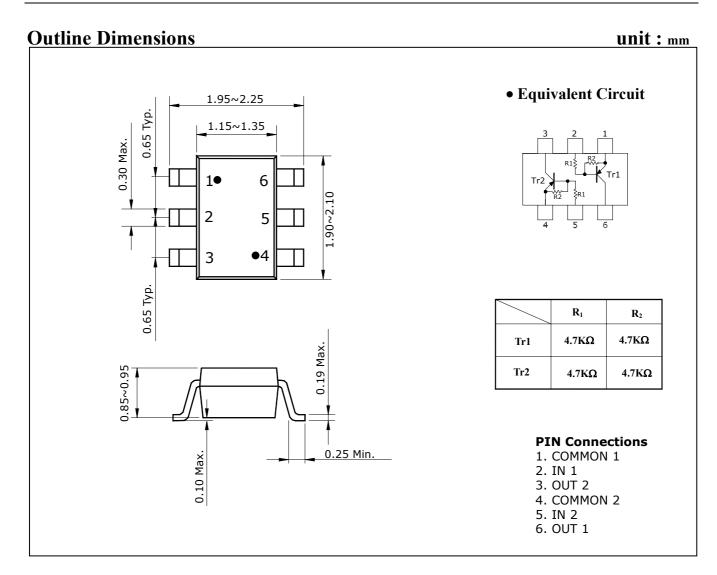
• Dual chip digital transistor

Features

- Two SRA2201 Chips in SOT-363 Package.
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

Ordering Information

Type NO.	Marking	Package Code	
SUR560J	OXO	SOT-363	



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Absolute Maximum Ratings [Tr1, Tr2]

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	$V_{\rm I}$	-20, 10	V
Output current	I_{O}	-100	mA
Power dissipation	P _D [∗]	200	mW
Junction temperature	T _J	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

※: Total rating

Electrical Characteristics [Tr1, Tr2]

(Ta=25°C)

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Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_I = 0$	-	-	-500	nA
DC current gain	G_{I}	V_0 =-5V, I_0 =-10mA	30	55	-	-
Output voltage	V _{O(ON)}	I_{O} =-10mA, I_{I} =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_0 = -0.2V$, $I_0 = -5mA$	-	-1.5	-2.0	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = -5V$, $I_0 = -0.1$ mA	-1.0	-1.2	-	V
Transition frequency	f _T *	V_0 =-10V, I_0 =-5mA, f=1MHz	-	200	-	MHz
Input current	$I_{\rm I}$	V_I =-5V, I_O =0	-	-	-1.8	mA
Input resistor (Input to base)	R ₁	-	3.3	4.7	6.1	K Ω
Input resistor (Base to common)	R ₂	-	3.3	4.7	6.1	K Ω

^{* :} Characteristic of transistor only

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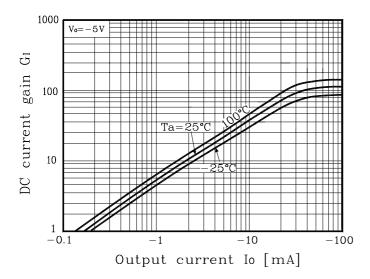
Electrical Characteristic Curves [Tr1,Tr2]

Fig. 1 I_0 - $V_{I(ON)}$ Output current Io [mA] -10-0.1-0.01 -0.01

Input on voltage Vi(on) [V]

Fig. 2 I_O - V_{I(OFF)} -10000 $V_0 = -5V$ Output current Io [#A] -1000 -100-10-1-0.4-0.8 -2.0Input off voltage Vi(off) [V]

Fig. 3 G_I - I_O



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