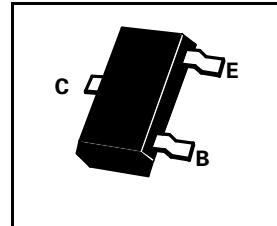


SOT23 NPN SILICON PLANAR SWITCHING TRANSISTOR

ISSUE 2 – MARCH 94

FMMT4124

PARTMARKING DETAIL – ZC



ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	VALUE	UNIT
Collector-Base Voltage	V_{CBO}	30	V
Collector-Emitter Voltage	V_{CEO}	25	V
Emitter-Base Voltage	V_{EBO}	5	V
Continuous Collector Current	I_C	200	mA
Power Dissipation at $T_{amb}=25^\circ\text{C}$	P_{tot}	330	mW
Operating and Storage Temperature Range	$T_j; T_{stg}$	-55 to +150	°C

ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25^\circ\text{C}$).

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	CONDITIONS.
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	30		V	$I_C=10\mu\text{A}$
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	25		V	$I_C=1\text{mA}^*$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	5		V	$I_E=10\mu\text{A}$
Collector Cut-Off Current	I_{CBO}		50	nA	$V_{CB}=20\text{V}$
Emitter Cut-Off Current	I_{EBO}		50	nA	$V_{EB}=3\text{V}$
Collector-Emitter Saturation Voltage	$V_{CE(\text{sat})}$		0.3	V	$I_C=50\text{mA}, I_B=5\text{mA}^*$
Base-Emitter Saturation Voltage	$V_{BE(\text{sat})}$		0.95	V	$I_C=50\text{mA}, I_B=5\text{mA}^*$
Static Forward Current Transfer Ratio	h_{FE}	120 60	360		$I_C=2\text{mA}, V_{CE}=1\text{V}^*$ $I_C=50\text{mA}, V_{CE}=1\text{V}^*$
Transistion Frequency	f_T	300		MHz	$I_C=10\text{mA}, V_{CE}=20\text{V}, f=100\text{MHz}$
Output Capacitance	C_{obo}		4	pF	$V_{CB}=5\text{V}, I_E=0, f=140\text{KHz}$
Input Capacitance	C_{ibo}		8	pF	$V_{BE}=0.5\text{V}, I_E=0, f=140\text{KHz}$
Noise Figure	N		6	dB	$I_C=200\mu\text{A}, V_{CE}=5\text{V}, R_g=2\text{k}\Omega$ $f=30\text{Hz}$ to 15KHz at 3dB points
Small Signal Current Transfer	h_{fe}	120	480		$I_C=2\text{mA}, V_{CE}=1\text{V}, f=1\text{KHz}$

SWITCHING CHARACTERISTICS (at $T_{amb} = 25^\circ\text{C}$).

PARAMETER	SYMBOL	TYP.	UNIT	CONDITIONS
Delay Time	t_d	24	ns	$V_{CC}=3\text{V}, V_{BE(\text{off})}=0.5\text{V}$
Rise Time	t_r	13	ns	$I_C=10\text{mA}, I_{B1}=1\text{mA}$
Storage Time	t_s	125	ns	$V_{CC}=3\text{V}, I_C=10\text{mA}$
Fall Time	t_f	11	ns	$I_{B1}=I_{B2}=1\text{mA}$

*Measured under pulsed conditions. Pulse width=300μs. Duty cycle ≤ 2%