

# CM75DY-34A

HIGH POWER SWITCHING USE

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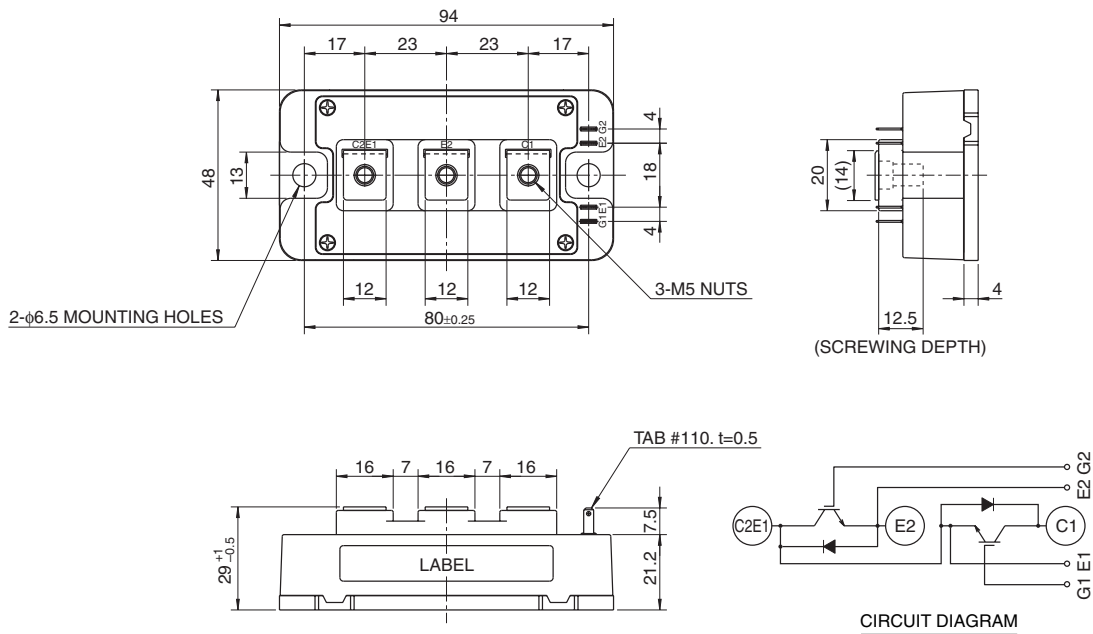
- IC ..... 75A
- VCES ..... 1700V
- Insulated Type
- 2-elements in a pack

## APPLICATION

General purpose inverters & Servo controls, etc

## OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



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**ABSOLUTE MAXIMUM RATINGS** (T<sub>J</sub> = 25°C, unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>CE</sub> S	Collector-emitter voltage	G-E Short	1700	V
V <sub>GE</sub> S	Gate-emitter voltage	C-E Short	±20	V
I <sub>C</sub>	Collector current	DC, T <sub>C</sub> = 111°C <sup>*1</sup>	75	A
I <sub>CM</sub>		Pulse	150	
I <sub>E</sub> (Note 1)	Emitter current	Operation	75	A
I <sub>EM</sub> (Note 1)		Pulse	150	
P <sub>C</sub> (Note 3)	Maximum collector dissipation	T <sub>C</sub> = 25°C <sup>*1</sup>	780	W
T <sub>J</sub>	Junction temperature		-40 ~ +150	°C
T <sub>stg</sub>	Storage temperature		-40 ~ +125	°C
V <sub>iso</sub>	Isolation voltage	Main terminal to base plate, AC 1 min.	3500	V
—	Torque strength	Main terminal M5	2.5 ~ 3.5	N • m
—		Mounting holes M6	3.5 ~ 4.5	
—	Weight	Typical value	310	g

**ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C, unless otherwise specified)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
I <sub>CE</sub> S	Collector cutoff current	V <sub>CE</sub> = V <sub>CE</sub> S, V <sub>GE</sub> = 0V	—	—	1	mA
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	I <sub>C</sub> = 7.5mA, V <sub>CE</sub> = 10V	5.5	7.0	8.5	V
I <sub>GE</sub> S	Gate leakage current	±V <sub>GE</sub> = V <sub>GE</sub> S, V <sub>CE</sub> = 0V	—	—	2.0	µA
V <sub>CE(sat)</sub>	Collector to emitter saturation voltage	T <sub>J</sub> = 25°C	—	2.2	2.8	V
		T <sub>J</sub> = 125°C	—	2.45	—	
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 10V V <sub>GE</sub> = 0V	—	—	18.5	nF
C <sub>oes</sub>	Output capacitance		—	—	2.1	
C <sub>res</sub>	Reverse transfer capacitance		—	—	0.4	
Q <sub>G</sub>	Total gate charge	V <sub>CC</sub> = 1000V, I <sub>C</sub> = 75A, V <sub>GE</sub> = 15V	—	500	—	nC
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> = 1000V, I <sub>C</sub> = 75A V <sub>GE1</sub> = V <sub>GE2</sub> = 15V R <sub>G</sub> = 6.4Ω, Inductive load switching operation I <sub>E</sub> = 75A	—	—	200	ns
t <sub>r</sub>	Turn-on rise time		—	—	150	
t <sub>d(off)</sub>	Turn-off delay time		—	—	550	
t <sub>f</sub>	Turn-off fall time		—	—	350	
t <sub>rr</sub> (Note 1)	Reverse recovery time		—	—	300	
Q <sub>rr</sub> (Note 1)	Reverse recovery charge	—	7.5	—	µC	
V <sub>EC</sub> (Note 1)	Emitter-collector voltage	I <sub>E</sub> = 75A, V <sub>GE</sub> = 0V	—	—	3.0	V
R <sub>th(j-c)Q</sub>	Thermal resistance	IGBT part (1/2 module) <sup>*1</sup>	—	—	0.16	°C/W
R <sub>th(j-c)R</sub>		FWDi part (1/2 module) <sup>*1</sup>	—	—	0.29	
R <sub>th(c-f)</sub>	Contact thermal resistance	Case to fin, Thermal compound applied (1/2 module) <sup>*1,*2</sup>	—	0.022	—	
R <sub>G</sub>	External gate resistance		6.4	—	64	Ω

\*1 : T<sub>C</sub>, T<sub>f</sub> measured point is just under the chips.

\*2 : Typical value is measured by using Shin-Etsu Chemical Co.,Ltd "G-746".

Note 1. I<sub>E</sub>, I<sub>EM</sub>, V<sub>EC</sub>, t<sub>rr</sub> & Q<sub>rr</sub> represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWDi).

2. Pulse width and repetition rate should be such that the device junction temperature (T<sub>J</sub>) does not exceed T<sub>Jmax</sub> rating.

3. Junction temperature (T<sub>J</sub>) should not increase beyond 150°C.

4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

PERFORMANCE CURVES

