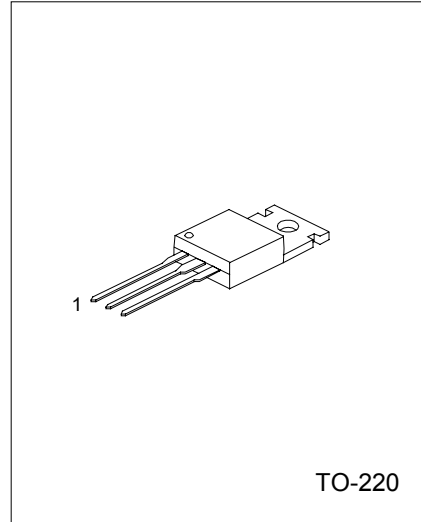


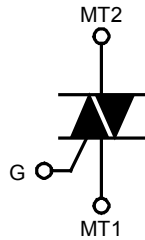
TRIACS LOGIC LEVEL

DESCRIPTION

Passivated triacs in a plastic envelope, intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating voltages and static switching.



SYMBOL



1:MT1 2:MT2 3:GATE

ABSOLUTE MAXIMUM RATINGS (T_j=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Repetitive Peak Off State Voltage	V _{DRM}	600 800	V
		BT138-600 BT138-800	
RMS On-state Current (Full sine wave; T _{mb} ≤99°C)	I _{T(RMS)}	12	A
Non-repetitive Peak. On-State Current (Full sine wave; T _j =25°C prior to surge)	I _{TSM}	95 105	A
		t=20ms t=16.7ms	
I ² t For Fusing (t=10ms)	I ² t	45	A ² s
Repetitive Rate of Rise of On-state Current after Triggering (I _{TM} =20A; I _G =0.2A; dI _G /dt=0.2A/μs)	dI _T /dt	50 50 50 10	A/μs
		T2+G+ T2+G- T2-G- T2-G+	
Peak Gate Voltage	V _{GM}	5	V
Peak Gate Current	I _{GM}	2	A
Peak Gate Power	P _{GM}	5	W
Average Gate Power	P _{G(AV)}	0.5	W
Operating Junction Temperature	T _j	125	°C
Storage Temperature	T _{stg}	-40~150	°C

*Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15A/μs.

THERMAL RESISTANCES

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Thermal Resistance, Junction to Mounting Base Full cycle Half cycle	$R_{\theta j-mb}$			1.5 2.0	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction to Ambient In free air	$R_{\theta j-a}$		60	-	$^{\circ}\text{C}/\text{W}$

STATIC CHARACTERISTICS (T_j=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Gate Trigger Current T2+G+ T2+G- T2-G- T2-G+	I _{GT}	V _D =12V, I _T =0.1A		5 8 10 12	35 35 35 70	mA
Latching Current T2+G+ T2+G- T2-G- T2-G+	I _L	V _D =12V, I _{GT} =0.1A		7 20 8 10	40 60 40 60	mA
Holding Current	I _H	V _D =12V, I _{GT} =0.1A		6	30	mA
On-State Voltage	V _T	I _T =15A		1.4	1.65	V
Gate Trigger Voltage	V _{GT}	V _D =12V, I _T =0.1A V _D =400V, I _T =0.1A, T _j =125°C	0.25	0.7 0.4	1.5	V
Off-state Leakage Current	I _D	V _D =V _{DRM(max)} , T _j =125°C		0.1	0.5	mA

DYNAMIC CHARACTERISTICS (T_j=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Critical Rate Of Rise Of Off-State Voltage	dV _D /dt	V _{DM} =67% V _{DRM(max)} , T _j =125°C Exponential waveform, Gate open circuit	100	250		V/μs
Critical Rate Of Change Of Commutating Voltage	dV _{com} /dt	V _{DM} =400V, T _j =95°C, I _{T(RMS)} =12A dI _{com} /dt =5.4A/ms, Gate open circuit		20		V/μs
Gate Controlled Turn-on Time	t _{gt}	I _{TM} =16A, V _D =V _{DRM(max)} , I _G =0.1A dI _G /dt=5A/μs		2		μs

TYPICAL CHARACTERISTICS

Figure 1. Maximum on-state Dissipation, P_{tot} vs RMS On-state Current, $I_T(RMS)$, Where α = conduction Angle.

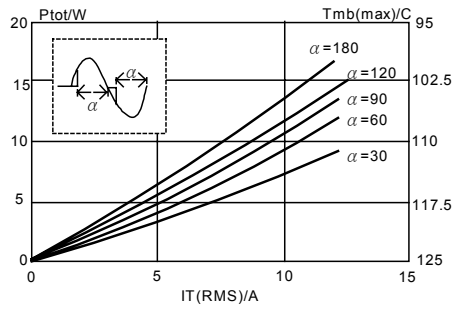


Figure 4. Maximum Permissible RMS Current $I_T(RMS)$ vs mounting base Temperature T_{mb}

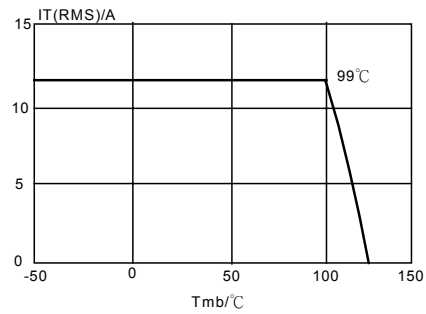


Figure 2. Maximum Permissible Non-repetitive Peak On-state Current I_{TSM} , vs Pulse Width t_p , for Sinusoidal Currents, $t_p \approx 20ms$

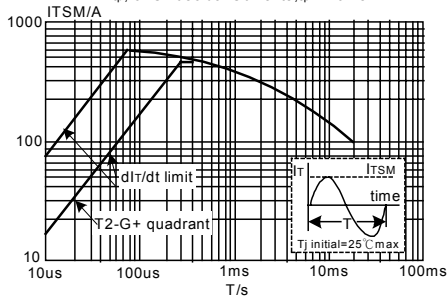


Figure 5. Maximum Permissible Repetitive RMS on-state Current $I_T(RMS)$, vs Surge Duration, for Sinusoidal Currents, $f=50Hz$, $T_{mb} \approx 99^\circ C$

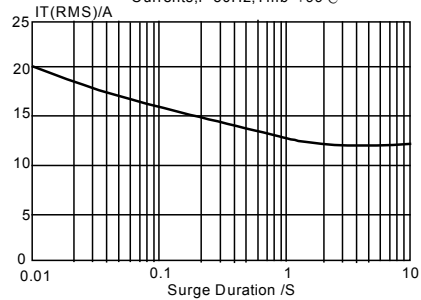


Figure 3. Maximum Permissible Non-Repetitive peak on-state Current I_{TSM} , vs Number of Cycles, for Sinusoidal Currents, $f=50Hz$

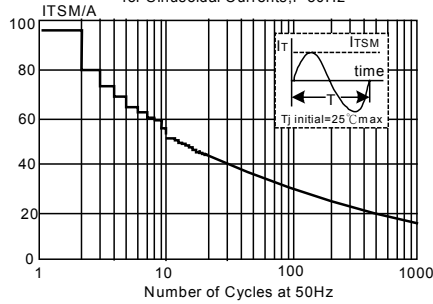
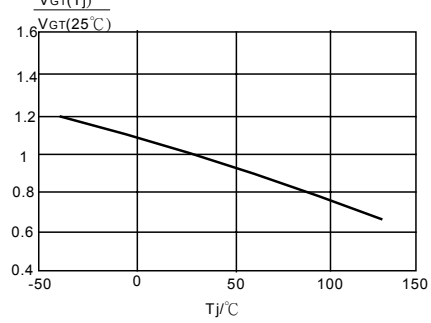
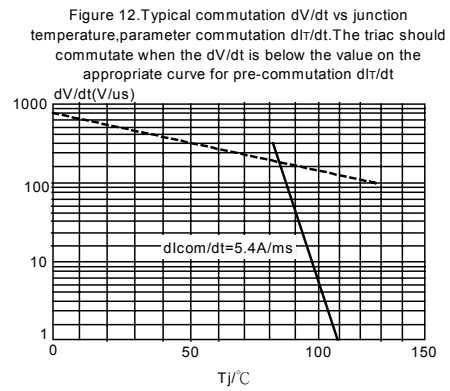
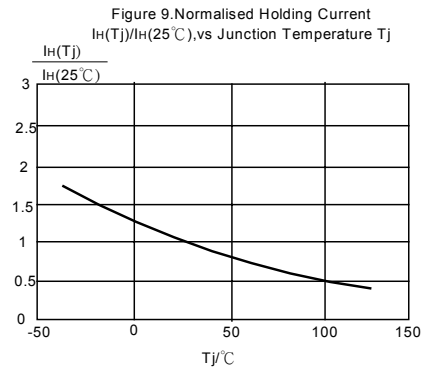
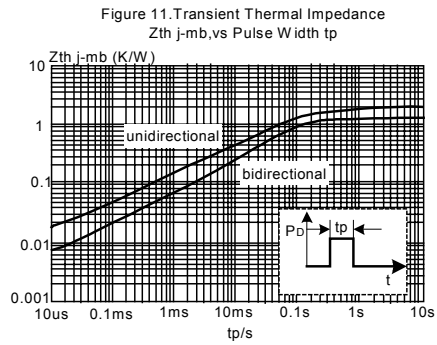
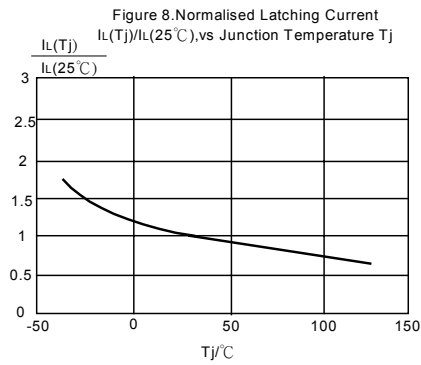
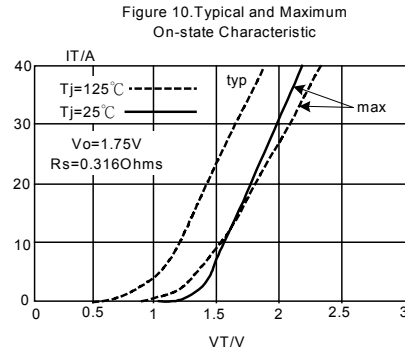
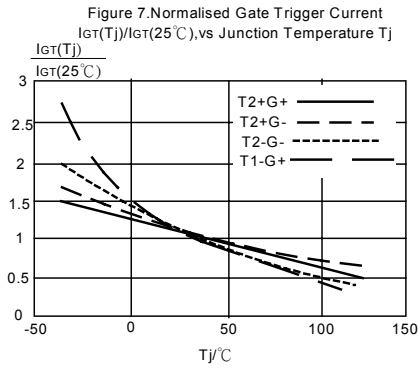


Figure 6. Normalised Gate Trigger Voltage $V_{GT}(T_j) / V_{GT}(25^\circ C)$, vs Junction Temperature T_j





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