

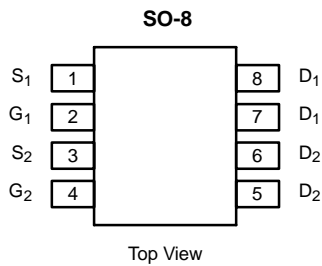


## Dual P-Channel 30-V(D-S) MOSFET

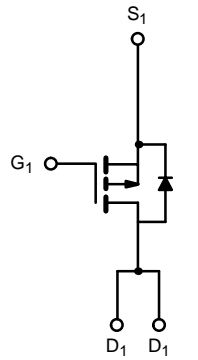
| PRODUCT SUMMARY |                           |           |
|-----------------|---------------------------|-----------|
| $V_{DS}$ (V)    | $r_{DS(on)}$ ( $\Omega$ ) | $I_D$ (A) |
| -30             | 0.053 @ $V_{GS} = -10$ V  | -4.9      |
|                 | 0.095 @ $V_{GS} = -4.5$ V | -3.6      |

**FEATURES**

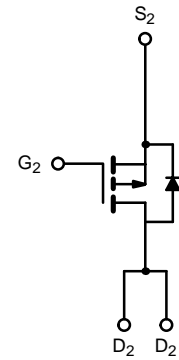
- 100%  $R_g$  Tested



Ordering Information: Si4953DY  
Si4953DY-T1 (with Tape and Reel)



P-Channel MOSFET



P-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED) |                |                          |                  |
|---|----------------|--------------------------|------------------|
| Parameter   | Symbol         | Limit                    | Unit             |
| Drain-Source Voltage  | $V_{DS}$       | -30                      | V                |
| Gate-Source Voltage   | $V_{GS}$       | $\pm 20$                 |                  |
| Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>         | $I_D$          | $T_A = 25^\circ\text{C}$ | -4.9             |
|   |                | $T_A = 70^\circ\text{C}$ | -3.9             |
| Pulsed Drain Current  | $I_{DM}$       | -30                      | A                |
| Continuous Source Current (Diode Conduction) <sup>a</sup>                   | $I_S$          | -1.7                     |                  |
| Maximum Power Dissipation <sup>a</sup>                                      | $P_D$          | $T_A = 25^\circ\text{C}$ | 2.0              |
|   |                | $T_A = 70^\circ\text{C}$ | 1.3              |
| Operating Junction and Storage Temperature Range                            | $T_J, T_{stg}$ | -55 to 150               | $^\circ\text{C}$ |

| THERMAL RESISTANCE RATINGS               |            |       |                           |
|--|------------|-------|---------------------------|
| Parameter                                | Symbol     | Limit | Unit                      |
| Maximum Junction-to-Ambient <sup>a</sup> | $R_{thJA}$ | 62.5  | $^\circ\text{C}/\text{W}$ |

Notes

a. Surface Mounted on FR4 Board,  $t \leq 10$  sec.

For SPICE model information via the Worldwide Web: <http://www.vishay.com>

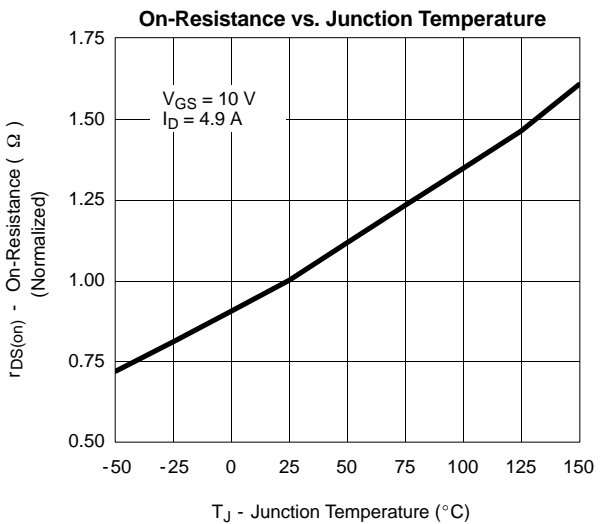
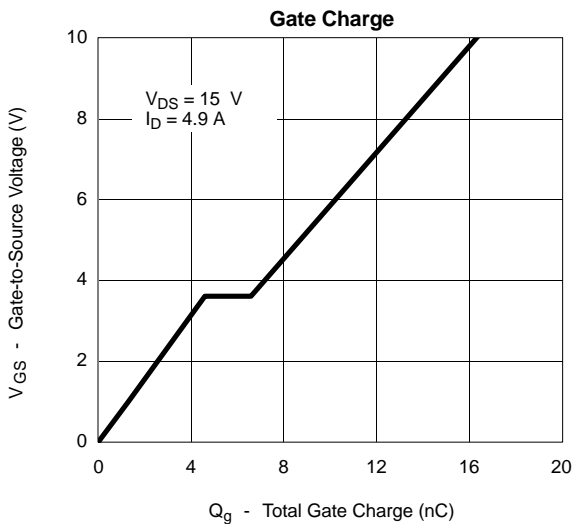
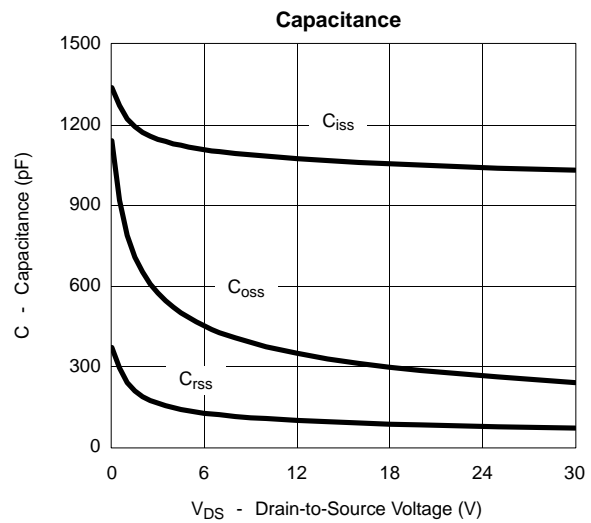
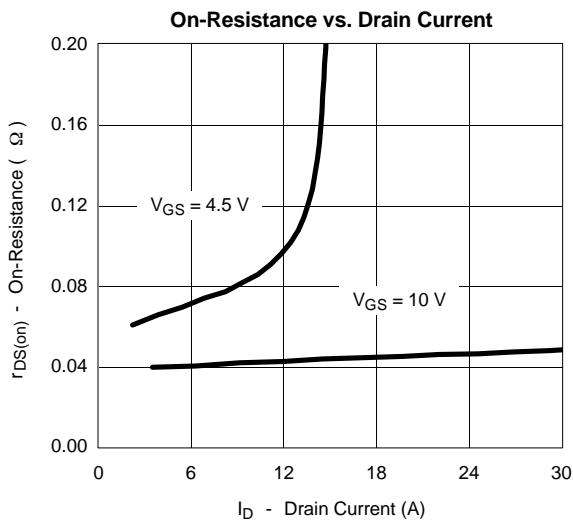
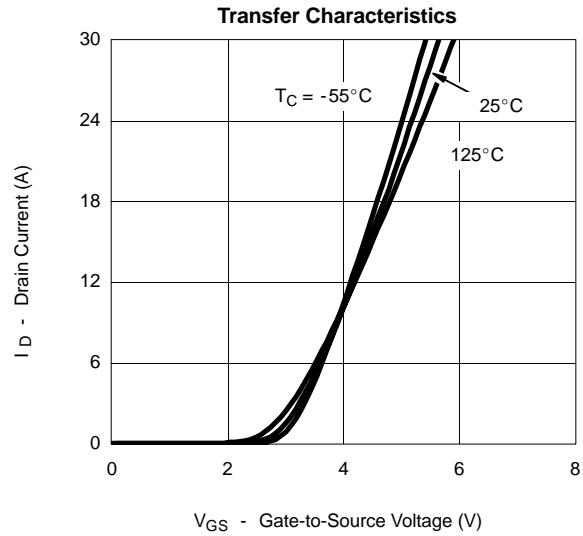
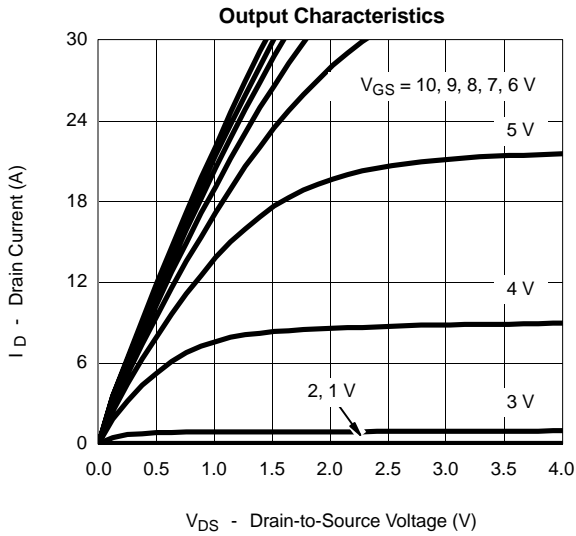
| SPECIFICATIONS (T <sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED) |                     |   |     |                  |       |      |
|--|---------------------|---|-----|------------------|-------|------|
| Parameter  | Symbol              | Test Condition  | Min | Typ <sup>a</sup> | Max   | Unit |
| <b>Static</b>  |                     |   |     |                  |       |      |
| Gate Threshold Voltage   | V <sub>GS(th)</sub> | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA  | -1  |                  |       | V    |
| Gate-Body Leakage  | I <sub>GSS</sub>    | V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V  |     |                  | ±100  | nA   |
| Zero Gate Voltage Drain Current                                | I <sub>DSS</sub>    | V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V  |     |                  | -1    | μA   |
|  |                     | V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C  |     |                  | -25   |      |
| On-State Drain Current <sup>b</sup>                            | I <sub>D(on)</sub>  | V <sub>DS</sub> ≤ -5 V, V <sub>GS</sub> = -10 V   | -20 |                  |       | A    |
| Drain-Source On-State Resistance <sup>b</sup>                  | r <sub>DS(on)</sub> | V <sub>GS</sub> = -10 V, I <sub>D</sub> = -4.9 A  |     | 0.043            | 0.053 | Ω    |
|  |                     | V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -3.6 A   |     | 0.070            | 0.095 |      |
| Forward Transconductance <sup>b</sup>                          | g <sub>fs</sub>     | V <sub>DS</sub> = -15 V, I <sub>D</sub> = -4.9 A  |     | 10               |       | S    |
| Diode Forward Voltage <sup>b</sup>                             | V <sub>SD</sub>     | I <sub>S</sub> = -1.7 A, V <sub>GS</sub> = 0 V  |     | 0.8              | -1.2  | V    |
| <b>Dynamic<sup>a</sup></b>                                     |                     |   |     |                  |       |      |
| Total Gate Charge  | Q <sub>g</sub>      | V <sub>DS</sub> = -15 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -4.9 A   |     | 16               | 25    | nC   |
| Gate-Source Charge   | Q <sub>gs</sub>     |   |     | 5                |       |      |
| Gate-Drain Charge  | Q <sub>gd</sub>     |   |     | 2                |       |      |
| Gate Resistance  | R <sub>g</sub>      |   | 2   |                  | 7.1   | Ω    |
| Turn-On Delay Time   | t <sub>d(on)</sub>  | V <sub>DD</sub> = -15 V, R <sub>L</sub> = 15 Ω<br>I <sub>D</sub> ≅ -1 A, V <sub>GEN</sub> = -10 V, R <sub>G</sub> = 6 Ω |     | 9                | 15    | ns   |
| Rise Time  | t <sub>r</sub>      |   |     | 13               | 20    |      |
| Turn-Off Delay Time  | t <sub>d(off)</sub> |   |     | 25               | 40    |      |
| Fall Time  | t <sub>f</sub>      |   |     | 15               | 25    |      |
| Source-Drain Reverse Recovery Time                             | t <sub>rr</sub>     | I <sub>F</sub> = -1.7 A, di/dt = 100 A/μs   |     | 60               | 90    |      |

## Notes

- a. For design aid only; not subject to production testing.  
b. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.



**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**



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