

## N-Channel 1.8-V (G-S) MOSFET With Schottky Diode

<b>MOSFET PRODUCT SUMMARY</b>		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
20	0.040 @ $V_{GS} = 4.5$ V	5.9
	0.045 @ $V_{GS} = 2.5$ V	5.6
	0.052 @ $V_{GS} = 1.8$ V	5.2

<b>SCHOTTKY PRODUCT SUMMARY</b>		
$V_{KA}$ (V)	Diode Forward Voltage	$I_F$ (A)
20	0.375 V @ 1.0	1.0

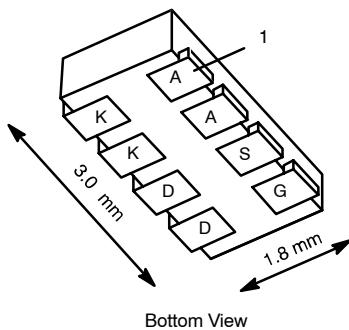
### FEATURES

- TrenchFET® Power MOSFETs
- Ultra Low  $r_{DS(on)}$
- Ultra Low  $V_F$  Schottky
- Si5853DC Pin Compatible

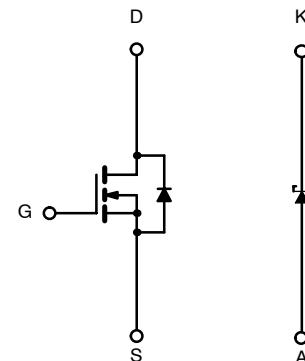
### APPLICATIONS

- Buck Rectifier Switch, Buck-Boost
- Synchronous Rectifier or Load
- Switch For Portable Devices

1206-8 ChipFET®



Marking Code  
  
 JD      Lot Traceability and Date Code  
 XXX    Part # Code



Ordering Information: Si5856DC-T1

N-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	5 sec	Steady State	Unit	
Drain-Source Voltage (MOSFET and Schottky)	$V_{DS}$	20	20	V	
Reverse Voltage (Schottky)	$V_{KA}$				
Gate-Source Voltage (MOSFET)	$V_{GS}$				
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) (MOSFET) <sup>a</sup>	$I_D$	5.9	4.4	A	
		4.2	3.1		
Pulsed Drain Current (MOSFET)	$I_{DM}$	20		W	
Continuous Source Current (MOSFET Diode Conduction) <sup>a</sup>	$I_S$	1.8	0.9		
Average Forward Current (Schottky)	$I_F$	1.0			
Pulsed Forward Current (Schottky)	$I_{FM}$	7			
Maximum Power Dissipation (MOSFET) <sup>a</sup>	$P_D$	2.1	1.1	W	
		1.1	0.6		
Maximum Power Dissipation (Schottky) <sup>a</sup>		1.9	1.1		
		1.0	0.56		
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		°C	
Soldering Recommendations (Peak Temperature) <sup>b, c</sup>		260			

#### Notes

- Surface Mounted on 1" x 1" FR4 Board.
- See Reliability Manual for profile. The ChipFET is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

<b>THERMAL RESISTANCE RATINGS</b>							
Parameter			Device	Symbol	Typical	Maximum	Unit
Junction-to-Ambient <sup>a</sup>	t ≤ 5 sec	MOSFET	R <sub>thJA</sub>	50	60	°C/W	
		Schottky		54	65		
	Steady State	MOSFET		90	110		
		Schottky		95	115		
Junction-to-Foot	Steady State	MOSFET	R <sub>thJF</sub>	30	40	°C/W	
		Schottky		30	40		

## Notes

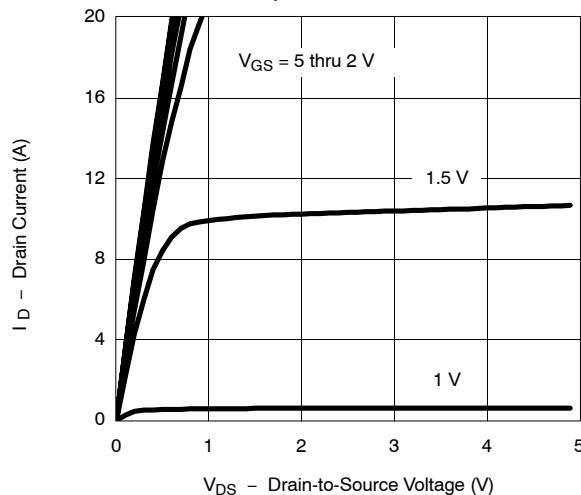
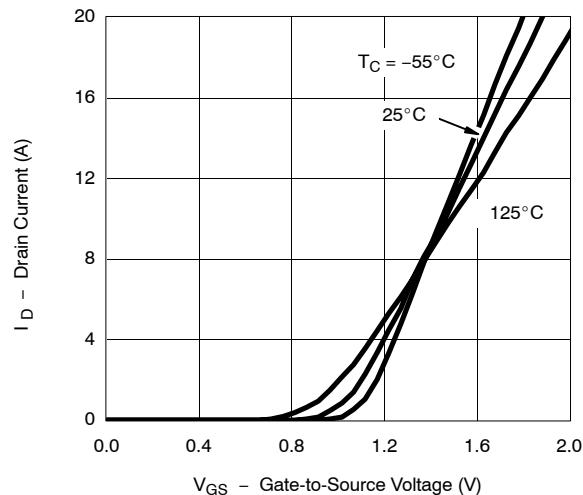
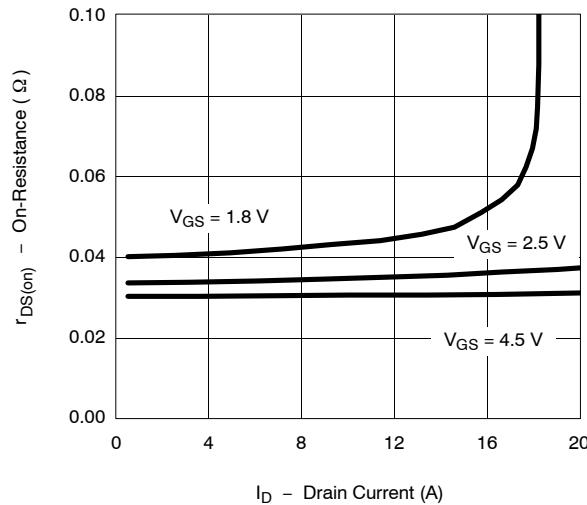
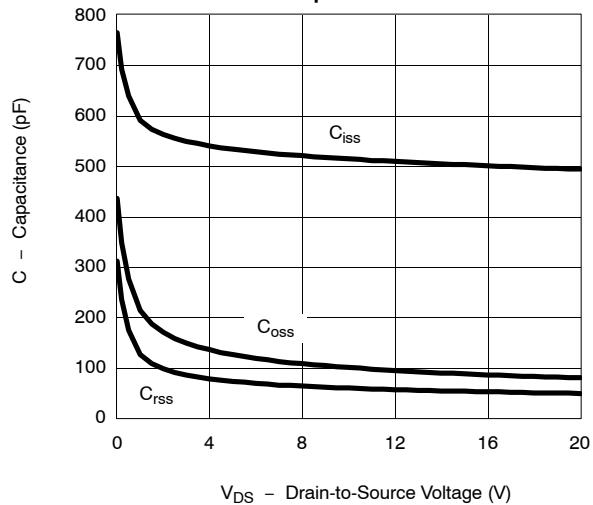
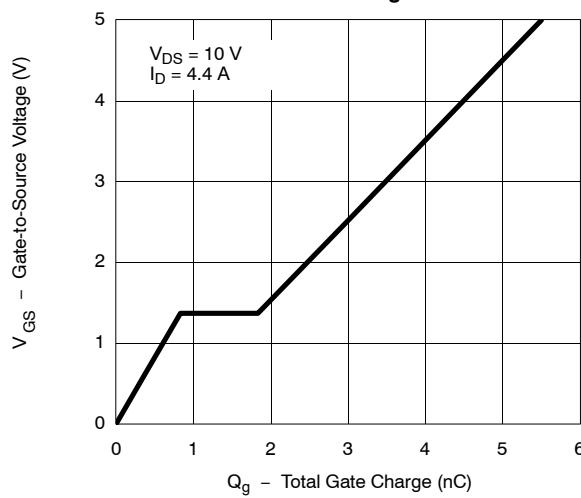
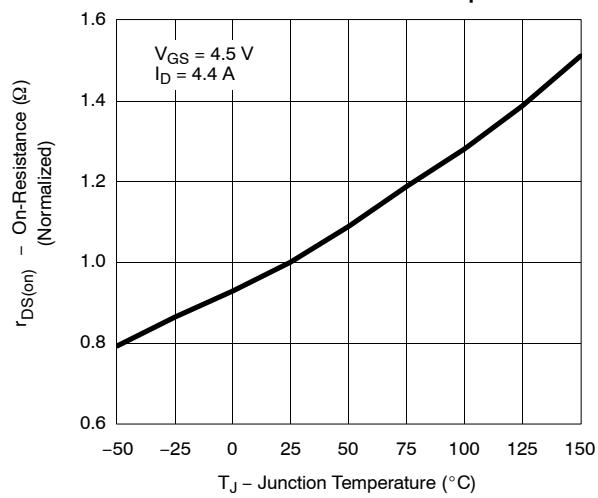
a. Surface Mounted on 1" x 1" FR4 Board.

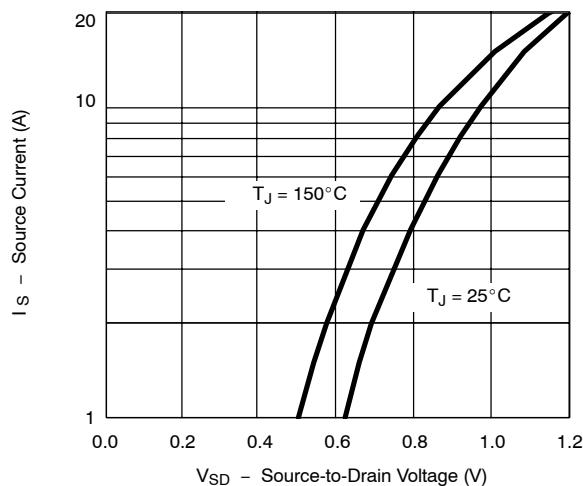
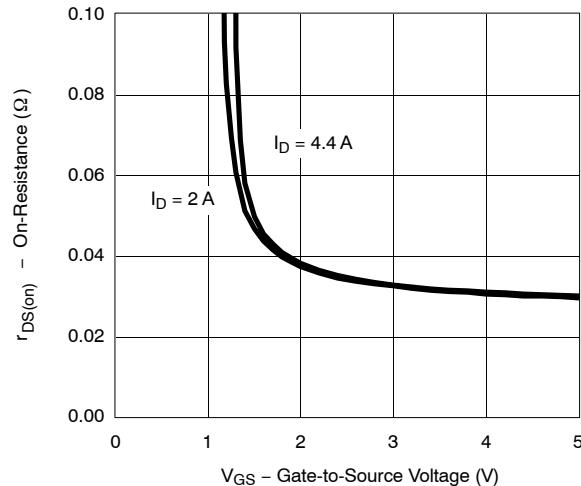
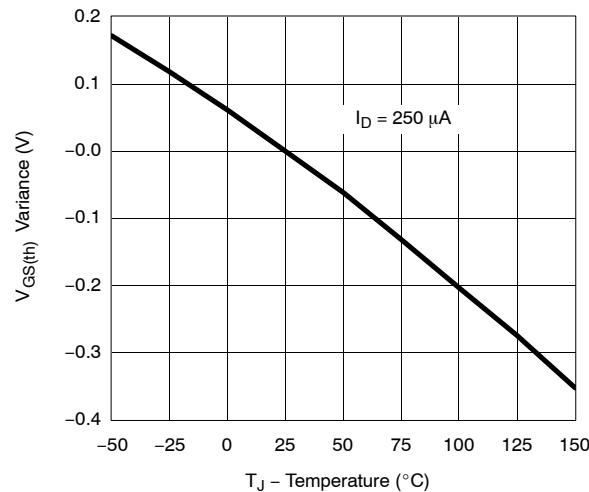
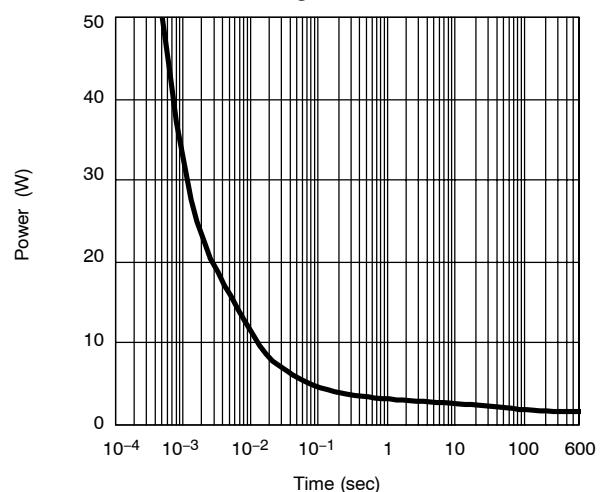
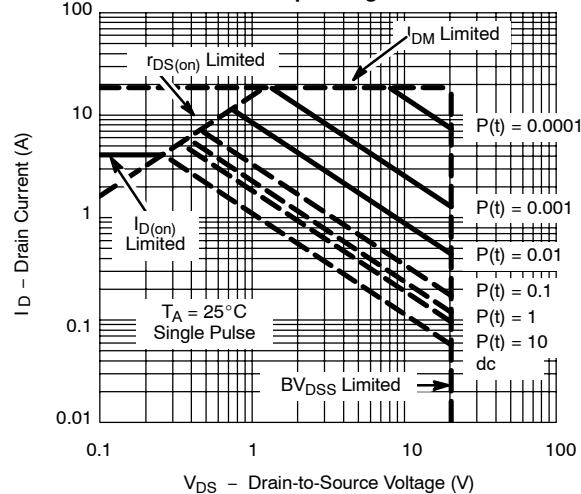
<b>MOSFET SPECIFICATIONS (T<sub>J</sub> = 25°C UNLESS OTHERWISE NOTED)</b>							
Parameter	Symbol	Test Condition		Min	Typ	Max	Unit
<b>Static</b>							
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA		0.4		1.0	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 8 V				± 100	nA
Zero Gate Voltage Drain Current	I <sub>DS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V				1	μA
		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85°C				5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 4.5 V		20			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 4.4 A			0.032	0.040	Ω
		V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 4.1 A			0.036	0.045	
		V <sub>GS</sub> = 1.8 V, I <sub>D</sub> = 1.9 A			0.042	0.052	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 4.4 A			22		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1.0 A, V <sub>GS</sub> = 0 V			0.8	1.2	V
<b>Dynamic<sup>b</sup></b>							
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 4.4 A			5	7.5	nC
Gate-Source Charge	Q <sub>gs</sub>				0.85		
Gate-Drain Charge	Q <sub>gd</sub>				1		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 10 V, R <sub>L</sub> = 10 Ω I <sub>D</sub> ≈ 1 A, V <sub>GEN</sub> = 4.5 V, R <sub>G</sub> = 6 Ω			20	30	ns
Rise Time	t <sub>r</sub>				36	55	
Turn-Off Delay Time	t <sub>d(off)</sub>				30	45	
Fall Time	t <sub>f</sub>				12	20	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>		I <sub>F</sub> = 0.9 A, di/dt = 100 A/μs		45	90	

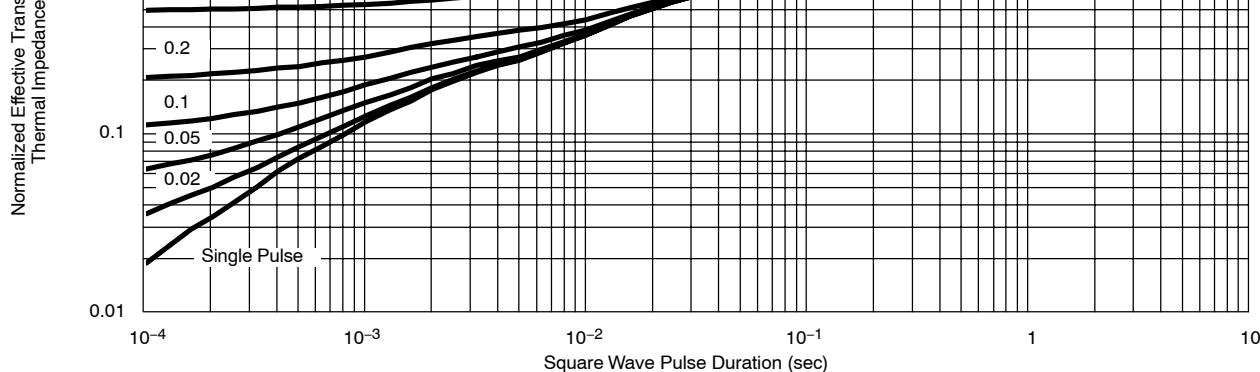
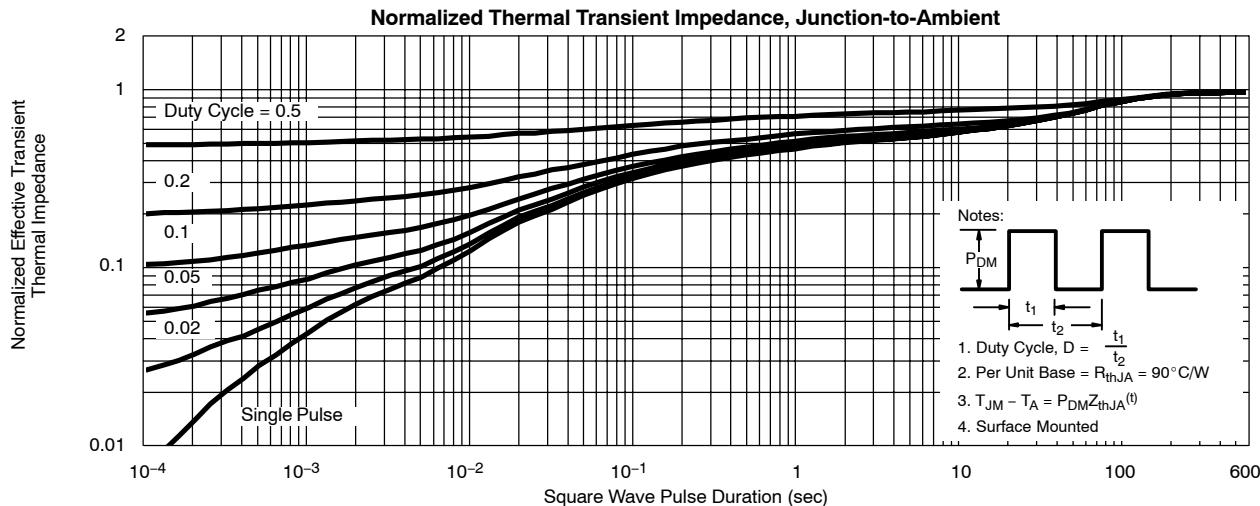
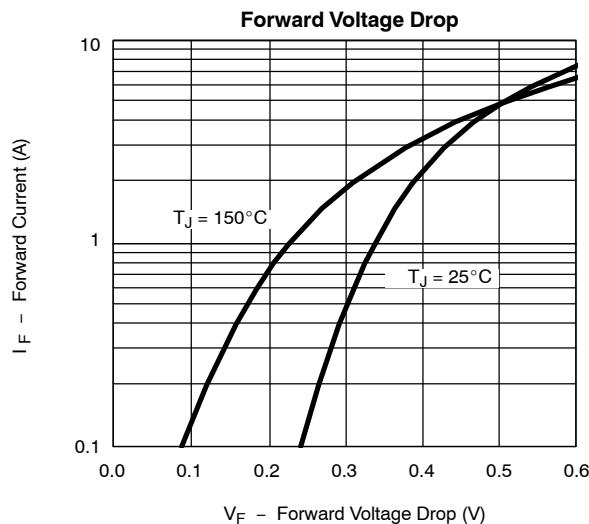
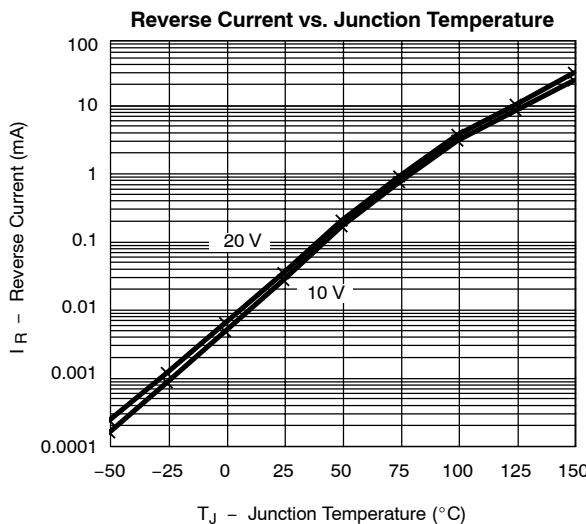
## Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.  
 b. Guaranteed by design, not subject to production testing.

<b>SCHOTTKY SPECIFICATIONS (T<sub>J</sub> = 25°C UNLESS OTHERWISE NOTED)</b>							
Parameter	Symbol	Test Condition		Min	Typ	Max	Unit
Forward Voltage Drop	V <sub>F</sub>	I <sub>F</sub> = 1.0			0.34	0.375	V
		I <sub>F</sub> = 1.0, T <sub>J</sub> = 125°C			0.255	0.290	
Maximum Reverse Leakage Current	I <sub>rm</sub>	V <sub>r</sub> = 20 V			0.05	0.500	mA
		V <sub>r</sub> = 20 V, T <sub>J</sub> = 85°C			2	20	
		V <sub>r</sub> = 20 V, T <sub>J</sub> = 125°C			10	100	
Junction Capacitance	C <sub>T</sub>	V <sub>r</sub> = 10 V			90		pF

**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**
**MOSFET**
**Output Characteristics**

**Transfer Characteristics**

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**


**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)****MOSFET****Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power****Safe Operating Area**

**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**
**MOSFET**

**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**
**SCHOTTKY**


**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)****SCHOTTKY**