Technical Data Datasheet 4308 REV. B

# Silicon Carbide Single Phase Full Wave Bridge

**DESCRIPTION:** 1200-VOLT, 5 AMP POWER SILICON CARBIDE SINGLE PHASE FULL WAVE BRIDGE IN A HERMETIC 5-LEAD TO-258 (MO-078) PACKAGE

### **FEATURES:**

- NO RECOVERY TIME OR REVERSE RECOVERY LOSSES
- NO TEMPERATURE INFLUENCE ON SWITCHING BEHAVIOR

### **MAXIMUM RATINGS**

ALL RATINGS ARE @  $T_C = 25$  °C UNLESS OTHERWISE SPECIFIED.

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RATING	SYMBOL	MAX.	UNITS		
PEAK INVERSE VOLTAGE	PIV	1200	Volts		
MAXIMUM DC OUTPUT CURRENT (With $T_{\rm C}$ = 65 $^{\rm O}$ C) WHEN USED AS A BRIDGE	Io	10	Amps		
MAXIMUM REPETITIVE FORWARD SURGE CURRENT (t = 8.3ms, Sine) per leg, $T_C$ = 25 $^{\circ}C$	I <sub>FRM</sub>	30	Amps		
MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT (t = $10\mu s$ , pulse) per leg, $T_C = 25$ °C	I <sub>FSM</sub>	100	Amps		
MAXIMUM JUNCTION CAPACITANCE (V <sub>r</sub> =5V) per leg		450	pF		
MAXIMUM POWER DISSIPATION, T <sub>C</sub> = 25 °C	P <sub>d</sub>	30	W		
MAXIMUM THERMAL RESISTANCE, Junction to Case (Connected as a BRIDGE)	$R_{ heta JC}$	1.0	°C/W		
MAXIMUM OPERATING AND STORAGE TEMPERATURE RANGE	Top, Tstg	-55 to +175	°C		

### **ELECTRICAL CHARACTERISTICS**

CHARACTERISTIC	TYP	MAX.	UNITS
MAXIMUM FORWARD VOLTAGE DROP ( $I_f = 5$ A PER LEG) $V_f$ $T_J = 25$ °C	1.65	1.80	
T <sub>J</sub> =150 °C	2.55	3.00	Volts
MAXIMUM REVERSE CURRENT (1200V PIV PER LEG) $I_r$ $T_J = 25$ °C	0.05	0.20	
T <sub>J</sub> = 150 °C	0.10	1.00	mA
TOTAL CAPACITANCE CHARGE (V <sub>R</sub> =1200V, I <sub>F</sub> =5A, di/dt=500A/ $\mu$ s and T <sub>J</sub> =25°C) Q <sub>C</sub> per leg	28	N/A	nC

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Note: The following curves are for individual legs of the bridge.

Figure 1. Forward Characteristics

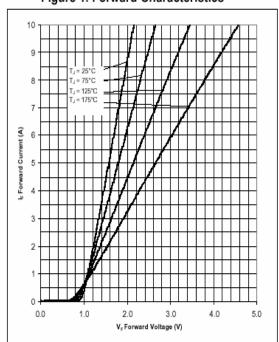
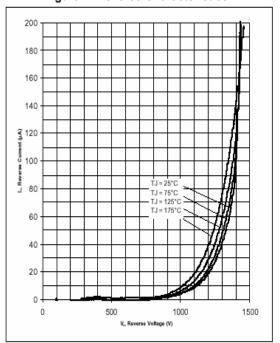
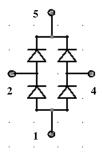


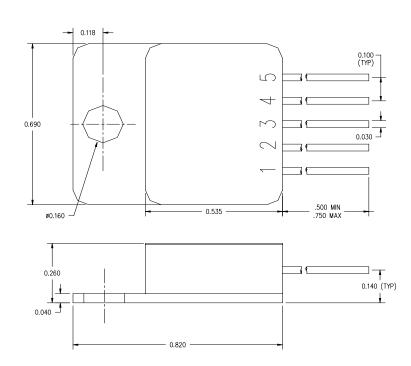
Figure 2. Reverse Characteristics



### **MECHANICAL DIMENSIONS (inches)**

### MO-078





#### **SENSITRON**

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### **PINOUT TABLE**

DEVICE TYPE	PIN 1	PIN 2	PIN 3	PIN 4	PIN 5
SINGLE PHASE FULL WAVE BRIDGE	DC(-)	AC(1)	NC	AC(2)	DC(+)

Application Note: Customers should be aware that at the current stage of technical development of SiC, the reverse avalanche capabilities of the device are limited.

Customer designs will need to accommodate these limitations and avoid exposure of the device to this and other potentially damaging conditions in their applications.

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