Technical Data
Datasheet 5008 REV. -

Silicon Carbide Single Phase Full Wave Bridge

DESCRIPTION: 600-VOLT, 10 AMP POWER SILICON CARBIDE SINGLE PHASE FULL WAVE BRIDGE IN A HERMETIC 5-LEAD TO-258 (MO-078) PACKAGE

FEATURES:

- NO RECOVERY TIME OR REVERSE RECOVERY LOSSES
- NO TEMPERATURE INFLUENCE ON SWITCHING BEHAVIOR

MAXIMUM RATINGS

ALL RATINGS ARE @ $T_C = 25$ °C UNLESS OTHERWISE SPECIFIED.

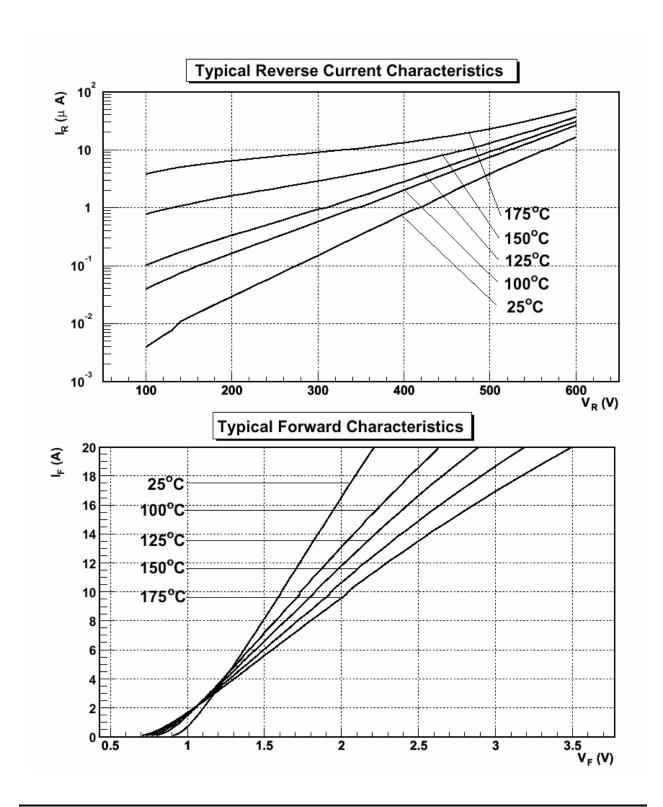
MAXIMOM IXATINOS	ALL RATINGS ARE & IC = 25 C UNLESS OTHERWISE SPECIFIED.			
RATING	SYMBOL	MAX.	UNITS	
PEAK INVERSE VOLTAGE	PIV	600	Volts	
MAXIMUM DC OUTPUT CURRENT (With $T_{\rm C}$ = 65 $^{\rm O}$ C) WHEN USED AS A BRIDGE	Io	20	Amps	
MAXIMUM REPETITIVE FORWARD SURGE CURRENT (t = 8.3ms, Sine) per leg, T_C = 25 $^{\circ}C$	I _{FRM}	50	Amps	
MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT (t = $10\mu s$, pulse) per leg, $T_C = 25$ °C	I _{FSM}	250	Amps	
MAXIMUM JUNCTION CAPACITANCE (V _r =5V) per leg	С	350	pF	
MAXIMUM POWER DISSIPATION, T _C = 25 °C	P _d	40	W	
MAXIMUM THERMAL RESISTANCE, Junction to Case (Connected as a BRIDGE)	$R_{ heta JC}$	1.0	°C/W	
MAXIMUM OPERATING AND STORAGE TEMPERATURE RANGE	Top, Tstg	-55 to +175	°C	

ELECTRICAL CHARACTERISTICS

CHARACTERISTIC		TYP	MAX.	UNITS
MAXIMUM FORWARD VOLTAGE (I _f = 10A PER LEG)	V _f T _J =25 °C	1.65	1.75	
	T _J =150 °C	1.85	1.95	Volts
MAXIMUM FORWARD VOLTAGE (I _f = 6A PER LEG)	V _f T _J =25 °C	1.35	1.45	
	T _J =150 °C	1.50	1.60	Volts
MAXIMUM REVERSE CURRENT (600V PIV PER LEG)	I_r $T_J = 25$ °C	0.04	0.15	
	T _J = 150 °C	0.08	0.50	mA
TOTAL CAPACITANCE CHARGE (V _R =600V, I _F =10A, di/dt=500A/ μ s and T _J =25°C) Q _C per leg		40	N/A	nC

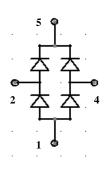
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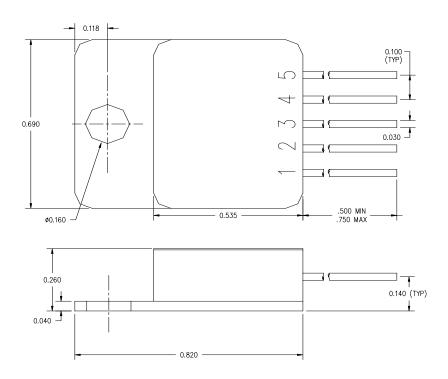
Note: The following curves are for individual legs of the bridge.



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MECHANICAL DIMENSIONS (inches) MO-078





PINOUT TABLE

DEVICE TYPE	PIN 1	PIN 2	PIN 3	PIN 4	PIN 5
SINGLE PHASE FULL WAVE BRIDGE	DC(-)	AC(1)	NC	AC(2)	DC(+)

Application Note: Customers should be aware that at the current stage of technical development of SiC, the reverse avalanche capabilities of the device are limited.

Customer designs will need to accommodate these limitations and avoid exposure of the device to this and other potentially damaging conditions in their applications.

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