SDAS203B - APRIL 1982 - REVISED DECEMBER 1994

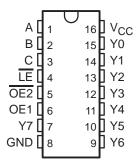
- Combines Decoder and 3-Bit Address Latch
- Incorporates Two Output Enables to Simplify Cascading
- Package Options Include Plastic Small-Outline (D) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

description

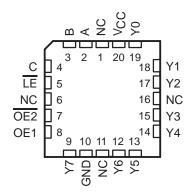
The SN54ALS137, SN74ALS137A, and 'AS137 are 3-line to 8-line decoders/demultiplexers with latches on the three address inputs. When the latch-enable (\overline{LE}) input is low, the SN54ALS137, SN74ALS137A, and 'AS137 act as a decoder/demultiplexer. When LE goes from low to high, the address present at the select (A, B, and C) inputs is stored in the latches. Further address changes are ignored as long as LE remains high. The output-enable (OE1 and $\overline{OE2}$) inputs control the outputs independently of the select or latch-enable inputs. All of the outputs are forced high if OE1 is low or OE2 is high. The SN54ALS137, SN74ALS137A, and 'AS137 are ideally suited for implementing glitch-free decoders in strobed (stored-address) applications in bus-oriented systems.

The SN54ALS137 and SN54AS137 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS137A and SN74AS137 are characterized for operation from 0°C to 70°C.

SN54ALS137, SN54AS137 . . . J PACKAGE SN74ALS137A, SN74AS137 . . . D OR N PACKAGE (TOP VIEW)

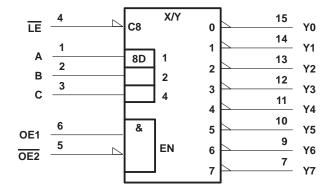


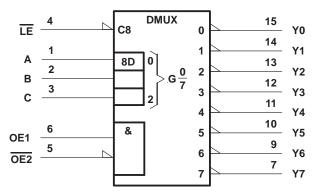
SN54ALS137, SN54AS137 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

logic symbols (alternatives)†



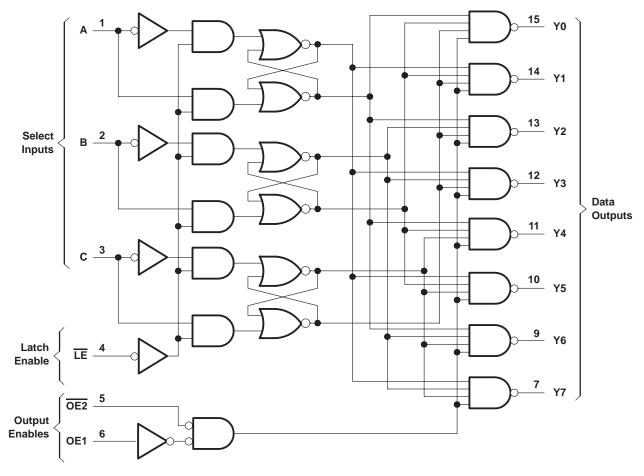


[†] These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for the D, J, and N packages.



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logic diagram (positive logic)



Pin numbers shown are for the D, J, and N packages.

FUNCTION TABLE

INPUTS									OUT	DUTE			
	ENABLE			SELECT	-	OUTPUTS							
LE	OE1	OE2	С	В	Α	Y0	Y1	Y2	Y3	Y4	Y5	Y6	Y7
Х	Х	Н	Х	Χ	Х	Н	Н	Н	Н	Н	Н	Н	Н
Х	L	Χ	Χ	Χ	Χ	Н	Н	Н	Н	Н	Н	Н	Н
L	Н	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н
L	Н	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н
L	Н	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н
L	Н	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
L	Н	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н
L	Н	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н
L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н
L	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L
Н	Н	L	Х	Χ	Χ	Ou	tputs co	rrespond	ling to sto	ored add	ress, L;	all others	, H



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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC}	7 V
Input voltage, V _I	7 V
Operating free-air temperature range, T _A : SN54ALS137	
SN74ALS137A	0°C to 70°C
Storage temperature range	-65°C to 150°C

recommended operating conditions

		SN54ALS137		SN74ALS137A			UNIT	
			NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
V _{IL}	Low-level input voltage			0.7			0.8	V
IOH	High-level output current			-0.4			-0.4	mA
lOL	Low-level output current			4			8	mA
t _W	Pulse duration, LE low	15			10			ns
t _{su}	Setup time at A, B, and C before LE↑	15			10			ns
th	Hold time at A, B, and C after LE↑	5			5			ns
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS			SN54ALS137			SN74ALS137A			
PARAMETER	TEST C	TEST CONDITIONS			MAX	MIN	TYP‡	MAX	UNIT	
VIK	$V_{CC} = 4.5 V$,	$I_{I} = -18 \text{ mA}$			-1.5			-1.5	V	
Vон	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2	2		V _{CC} -2	2		V	
\/a:	V22 - 45 V	$I_{OL} = 4 \text{ mA}$		0.25	0.4		0.25	0.4	V	
VOL	V _{CC} = 4.5 V	$I_{OL} = 8 \text{ mA}$					0.35	0.5	V	
Ι _Ι	$V_{CC} = 5.5 V$,	$V_I = 7 V$			0.1			0.1	mA	
lіН	$V_{CC} = 5.5 V,$	V _I = 2.7 V			20			20	μΑ	
Ι _{ΙL}	$V_{CC} = 5.5 V,$	V _I = 0.4 V			-0.1			-0.1	mA	
ΙΟ§	$V_{CC} = 5.5 V,$	V _O = 2.25 V	-30		-112	-30		-112	mA	
lcc	V _{CC} = 5.5 V			5	11		5	11	mA	

[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.



[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

[§] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

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switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C _L R _L	= 50 pl = 500 g		,	UNIT
		, , , ,	SN54ALS137		SN74ALS137		
			MIN	MAX	MIN	MAX	
t _{PLH}	A, B, C	Y	5	25	5	20	ns
^t PHL			6	25	6	20	115
^t PLH	OE2	Y	4	15	3	12	ns
^t PHL	OE2		5	18	4	15	115
^t PLH	OE1	· ·	5	21	4	17	ns
^t PHL	OLI	1	5	19	4	15	115
^t PLH	<u>LE</u>	Y	7	27	6	22	sn
^t PHL	LE LE	1	7	25	7	20	311

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, V _{CC}	7 V
Input voltage, V _I	
Operating free-air temperature range, TA: SN54AS137	′ −55°C to 125°C
SN74AS137	7 0°C to 70°C
Storage temperature range	–65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN54AS137		SN74AS137			UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V_{IH}	High-level input voltage	2			2			V
V _{IL}	Low-level input voltage			0.8			0.8	V
ІОН	High-level output current			-2			-2	mA
loL	Low-level output current			20			20	mA
t _W	Pulse duration, LE low	5			6.5			ns
t _{su}	Setup time at A, B, and C before LE↑	4.5			4			ns
t _h	Hold time at A, B, and C after LE↑	1			1			ns
TA	Operating free-air temperature	-55		125	0		70	°C

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SN54AS137			SN	UNIT		
PARAMETER			MIN	TYP [†]	MAX	MIN	TYP [†]	MAX	UNIT
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.2			-1.2	V
Voн	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -2 \text{ mA}$	V _{CC} -2			V _{CC} -2			V
V _{OL}	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 20 \text{ mA}$		0.35	0.5		0.35	0.5	V
IĮ	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V			0.1			0.1	mA
lіН	$V_{CC} = 5.5 \text{ V},$	V _I = 2.7 V			20			20	μΑ
I _{IL}	$V_{CC} = 5.5 \text{ V},$	V _I = 0.4 V			-0.5			-1	mA
10 [‡]	V _{CC} = 5.5 V,	V _O = 2.25 V	-30		-112	-30		- 112	mA
Icc	V _{CC} = 5.5 V			15	24		15	24	mA

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)			V_{CC} = 4.5 V to 5.5 V, C_L = 50 pF, R_L = 500 Ω , T_A = MIN to MAX§					
	, ,	, ,	SN54AS137		SN74AS137				
			MIN	MAX	MIN	MAX			
t _{PLH}	A, B, C	Y	2	14	2	12.5	ns		
t _{PHL}		1	2	14	2	12.5	115		
tPLH	OE2	Y	2	9	2	8	ns		
^t PHL		1	2	9	2	8.5	115		
tPLH	OE1	Y	2	11	2	10	ns		
^t PHL	OLI	1	2	10	2	9	115		
^t PLH	<u>LE</u>	Y	2	14.5	3	13.5	ns		
t _{PHL}	1 6 1	1	2	15	3	14	119		

[§] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[†] All typical values are at V_{CC} = 5 V, T_A = 25°C. ‡ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS}.

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