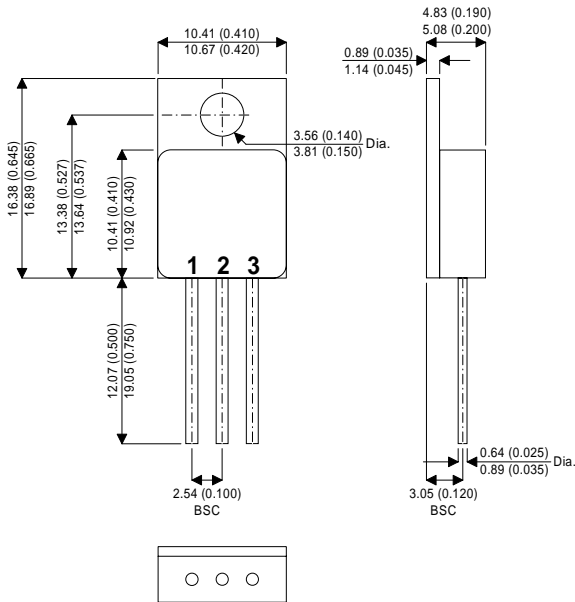


MECHANICAL DATA

Dimensions in mm (inches)



TO-257AA – Metal Package

Pad 1 – Gate Pad 2 – Drain Pad 3 – Source

**N-CHANNEL
POWER MOSFET
FOR HI-REL
APPLICATIONS**

V_{DSS} 200V
 $I_{D(cont)}$ 9A
 $R_{DS(on)}$ 0.40 Ω

FEATURES

- HERMETICALLY SEALED TO-257AA METAL PACKAGE
- SIMPLE DRIVE REQUIREMENTS
- LIGHTWEIGHT
- SCREENING OPTIONS AVAILABLE
- ALL LEADS ISOLATED FROM CASE

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

V_{GS}	Gate – Source Voltage	$\pm 20V$
I_D	Continuous Drain Current @ $T_{case} = 25^{\circ}C$	9A
I_D	Continuous Drain Current @ $T_{case} = 100^{\circ}C$	6A
I_{DM}	Pulsed Drain Current	36A
P_D	Power Dissipation @ $T_{case} = 25^{\circ}C$	75W
	Linear Derating Factor	0.6W/ $^{\circ}C$
T_J, T_{stg}	Operating and Storage Temperature Range	-55 to 150 $^{\circ}C$
$R_{\theta JC}$	Thermal Resistance Junction to Case	1.67 $^{\circ}C/W$ max..

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ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise stated)

Parameter	Test Conditions	Min.	Typ.	Max.	Unit	
STATIC ELECTRICAL RATINGS						
BV_{DSS}	Drain – Source Breakdown Voltage	$V_{GS} = 0$	$I_D = 1\text{mA}$	200	V	
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Temperature Coefficient of Breakdown Voltage	Reference to 25°C $I_D = 1\text{mA}$		0.29	$\text{V}/^\circ\text{C}$	
$R_{DS(on)}$	Static Drain – Source On–State Resistance	$V_{GS} = 10\text{V}$	$I_D = 6\text{A}$		0.40	
		$V_{GS} = 10\text{V}$	$I_D = 9\text{A}$		0.49	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$	$I_D = 250\mu\text{A}$	2	4	V
g_{fs}	Forward Transconductance	$V_{DS} \geq 15\text{V}$	$I_{DS} = 6\text{A}$	3		$\text{S}(\bar{\omega})$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0$	$V_{DS} = 0.8BV_{DSS}$		25	μA
			$T_J = 125^\circ\text{C}$			250
I_{GSS}	Forward Gate – Source Leakage	$V_{GS} = 20\text{V}$			100	nA
I_{GSS}	Reverse Gate – Source Leakage	$V_{GS} = -20\text{V}$			-100	nA
DYNAMIC CHARACTERISTICS						
C_{iss}	Input Capacitance	$V_{GS} = 0$			600	pF
C_{oss}	Output Capacitance	$V_{DS} = 25\text{V}$			250	
C_{rss}	Reverse Transfer Capacitance	$f = 1\text{MHz}$			80	
Q_g	Total Gate Charge	$V_{GS} = 10\text{V}$	$I_D = 9\text{A}$	16	39	nC
Q_{gs}	Gate – Source Charge	$I_D = 9\text{A}$		3	5.1	nC
Q_{gd}	Gate – Drain (“Miller”) Charge	$V_{DS} = 0.5BV_{DSS}$		8	20	
$t_{d(on)}$	Turn–On Delay Time	$V_{DD} = 100\text{V}$ $I_D = 9\text{A}$ $R_G = 7.5\Omega$			35	ns
t_r	Rise Time				80	
$t_{d(off)}$	Turn–Off Delay Time				60	
t_f	Fall Time				40	
SOURCE – DRAIN DIODE CHARACTERISTICS						
I_S	Continuous Source Current				9	A
I_{SM}	Pulse Source Current				36	
V_{SD}	Diode Forward Voltage	$I_S = 9\text{A}$	$T_J = 25^\circ\text{C}$		1.4	V
t_{rr}	Reverse Recovery Time	$I_S = 9\text{A}$	$T_J = 25^\circ\text{C}$		500	ns
Q_{rr}	Reverse Recovery Charge	$d_i / d_t \leq 100\text{A}/\mu\text{s}$		$V_{DD} \leq 50\text{V}$	6	μC
PACKAGE CHARACTERISTICS						
L_D	Internal Drain Inductance	(from 6mm down drain lead pad to centre of die)			8.7	nH
L_S	Internal Source Inductance	(from 6mm down source lead to centre of source bond pad)			8.7	

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