# **Silicon Controlled Rectifier**

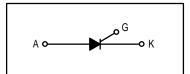
## **Reverse Blocking Triode Thyristors**

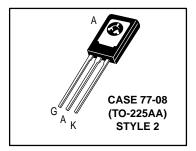
... Glassivated PNPN devices designed for high volume consumer applications such as temperature, light, and speed control; process and remote control, and warning systems where reliability of operation is important.

- · Glassivated Surface for Reliability and Uniformity
- · Power Rated at Economical Prices
- Practical Level Triggering and Holding Characteristics
- Flat, Rugged, Thermopad Construction for Low Thermal Resistance, High Heat Dissipation and Durability

# C106 Series

SCRs 4 AMPERES RMS 200 thru 600 VOLTS





## **MAXIMUM RATINGS** ( $T_J = 25^{\circ}C$ unless otherwise noted.)

Rating		Symbol	Value	Unit	
Peak Repetitive Forward and Reverse Blocki (RGK = 1 k $\Omega$ ) (T <sub>C</sub> = -40° to 110°C)	ng Voltage <sup>(1)</sup> C106B C106D, C106D1 C106M, C106M1	VDRM or VRRM	200 400 600	Volts	
RMS Forward Current (All Conduction Angles)		I <sub>T(RMS)</sub>	4	Amps	
Average Forward Current (T <sub>A</sub> = 30°C)		I <sub>T(AV)</sub>	2.55	Amps	
Peak Non-repetitive Surge Current (1/2 Cycle, 60 Hz, T <sub>J</sub> = -40 to +110°C)		ITSM	20	Amps	
Circuit Fusing (t = 8.3 ms)		l <sup>2</sup> t	1.65	A <sup>2</sup> s	
Peak Gate Power		P <sub>GM</sub>	0.5	Watt	
Average Gate Power		P <sub>G(AV)</sub>	0.1	Watt	
Peak Forward Gate Current		IGFМ	0.2	Amp	

<sup>1.</sup> VDRM and VRRM for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

REV 1



## C106 Series

## **MAXIMUM RATINGS** — continued

Rating	Symbol	Value	Unit
Peak Reverse Gate Voltage	<sup>V</sup> GRM	6	Volts
Operating Junction Temperature Range	TJ	-40 to +110	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to +150	°C
Mounting Torque(1)	_	6	in. lb.

<sup>1.</sup> Torque rating applies with use of compression washer (B52200F006). Mounting torque in excess of 6 in. lb. does not appreciably lower case-to-sink thermal resistance. Anode lead and heatsink contact pad are common.

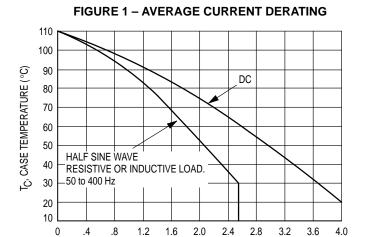
## **THERMAL CHARACTERISTICS** (T<sub>C</sub> = 25°C, R<sub>GK</sub> = 1 k $\Omega$ unless otherwise noted.)

Characteristic	Symbol Max		Unit	
Thermal Resistance, Junction to Case	$R_{ heta JC}$	3	°C/W	
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	75	°C/W	

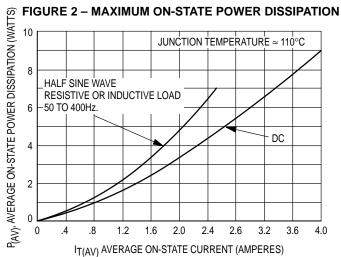
## **ELECTRICAL CHARACTERISTICS** ( $T_C = 25$ °C unless otherwise noted.)

Characteristic		Symbol	Min	Тур	Max	Unit
Peak Forward or Reverse Blocking Current (V <sub>AK</sub> = Rated V <sub>DRM</sub> or V <sub>RRM</sub> , R <sub>GK</sub> = 1000 Ohms)	T <sub>J</sub> = 25°C T <sub>J</sub> = 110°C	IDRM, IRRM	_		10 100	μΑ μΑ
Forward "On" Voltage (I <sub>FM</sub> = 1 A Peak for C106B, D, & M) (I <sub>FM</sub> = 4 A Peak for C106D1, & M1)		Vтм	_		2.2	Volts
Gate Trigger Current (Continuous dc) (VAK = 6 Vdc, R <sub>L</sub> = 100 Ohms) (VAK = 6 Vdc, R <sub>L</sub> = 100 Ohms, T <sub>C</sub> = -40°C)		<sup>I</sup> GT	_	30 75	200 500	μΑ
Gate Trigger Voltage (Continuous dc) $ \begin{array}{l} (V_{AK}=6 \text{ Vdc},  R_L=100 \text{ Ohms},  R_{GK}=1000 \text{ Ohms}) \\ (V_{AK}=Rated  V_{DRM},  R_L=3000 \text{ Ohms}, \\ R_{GK}=1000 \text{ Ohms},  T_J=110^{\circ}\text{C}) \end{array} $	$T_J = 25^{\circ}C$ $T_J = -40^{\circ}C$	Vgт	0.4 0.5 0.2		0.8 1 —	Volts
Holding Current (V <sub>D</sub> = 12 Vdc, R <sub>GK</sub> = 1000 Ohms)	T <sub>J</sub> = 25°C T <sub>J</sub> = -40°C T <sub>J</sub> = +110°C	Ιн	0.3 0.4 0.14	 	3 6 2	mA
Forward Voltage Application Rate (T <sub>J</sub> = 110°C, R <sub>GK</sub> = 1000 Ohms, V <sub>D</sub> = Rated V <sub>DRM</sub>	)	dv/dt	_	8	_	V/µs
Turn-On Time		tgt	_	1.2	_	μs
Turn-Off Time		tq	_	40	_	μs

For soldering purposes (either terminal connection or device mounting), soldering temperatures shall not exceed +200°C. For optimum results, an activated flux (oxide removing) is recommended.

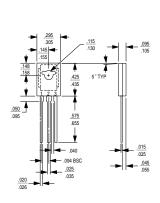


I<sub>T(AV)</sub> AVERAGE ON-STATE CURRENT (AMPERES)

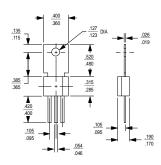


## Package Interchangeability

The dimensional diagrams below compare the critical dimensions of the Motorola C-106 package with competitive devices. It has been demonstrated that the smaller dimensions of the Motorola package make it compatible in most lead-mount and chassis-mount applications. The user is advised to compare all critical dimensions for mounting compatibility.



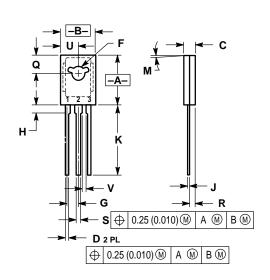
Motorola C-106 Package



Competitive C-106 Package

#### PACKAGE DIMENSIONS

STYLE 2: PIN 1. CATHODE 2. ANODE 3. GATE



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
   VALUE AND TOLERANCING PER ANSI
   VALU
- Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.

	INCHES MILLIMETERS			IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.425	0.435	10.80	11.04	
В	0.295	0.305	7.50	7.74	
С	0.095	0.105	2.42	2.66	
D	0.020	0.026	0.51	0.66	
F	0.115	0.130	2.93	3.30	
G	0.094 BSC 2.39		2.39	BSC	
Н	0.050	0.095	1.27	2.41	
J	0.015	0.025	0.39	0.63	
K	0.575	0.655	14.61	16.63	
M	5° TYP 5°		TYP		
Q	0.148	0.158	3.76	4.01	
R	0.045	0.055	1.15	1.39	
S	0.025	0.035	0.64	0.88	
U	0.145	0.155	3.69	3.93	
٧	0.040		1.02		

CASE 77-08 (TO-225AA)

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C106/D