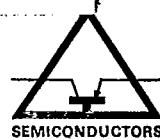


T-43-01



INTEGRATED CIRCUITS

(i) Phase locked loops

LN - 06

Type	Upper Frequency (MHz)	Max. Lock Range (% fo)	FM Distortion (%)	Output Swing +5% Deviation (Vpp)	Centre Frequency Stability (ppm/°C)	Frequency Drift with Voltage (%) V	Input Resistance (Ohms)	AM output available	Supply Current (mA)	Supply Voltage (V)	Temp Range (°C)	Case:
NE565	0.5	120	0.2	0.15	±200	0.16	5K	No	8	+5/-12	0/70	TO-96
NE566	0.5	-	0.2	30%/V***	±200	0.16	-	-	7	10/+26	0/70	TO-99
NE567	0.5	14	5*	0.20	35±60	0.7	20K**	Yes*	7	+4.5/+9	0/70	TO-99

* The 567 AM and FM outputs are available but are not optimised for Linear demodulation.

** Input biased internally.

*** Figures shown in VCO gain in % deviation per volt.

LN 05

(j) High accuracy FET input operational amplifiers

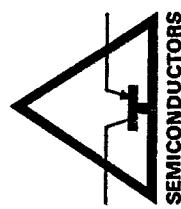
TYPE	Open loop gain @ $V_{out} = \pm 10V$ $R_L \geq 2K\Omega$ min	Output Characteristics Voltage @ $R_L = 2K\Omega$ n $T_a = \text{min to max}$ typ (V)	Short ckt. current (mA)	Input offset Voltage max. (mV)	Input Bias current max (pA)	Input Voltage range Differential (V)	Power Supply Operating (V)	Temp. range Operating (°C)	Case
AD 540J	20,000	± 13	25	50	50	± 20	± 5 to 18	+ 25°C to 70°C	TO-99
AD 540K	50,000	± 13	25	20	25	± 20	± 5 to 18	+ 25°C to 70°C	TO-99
AD 540S	50,000	± 13	25	20	25	± 20	± 5 to 18	+ 25°C to 125°C	TO-99

LN - 09

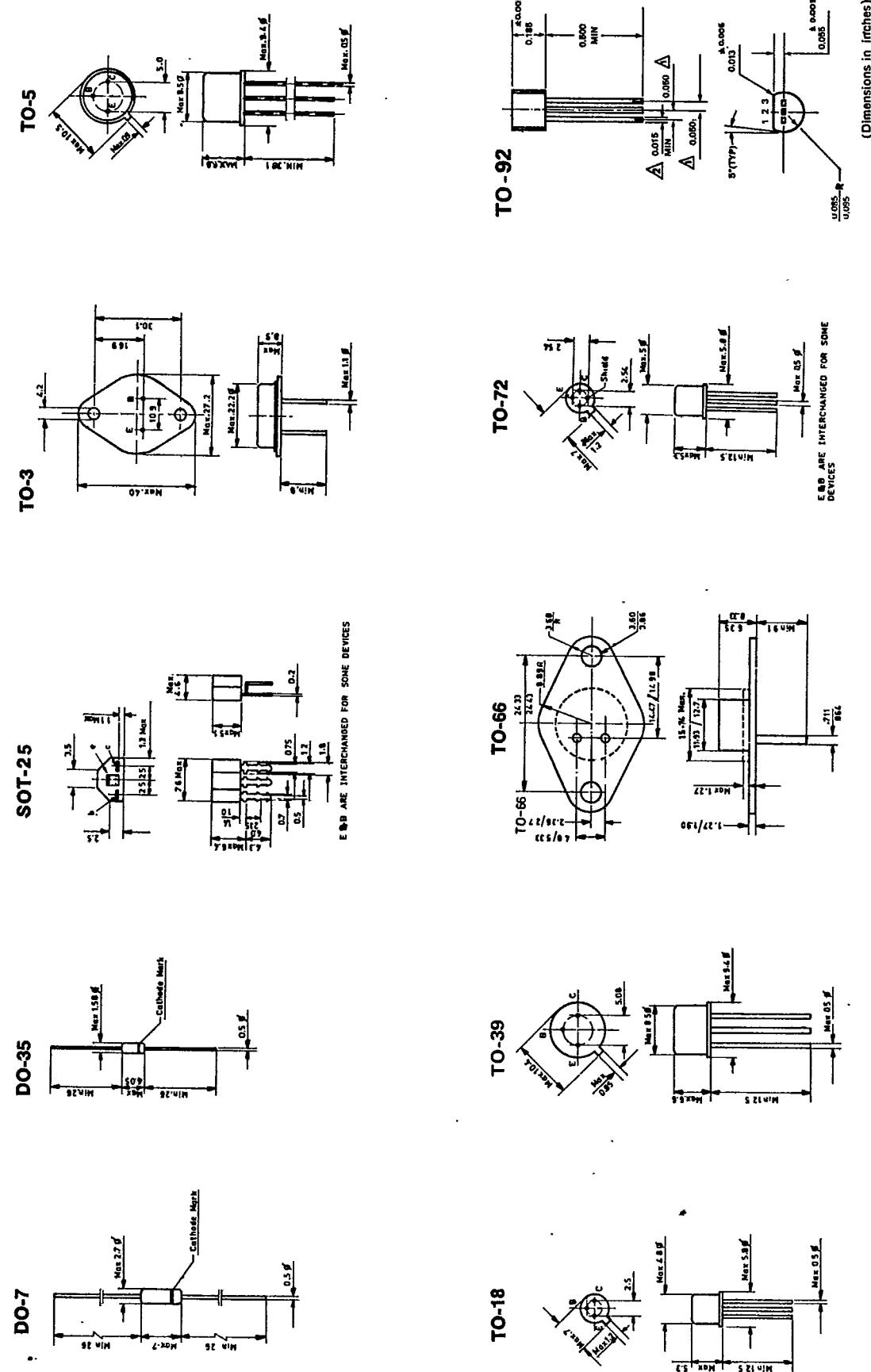
(k) Two terminal temperature transducer IC

TYPE	Operating Voltage Range V	Nominal Current Output @ 25°C μA	Nominal Temperature Coefficient $\mu A/^\circ C$	Calibration Error @ 25°C max	Non-linearity -55°C to + 150°C $^\circ C$ max	TON μs	Reverse Bias Leakage Current pA	Case
AD590H	+4 to +30	298.2	1.0	±5.0	±2.0	20	10 @ 10V	TO-18

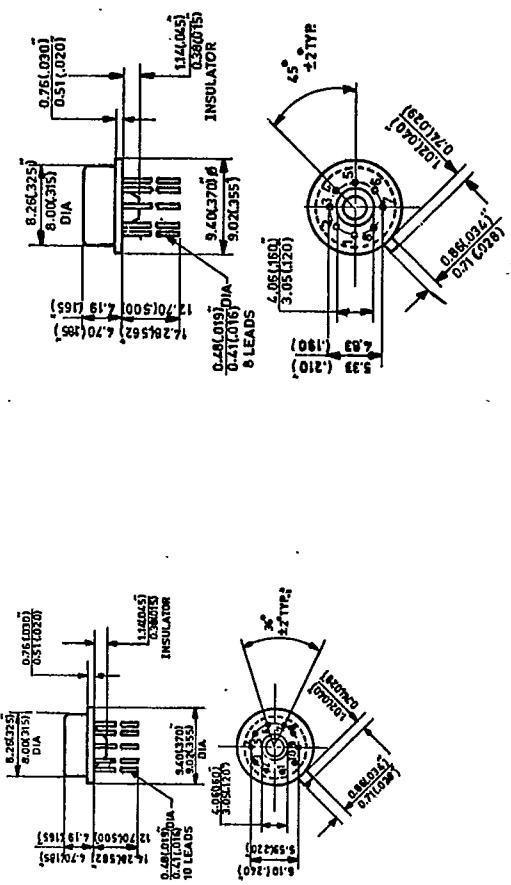
T-90-20



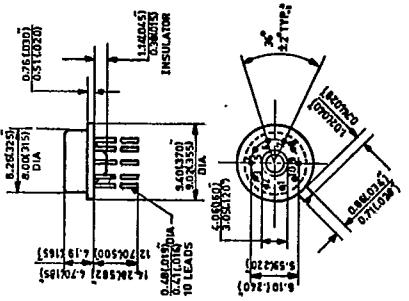
CASE OUTLINES



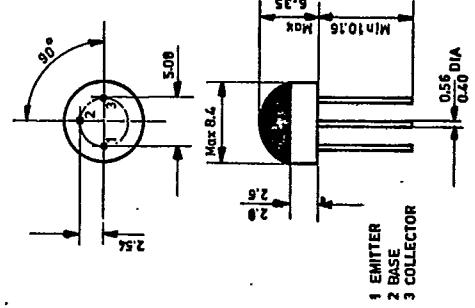
TO-99



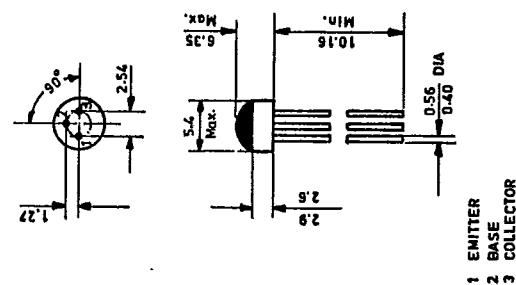
TO-96



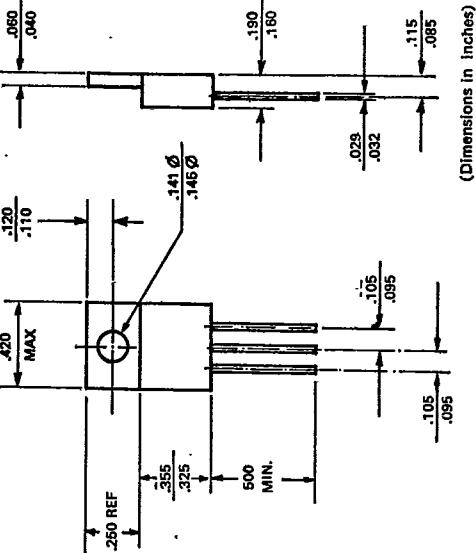
TO-105



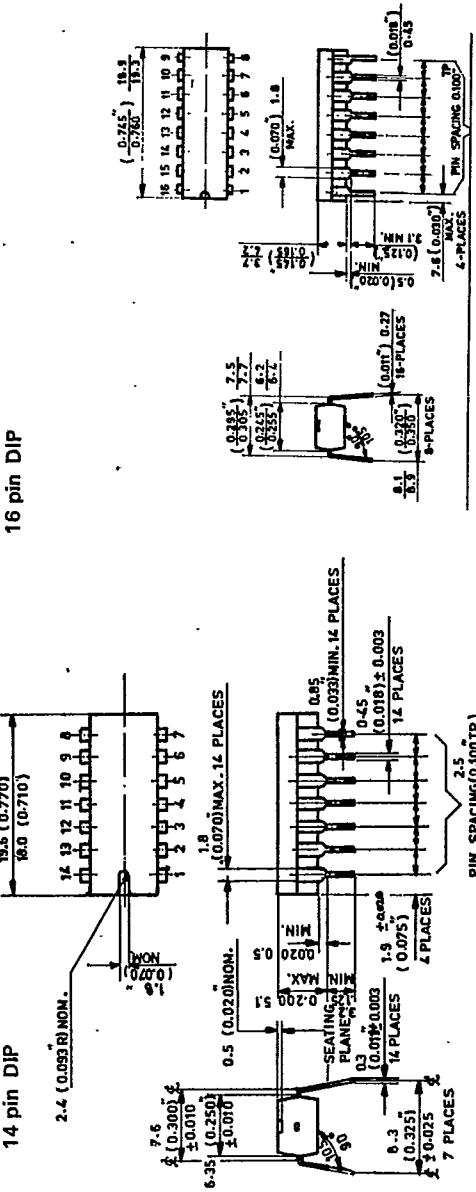
TO-106



TO-220



16 pin DIP



(Dimensions in Inches)

Note : Dimensions in mm unless otherwise specified