

ML53001

10/100BASE-TX Fast EtherNet Transceiver LSI

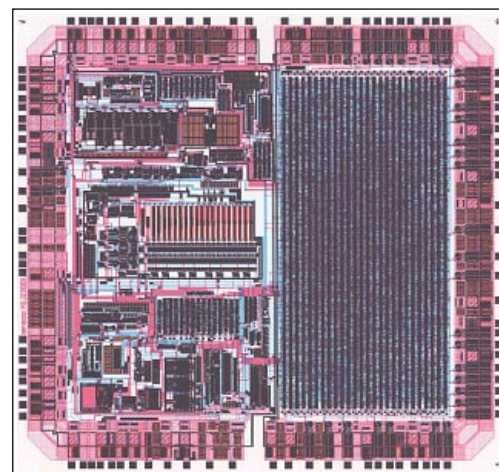
GENERAL DESCRIPTION

ML53001 is a single chip transceiver LSI which facilitates both 100BASE-TX and 10BASE-T EtherNet protocols. The physical layer function can be realised with only a few external components, such as a magnetics. The host interface with commercial MAC (Media Access Control) device can be also realised simply through the MII (Media Independent Interface) bus.

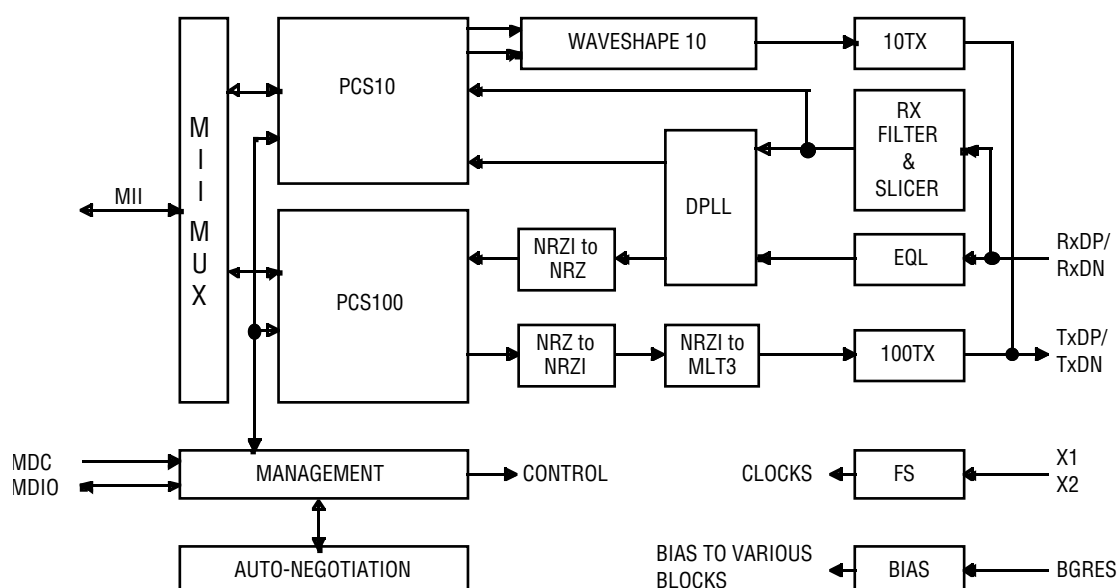
FEATURES

- 100BASE-TX Transceiver fully compliant with IEEE802.3u Spec.
- 10BASE-T Transceiver fully compliant with IEEE802.3u Spec.
- Support MII (Media Independent Interface)
- Automatic Negotiation
- Integrated LED Driver for the status indication
- Supports the function as repeater
- Supports power down mode
- Single +5V ($\pm 5\%$) power supply voltage
- Operating temperature 0 to +70°C
- 100-pin TQFP (TQFP100-P-1414-0.50-K)
- Product name ML53001TB

CHIP MICROGRAPH



BLOCK SCHEMATIC

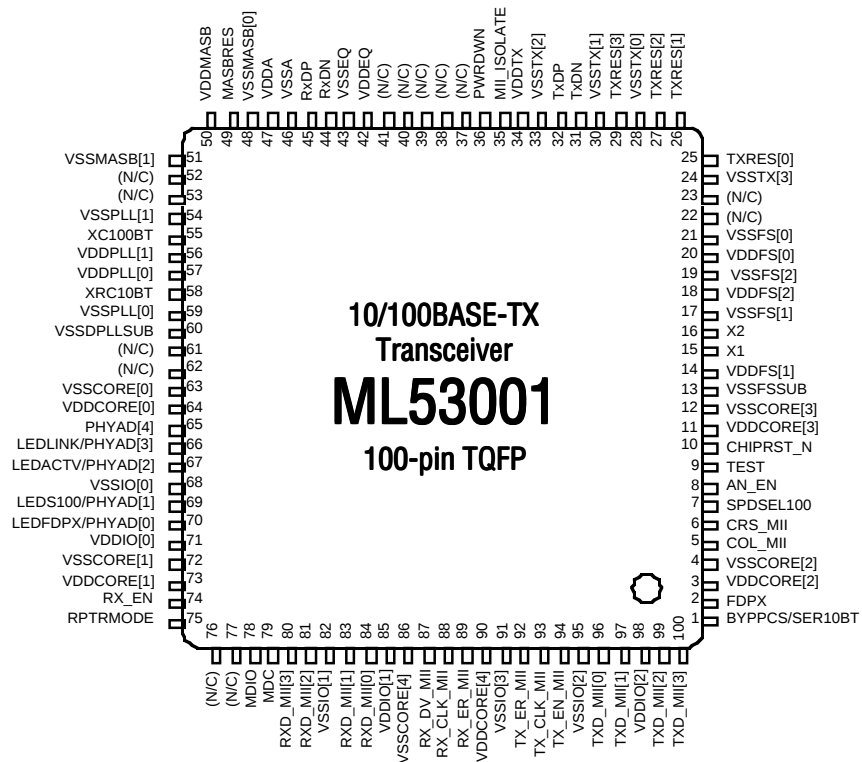


TYPICAL APPLICATIONS

- NIC (Network Interface Card)
- Hub/Switch/Repeater/Router

PIN ASSIGNMENT

100-pin TQFP
(top view)



PACKAGE DIMENSIONS

100-pin TQFP

Unit in millimeters, unless otherwise specified.

