
HVC134

Silicon Epitaxial Planar Pin Diode for High Frequency Switching

HITACHI

ADE-208-622(Z)

Rev 0

May 1998

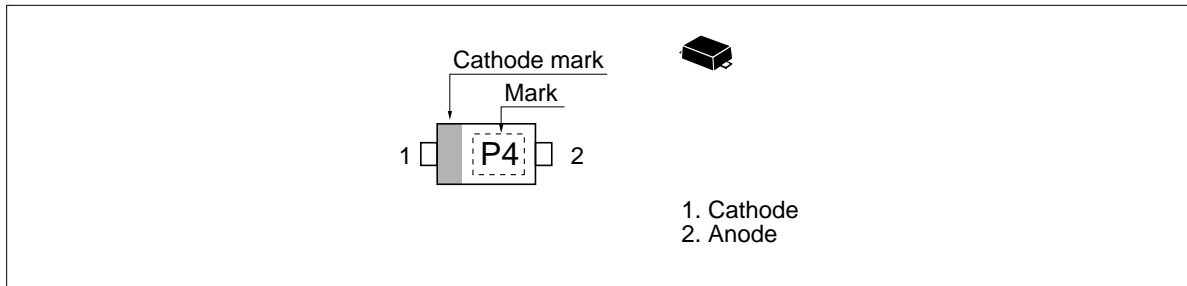
Features

- Low capacitance. ($C=0.4\text{pF max}$)
- Low forward resistance. ($r_f=2.0\Omega \text{ max}$)
- Ultra small Flat Package (UFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVC134	P4	UFP

Outline



HVC134

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}	65	V
Reverse voltage	V_R	60	V
Forward current	I_F	100	mA
Power dissipation	P_d	150	mW
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_R	—	—	0.1	μA	$V_R = 60V$
Forward voltage	V_F	—	—	1.0	V	$I_F = 10\text{ mA}$
Capacitance	C	—	—	0.4	pF	$V_R = 1V, f = 1\text{ MHz}$
Forward resistance	r_f	—	—	2.0		$I_F = 10\text{ mA}, f = 100\text{ MHz}$
ESD-Capability ^{*1}	—	200	—	—	V	$C = 100\text{pF}, R = 1.5K\ \Omega$, Both forward and reverse direction 5 pulse

Notes 1. Failure criterion ; $I_R > 0.1\ \mu A$ at $V_R = 60V$.

Main Characteristic

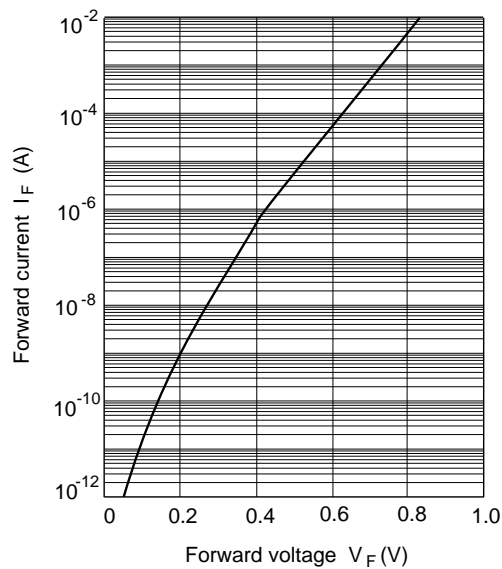


Fig.1 Forward current Vs. Forward voltage

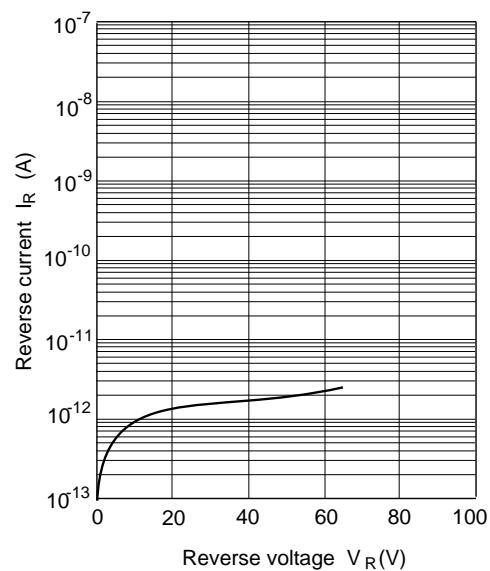


Fig.2 Reverse current Vs. Reverse voltage

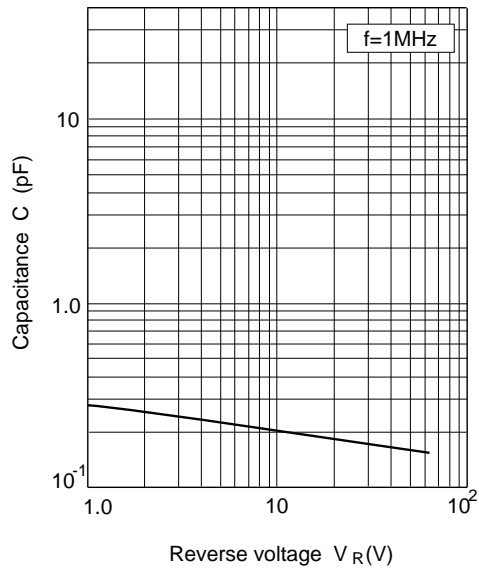


Fig.3 Capacitance Vs. Reverse voltage

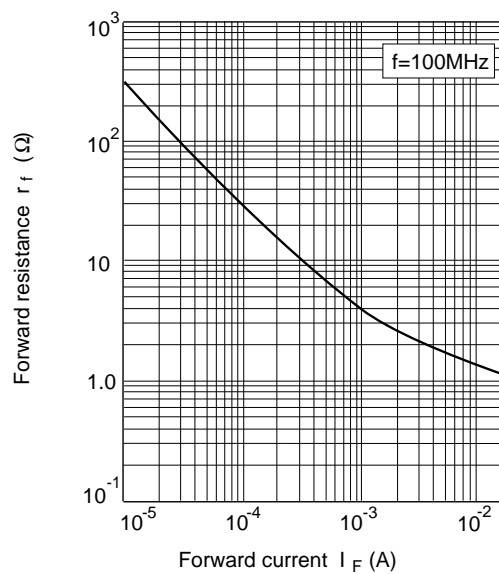
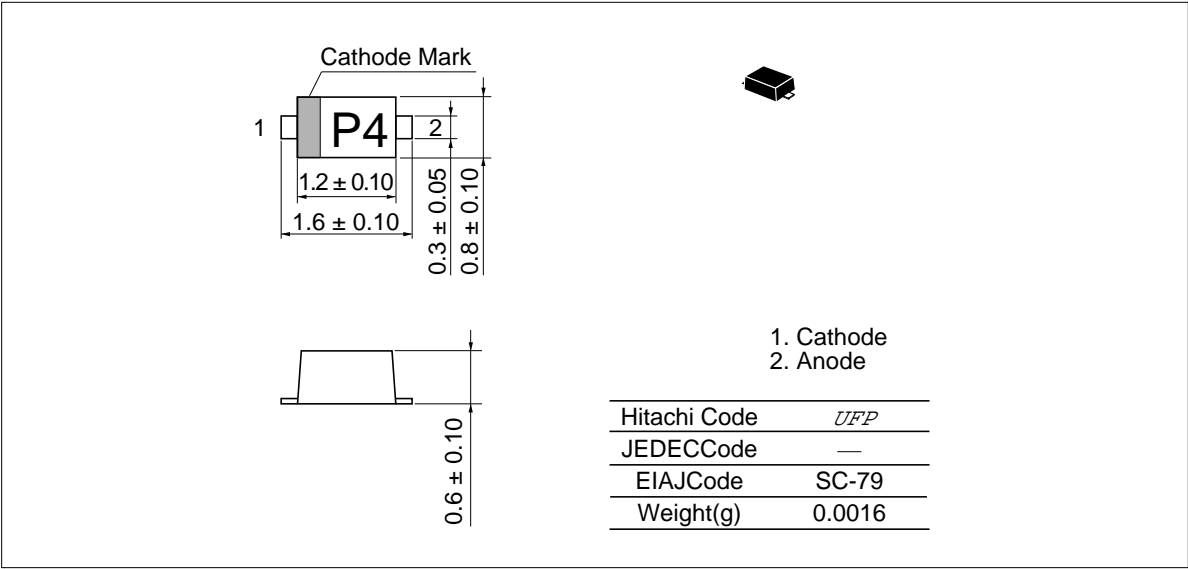


Fig.4 Forward resistance Vs. Forward current

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Package Dimensions

Unit : mm



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