

### Features

- Operating voltage: 3.5V~5.5V
- Differential input
- Power down control
- Bellcore CAS detection (Type II)
- 3.58MHz clock input

### **Applications**

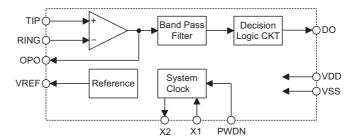
- Feature phone system
- Caller ID with Call Waiting

- Low standby current
- High sensitivity: -33dBm
- Data valid output
- 16-pin DIP/SOP package
- Computer telephony interface products
- Analog Display Service Interface (ADSI)

### **General Description**

The HT9033 is a CPE Alerting Signal (CAS) tone receiver integrated with digital filter/detector and analog front end circuit. The HT9033 using analog filter techniques to achieve very high precision detection for the present of CAS tone. It is ideal for receiving physical layer signal like Bellcore's CPE Alerting Signal (Type II caller ID) and similar evolving services like ETS/BT.

# **Block Diagram**



## **Pin Assignment**

1				1		
TIP 🗆	1	$\cup$	16	ОРО		
RING 🗆	2		15			
AVSS 🗆	3		14			
VSS 🗆	4		13			
PWDN	5		12			
NC 🗆	6		11	⊐x2		
NC 🗆	7		10	□X1		
	8		9	DNC		
HT9033						
-16 DIP-A/SOP-A						



# **Pin Description**

Pin Name	I/O	Description
TIP	Ι	This pin is connected to the tip side of the twisted pair wires.
RING	Т	This pin is connected to the ring side of the twisted pair wires.
OPO	0	Output of OP AMP
VREF	0	Reference voltage output, normally 1/2 V <sub>DD</sub> .
X1	I	A crystal or ceramic resonator should be connected to this pin and X2. This pin may be driven from an external clock source.
X2	0	A crystal or ceramic resonator should be connected to this pin and X1.
DO	0	CAS detection output. Active high.
PDWN	I	A logic "1" on this pin puts the chip in power down mode. When a logic "0" is on this pin, the chip in power up mode. This is a Schmitt trigger input.
VDD	_	Positive power
VSS	_	Negative power, ground
AVDD	_	Analog positive power
AVSS	_	Analog negative power
NC	—	No connection

### Absolute Maximum Ratings

Voltages are referenced to  $\mathsf{V}_{\mathsf{SS}},$  except where noted.

Supply Voltage0.5V to 6.0V	All Input Voltages25mW
Operating Temperature Range0°C to 70°C	Storage Temperature Range40°C to 150°C

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

# **D.C. Characteristics**

Symbol	Parameter		Test Conditions	Min.	Tun	Max.	Unit
Symbol	Falameter	V <sub>DD</sub>	Conditions	IVIIII.	Тур.	Wax.	Unit
V <sub>DD</sub>	Supply Voltage	_		3.5	_	5.5	V
I <sub>DD</sub>	Operating Current	5V	PDWN=0, 3.58MHz on	_	2.5	3	mA
I <sub>STBY</sub>	Standby Current	5V	PDWN=1, 3.58MHz off	_	1	2	μA
V <sub>IL</sub>	"Low" Input Voltage	5V		_	_	0.3	V <sub>DD</sub>
VIH	"High" Input Voltage	5V		0.7	_	_	V <sub>DD</sub>
I <sub>OL</sub>	Output Source Current (DO)	5V	V <sub>OL</sub> =0.1V <sub>DD</sub>	_	0.8	_	mA
I <sub>OH</sub>	Output Sink Current (DO)	5V	V <sub>OH</sub> =0.9V <sub>DD</sub>	_	-0.3		mA
f <sub>SYS</sub>	System Clock	_	_		3.58		MHz

Ta=25°C



Ta=25°C

Ta=25°C

# A.C. Characteristics

Symphol	Parameter		Test Conditions	Min	-		Unit
Symbol	Parameter	$V_{DD}$	Conditions	Min. Typ.		Max.	Unit
	Low Tone Frequency	5V			2130		Hz
	High Tone Frequency	5V			2750		Hz
	Frequency Deviation Acceptance	5V	Range within which, tones are accepted		_	±0.7	%
	Frequency Deviation Reject	5V	Range outside of which, tones are rejected	±2.5	_		%
	Acceptable Signal Level per Tone	5V	No speech	-33	_	0	dBm
	Reject Signal Level per Tone	5V	No speech		-36		dBm
	Twist Accept Limit	5V			±7	_	dB
	Signal to Noise Ratio	5V	—		-19		dB
t <sub>DET</sub>	Present Detect Time	5V	_		55	_	ms

# **Clock (System Frequency)**

Symbol	Deveneter		Test Conditions	Min.	Turn	Max.	Unit
Symbol	Parameter	Parameter V <sub>DD</sub> Conditions Min		win.	Тур.	wax.	Unit
f <sub>C</sub>	Clock Input Frequency	5V	_	3.5759	3.5795	3.5831	MHz
t <sub>LHCL</sub>	Clock Input Rising Time	5V	—	_	_	110	ns
t <sub>HLCL</sub>	Clock Input Falling Time	5V	—		_	110	ns
DC <sub>CL</sub>	Clock Input Duty Cycle	5V	_	40	50	60	%

## **Functional Description**

#### Power Down Mode

If PDWN pin= 1, the HT9033 is in reset status and the CAS detector is at power down mode, all function is disable. The oscillator circuit will be stop.

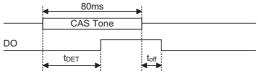
If PDWN pin= 0, CAS detector is activated.

#### **CAS Detector Output Signal**

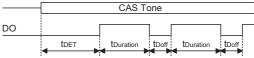
The output pin DO is CMOS structure. At the normal status (CAS tone is not presence), the DO pin output low. When the CAS tone is detected, the DO pin will output high level.

#### CAS Detector Timing





Condition 2 (Continuous CAS Tone):

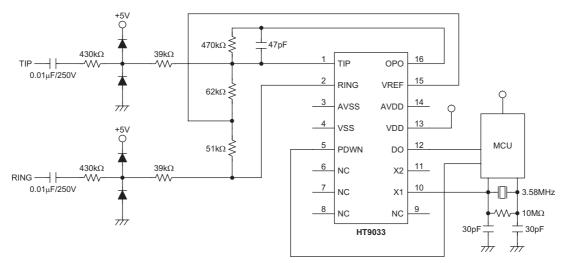


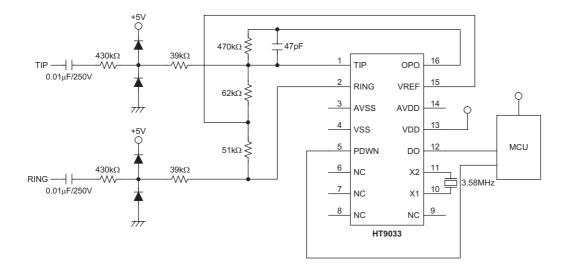
Parameter	Note	Min.	Тур.	Max.	Unit
t <sub>DET</sub>	CAS Detecting Time		55	_	ms
t <sub>off</sub>	DO Off Time	_	8	_	ms
t <sub>Duration</sub>	DO Duration Time		145		ms
t <sub>Doff</sub>	DO Duration Off Time		48		ms

If the CAS tone is presence more than  $t_{\text{DET}}$  time, the CAS tone will be detected, then the DO will output high.



# **Application Circuits**

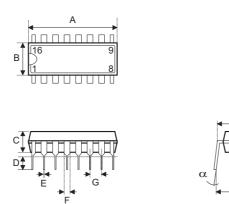






# Package Information

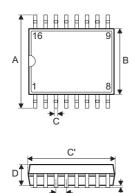
16-pin DIP (300mil) Outline Dimensions



Cumula al		Dimensions in mil				
Symbol	Min.	Nom.	Max.			
А	745		775			
В	240		260			
С	125		135			
D	125		145			
E	16		20			
F	50		70			
G	_	100	_			
Н	295		315			
I	335		375			
α	0°		15°			



### 16-pin SOP (300mil) Outline Dimensions



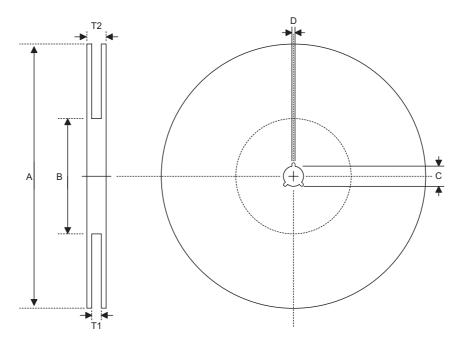


Courseland I		Dimensions in mil				
Symbol	Min.	Nom.	Max.			
А	394		419			
В	290		300			
С	14		20			
C′	390	_	413			
D	92	_	104			
E	_	50				
F	4	_				
G	32	_	38			
Н	4	_	12			
α	0°	_	10°			



# Product Tape and Reel Specifications

# **Reel Dimensions**

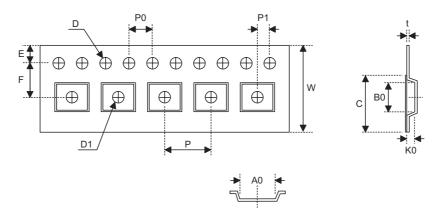


### SOP 16W (300mil)

Symbol	Description	Dimensions in mm
А	Reel Outer Diameter	330±1
В	Reel Inner Diameter	62±1.5
С	Spindle Hole Diameter	13±0.5 _0.2
D	Key Slit Width	2±0.5
T1	Space Between Flange	16.8+0.3 _0.2
T2	Reel Thickness	22.2±0.2



### **Carrier Tape Dimensions**



# SOP 16W (300mil)

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	16±0.2
Р	Cavity Pitch	12±0.1
E	Perforation Position	1.75±0.1
F	Cavity to Perforation (Width Direction)	7.5±0.1
D	Perforation Diameter	1.5+0.1
D1	Cavity Hole Diameter	1.5+0.25
P0	Perforation Pitch	4±0.1
P1	Cavity to Perforation (Length Direction)	2±0.1
A0	Cavity Length	10.9±0.1
В0	Cavity Width	10.8±0.1
K0	Cavity Depth	3±0.1
t	Carrier Tape Thickness	0.3±0.05
С	Cover Tape Width	13.3



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