



VN340SP(8933)

QUAD HIGH SIDE SMART POWER SOLID STATE RELAY

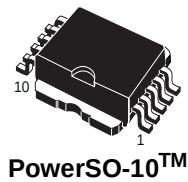
TYPE	$V_{\text{demag}}^{(*)}$	$R_{\text{DSon}}^{(*)}$	$I_{\text{OUT}}^{(*)}$	V_{CC}
VN340SP(8933)	$V_{\text{CC}}-55\text{V}$	0.2Ω	1A	36 V

(*) Per channel

- OUTPUT CURRENT: 1A PER CHANNEL
- DIGITAL I/O's CLAMPED AT 32V MINIMUM VOLTAGE
- PROTECTION AGAINST SHORTED LOAD AND OVERTEMPERATURE
- BUILT-IN CURRENT LIMITER
- UNDER VOLTAGE SHUT DOWN
- OPEN DRAIN DIAGNOSTIC OUTPUT
- FAST DEMAGNETIZATION OF INDUCTIVE LOADS
- CONFORMS TO IEC 1131-2

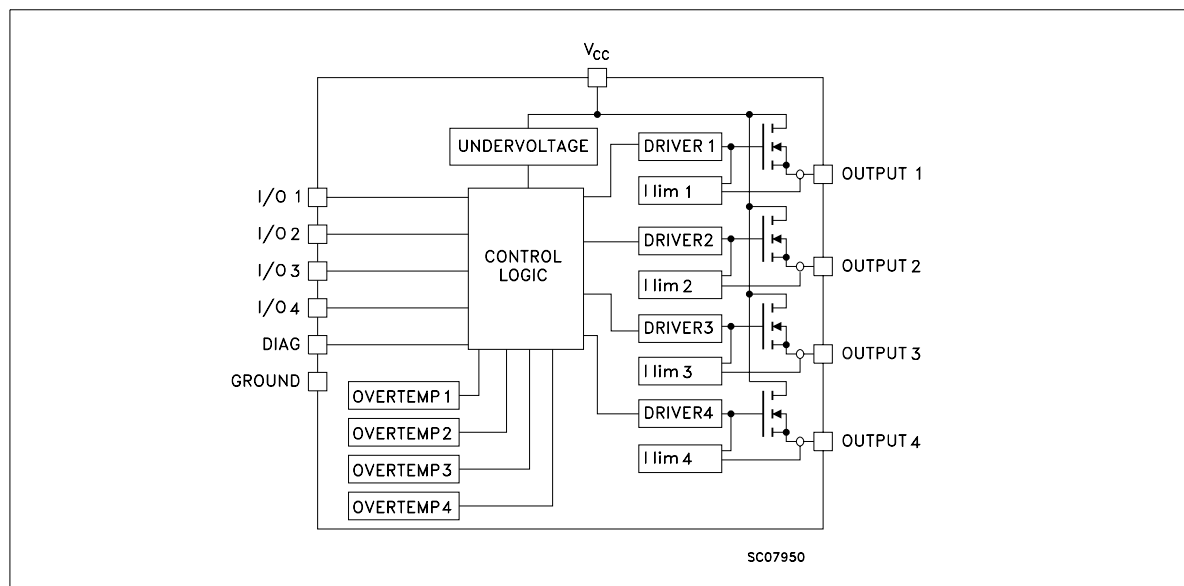
DESCRIPTION

The VN340SP(8933) is a monolithic device made using STMicroelectronics VIPower technology, intended for driving four independent resistive or inductive loads with one side connected to ground. Active current limitation avoids dropping the system power supply in case of shorted load.



Built-in thermal shut down protects the chip from overtemperature and short circuit. The open drain diagnostic output indicates overtemperature conditions. Each I/O is pulled down when overtemperature condition of the relative channel is verified.

BLOCK DIAGRAM



CONNECTION DIAGRAM TOP VIEW



THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{thj-case}$	Thermal resistance junction-case (1) (MAX)	2	°C/W
$R_{thj-amb}$	Thermal resistance junction-ambient (\$) (MAX)	50	°C/W

(1) All channels on

(\$) When mounted using minimum recommended pad size on FR-4 board

ELECTRICAL CHARACTERISTICS (10V<V_{CC}<36V; -25°C<T_j<85°C; unless otherwise specified)

POWER

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V _{CC}	Supply voltage		10		36	V
R _{ON}	On state resistance	I _{OUT} =0.5A; T _j =25°C I _{OUT} =0.5A; -25°C<T _j <85°C I _{OUT} =0.5A; T _j =125°C			0.2 0.32 0.4	Ω Ω Ω
I _S	Supply current	All channels off On state; V _{IN} =30V, I _{OUTn} =0V (T _j =125°C)			1 6	mA mA
V _{OL}	Low state output voltage	V _{IN} =V _{IL} ; R _{LOAD} ≥10MΩ			1.5	V
V _{demag}	Output voltage at turn-off	I _{OUT} =0.5A, L _{LOAD} =1mH	V _{CC} -65	V _{CC} -55	V _{CC} -45	V
I _{LGND}	Output current at turn-off	V _{CC} =V _{INn} =V _{GND} =V _{STAT} =18 to 30V T _{amb} =-25°C to 85°C (*)			2	mA

(*) see test configuration and application description

SWITCHING (V_{CC}=24V)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
t _{d(on)}	Turn-on delay on output current	I _{OUT} =0.5A, Resistive Load Input rise time <0.1μs, T _j =25°C		52	100	μs
t _r	Rise time of output current	I _{OUT} =0.5A, Resistive Load Input rise time <0.1μs, T _j =25°C		94	250	μs
t _{d(off)}	Turn-off delay on output current	I _{OUT} =0.5A, Resistive Load Input rise time <0.1μs, T _j =25°C		34	50	μs
t _f	Fall time of output current	I _{OUT} =0.5A, Resistive Load Input rise time <0.1μs, T _j =25°C		8	20	μs

ELECTRICAL CHARACTERISTICS (continued)**LOGIC INPUT** (Per each channel)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{IL}	I/O Input low level voltage				2	V
V_{IH}	I/O Input high level voltage (see note 1)		3.5			V
$V_{I(HYST)}$	I/O Input hysteresis voltage			0.5		V
I_{IN}	I/O Input current	$V_{IN}=30V$			25	μA
V_{ICL}	I/O Input clamp voltage (see note 1)	$I_{IN}=1mA$ $I_{IN}=-1mA$	32	36 -0.7		V V

Note 1 : The input voltage is internally clamped at 32V minimum , it is possible to connect the input pins to an higher voltage via an external resistor calculate to not exceed 10mA.

PROTECTION AND DIAGNOSTICS

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{DIAG} (*)$	Status voltage output low	$I_{STAT}=5mA$ (Fault condition)			1	V
$V_{SCL} (*)$	Status clamp voltage	$I_{DIAG}=1mA$ $I_{DIAG}=-1mA$	32	36 0.7		V V
V_{USD}	Under voltage shut down		5		8	V
I_{LIM}	DC Short circuit current	$V_{CC}=24V$; $R_{LOAD}<10m\Omega$	1		2	A
I_{OVPK}	Peak short circuit current	$V_{CC}=24V$; $V_{IN}=30V$; $R_{LOAD}<10m\Omega$ (see figure 2)			4	A
I_{DIAGH}	Leakage on diag pin in high state	$V_{DIAG}=24V$			25	μA
I_{LOAD}	Output leakage current	$V_{CC}=10$ to $36V$; $V_{IN}=V_{IL}$			50	μA
t_{SC}	Delay time of current limiter				100	μs
T_{TSD}	Thermal shut down temperature		150	170		$^{\circ}C$
T_R	Thermal reset temperature		135	155		$^{\circ}C$

(*) Status determination > 100ms after the switching edge.

Note: If INPUT pin is floating the corresponding channel will automatically switch off. If GND pin is disconnected, the channel will switch off provided V_{CC} not exceed 36V.

FIGURE 1: Avalanche Energy Test Circuit

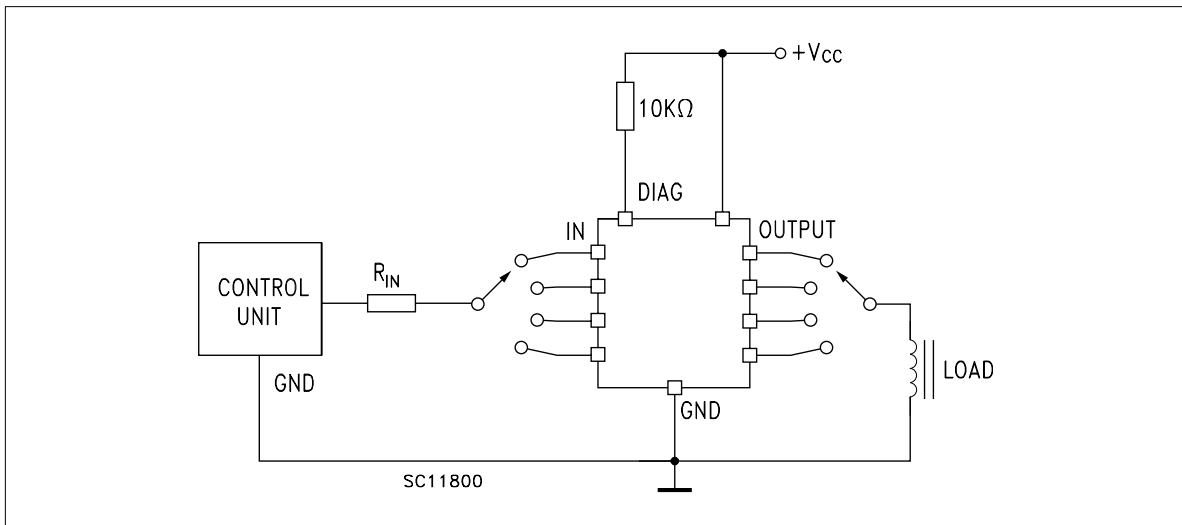
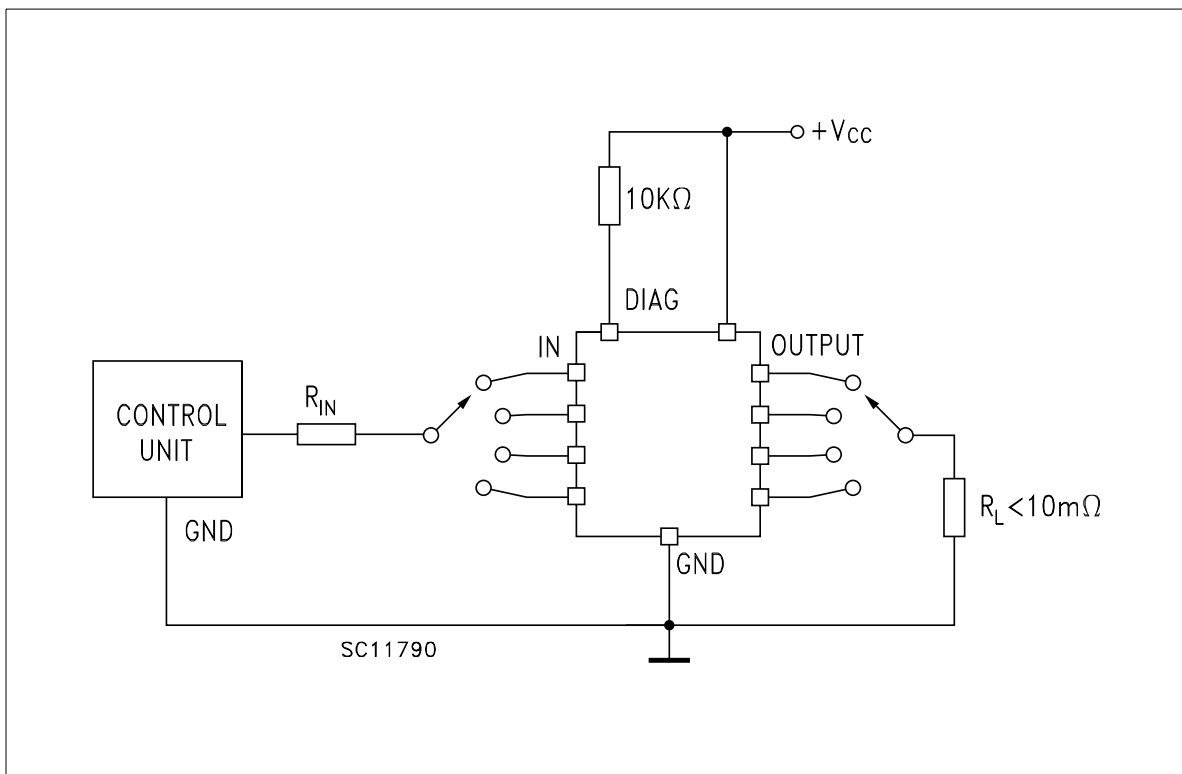


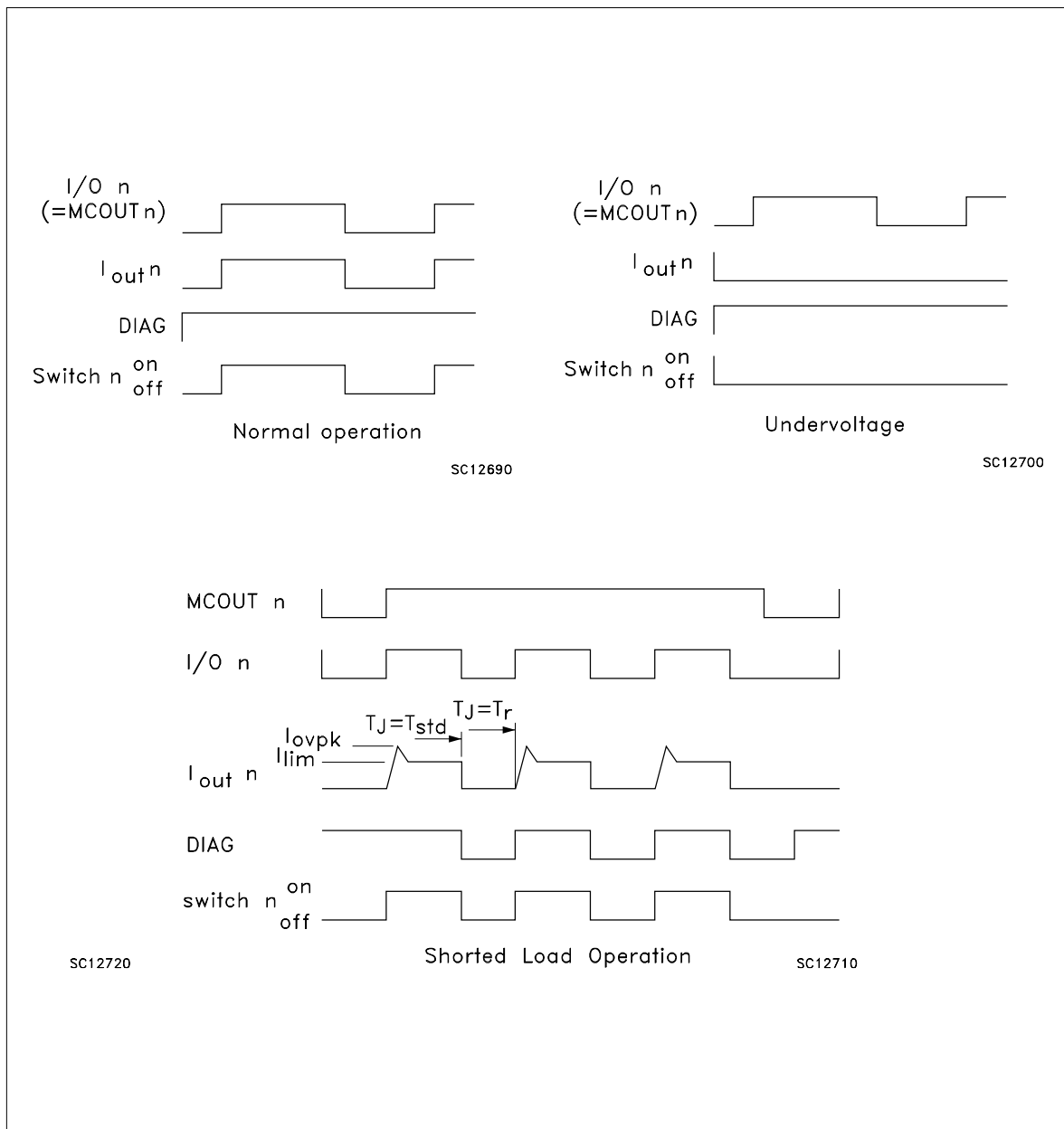
FIGURE 2: Peak Short Circuit Current Test Circuit



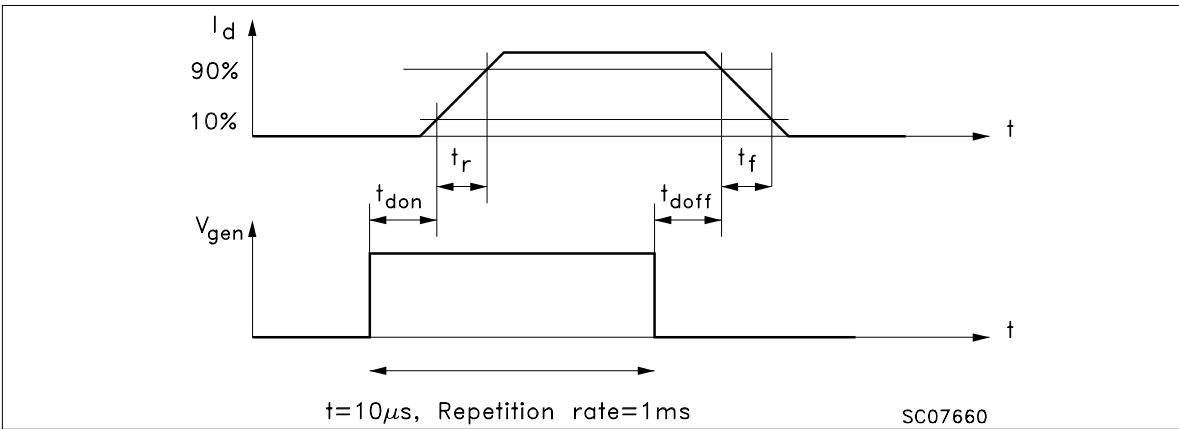
THRUTH TABLE

	MCOUTn	I/On	OUTPUTn	DIAGNOSTIC
Normal operation	L	L	L	H
	H	H	H	H
Overtemperature	L	L	L	H
	H	L	L	L
Undervoltage	L	L	L	H
	H	H	L	H
Shorted load (Current limitation)	L	L	L	H
	H	H	H	H

Figure 3: Switching Waveforms



SWITCHING PARAMETERS TEST CONDITIONS



DRIVING CIRCUIT

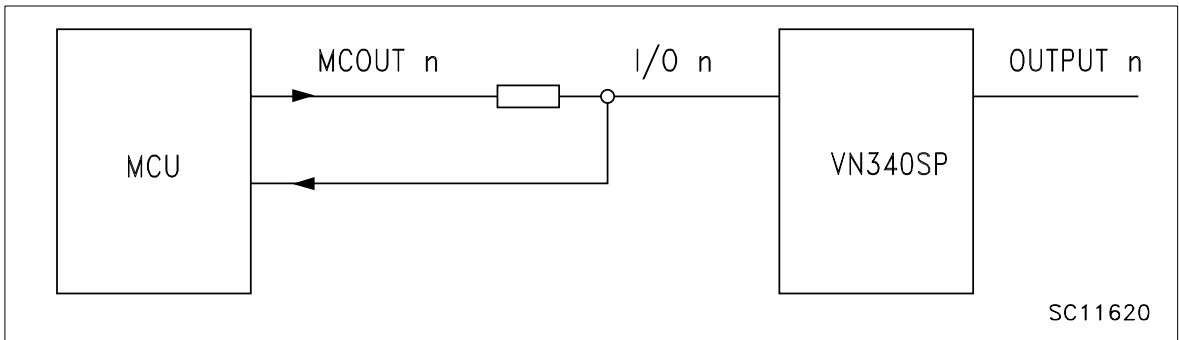
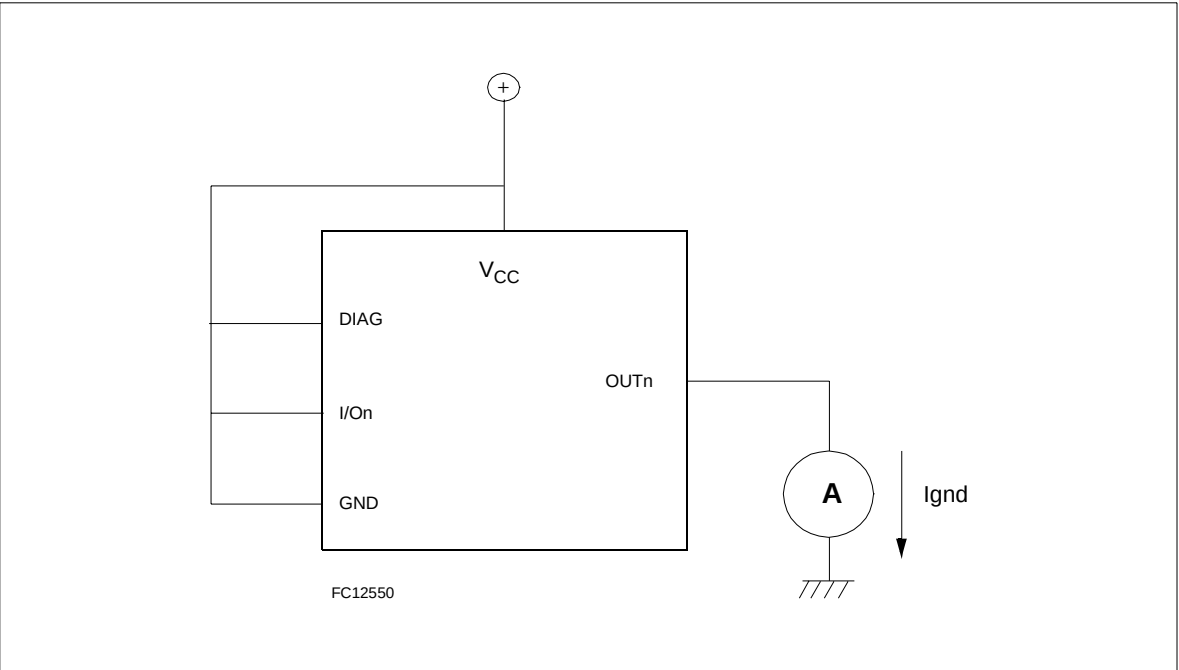


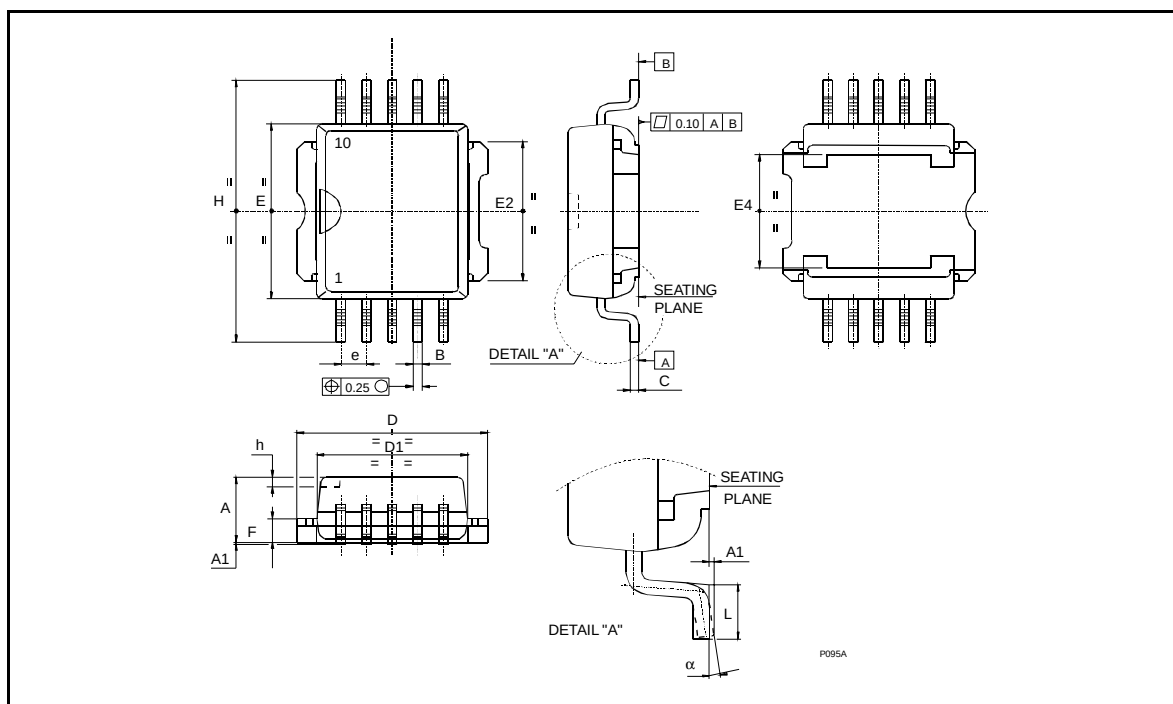
Fig. 4: I_{LGND} Test Configuration



PowerSO-10™ MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	3.35		3.65	0.132		0.144
A (*)	3.4		3.6	0.134		0.142
A1	0.00		0.10	0.000		0.004
B	0.40		0.60	0.016		0.024
B (*)	0.37		0.53	0.014		0.021
C	0.35		0.55	0.013		0.022
C (*)	0.23		0.32	0.009		0.0126
D	9.40		9.60	0.370		0.378
D1	7.40		7.60	0.291		0.300
E	9.30		9.50	0.366		0.374
E2	7.20		7.60	0.283		300
E2 (*)	7.30		7.50	0.287		0.295
E4	5.90		6.10	0.232		0.240
E4 (*)	5.90		6.30	0.232		0.248
e		1.27			0.050	
F	1.25		1.35	0.049		0.053
F (*)	1.20		1.40	0.047		0.055
H	13.80		14.40	0.543		0.567
H (*)	13.85		14.35	0.545		0.565
h		0.50			0.002	
L	1.20		1.80	0.047		0.070
L (*)	0.80		1.10	0.031		0.043
α	0°		8°	0°		8°
α (*)	2°		8°	2°		8°

(*) Muar only POA P013P



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