4AK16

Silicon N-Channel Power MOS FET Array

HITACHI

Application

High speed power switching

Features

· Low on-resistance

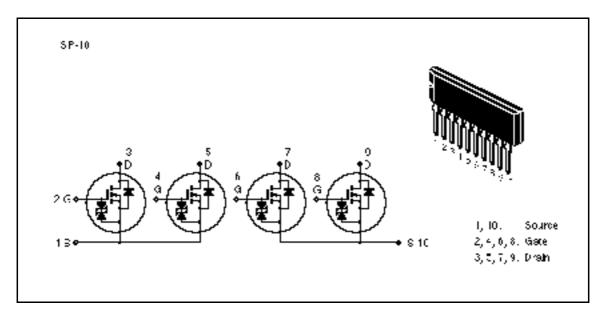
$$\begin{split} R_{\rm DS(on)} & 0.18 \quad , \, V_{\rm GS} = 10 \; V, \, I_{\rm D} = 5 \; A \\ R_{\rm DS(on)} & 0.25 \quad , \, V_{\rm GS} = 4 \; V, \, I_{\rm D} = 5 \; A \end{split}$$

- Capable of 4 V gate drive
- · Low drive current
- · High speed switching
- High density mounting
- Suitable for motor driver, solenoid driver and lamp driver
- Discrete packaged devices of same die: 2SK970, 2SK1093, 2SK974(L), 2SK974(S)



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Outline



Absolute Maximum Ratings ($Ta = 25^{\circ}C$) (1 Unit)

Item	Symbol	Rating	Unit
Drain to source voltage	$V_{ t DSS}$	60	V
Gate to source voltage	$V_{\sf GSS}$	±20	V
Drain current	I _D	5	A
Drain peak current	I _{D(pulse)} *1	20	A
Body to drain diode reverse drain current	I _{DR}	5	А
Channel dissipation	Pch (Tc = 25°C)*2	28	W
Channel dissipation	Pch*2	4	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW 10 µs, duty cycle 1%

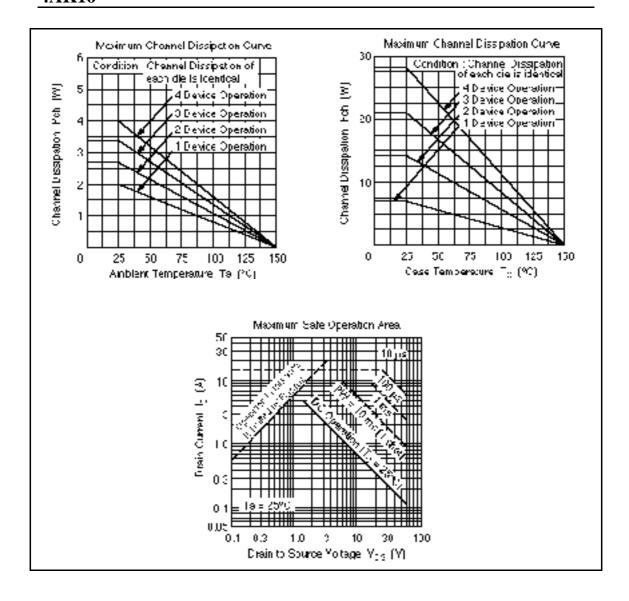
2. 4 devices operation

Electrical Characteristics (Ta = 25°C) (1 Unit)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	250	μΑ	$V_{DS} = 50 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{\text{DS(on)}}$	_	0.12	0.18		$I_D = 5 A$ $V_{GS} = 10 V^{*1}$
		_	0.17	0.25		$I_D = 5 A$ $V_{GS} = 4 V^{*1}$
Forward transfer admittance	y _{fs}	3.5	6.0	_	S	$I_D = 5 A$ $V_{DS} = 10 V^{*1}$
Input capacitance	Ciss	_	400	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	220	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	60	_	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	5	_	ns	I _D = 5 A
Rise time	t _r	_	55	_	ns	V _{GS} = 10 V
Turn-off delay time	$t_{\text{d(off)}}$	_	140	_	ns	$R_L = 6$
Fall time	t _f	_	90		ns	
Body to drain diode forward voltage	V_{DF}		1.0		V	$I_F = 5 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	_	100	_	ns	$I_F = 5 \text{ A}, V_{GS} = 0$ $dIF/dt = 50 \text{ A}/\mu \text{s}$

Note: 1. Pulse test

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