

## Features

- Operating voltage: 2.4V~5.0V
- Operating frequency: 3.58MHz~12MHz (typ. 8MHz)
- 20 bidirectional I/O lines
- Two 8-bit programmable timer with 8 stage prescaler
- Watchdog Timer
- Built-in 8-bit MCU with 208×8 bits RAM
- Built-in 32K×16-bit ROM for program/data shared
- Mono output
- High D/A converter resolution: 16 bits
- Polyphonic up to 8 notes
- Independent pan and volume mix can be assigned to each sound component

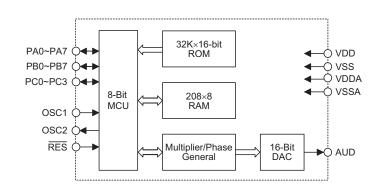
## **General Description**

**Block Diagram** 

The HT36A3 is an 8-bit high performance RISC-like microcontroller specifically designed for music applications. It provides an 8-bit MCU and a 8 channel wavetable synthesizer. The program ROM is composed of both program control codes and wavetable voice codes, and can be easily programmed.

- Sampling rate of 25kHz as 6.4MHz for system frequency
- Eight-level subroutine nesting
- HALT function and wake-up feature to reduce power consumption
- Bit manipulation instructions
- 16-bit table read instructions
- 63 powerful instructions
- All instructions in 1 or 2 machine cycles
- 28-pin SOP, 48-pin SSOP package

The HT36A3 has a built-in 8-bit microprocessor which programs the synthesizer to generate the melody by setting the special register from 20H~2AH. A HALT feature is provided to reduce power consumption.

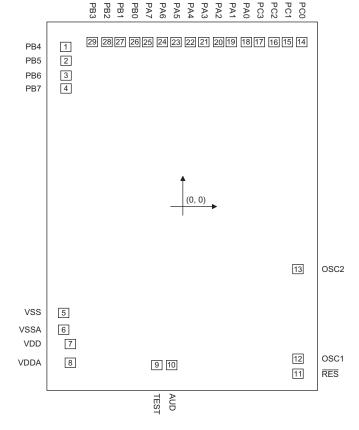




## **Pin Assignment**

		PA4 🗖 1	48 🗖 PA3
		PA5 🗖 2	47 🗖 PA2
		PA6 🗖 3	46 🗖 PA1
		PA7 🗖 4	45 🗖 PA0
		РВ0 🗖 5	44 🗖 PC3
		PB1 🗖 6	43 🗖 PC2
		PB2 🗖 7	42 🗖 PC1
		PB3 🗖 8	41 🗖 PC0
		РВ4 🗖 9	40 🗖 NC
		PB5 🗖 10	39 🗖 NC
PA4 🗖 1	28 🗖 PA3	PB6 🗖 11	38 🗆 NC
PA5 🗖 2	27 🗖 PA2	PB7 🗖 12	37 🗖 NC
PA6 🗖 3	26 🗖 PA1	NC 🗖 13	36 🗖 NC
PA7 🗖 4	25 🗖 PA0	NC 🗖 14	35 🗆 NC
NC 🗖 5	24 🗖 NC	VSS 🗖 15	34 🗆 OSC2
NC 🗆 6	23 🗖 NC	VSSA 🗖 16	33 🗖 OSC1
NC 🗆 7	22 🗖 OSC2		32 🗖 NC
VSS 🗆 8	21 🗖 NC		31 🗖 NC
VSSA 🗖 9	20 🗖 OSC1	NC 🗖 19	30 🗆 RES
VDD 🗖 10	19 🗖 NC	NC 🗖 20	29 🗖 NC
VDDA 🗖 11		NC 🗆 21	28 🗆 NC
NC 🗖 12	17 🗖 NC	NC 🗖 22	27 🗖 NC
TEST 🗖 13	16 🗖 NC	NC 🗖 23	26 🗖 AUD
AUD 🗖 14	15 🗖 NC	NC 🗖 24	25 🗆 TEST
	36A3 SOP-A	HT36/ - 48 SSC	

## **Pad Assignment**



 $\label{eq:chip} \mbox{Chip size: 83 \times 111 (mil)}^2 $$ * The IC substrate should be connected to VSS in the PCB layout artwork. $$$ 



## **Pad Coordinates**

Pad No.	х	Y	Pad No.	х	Y
1	-879.100	1202.400	16	693.900	1240.150
2	-879.100	1097.100	17	583.300	1240.150
3	-879.100	986.500	18	483.300	1240.150
4	-879.100	886.500	19	372.700	1240.150
5	-898.600	-805.050	20	272.700	1240.150
6	-898.600	-931.910	21	162.100	1240.150
7	-847.650	-1041.910	22	62.100	1240.150
8	-847.650	-1180.350	23	-48.500	1240.150
9	-193.090	-1200.600	24	-148.500	1240.150
10	-81.290	-1200.600	25	-259.100	1240.150
11	875.950	-1262.274	26	-359.100	1240.150
12	875.950	-1149.626	27	-469.700	1240.150
13	875.950	-471.774	28	-569.700	1240.150
14	901.500	1240.150	29	-680.300	1240.150
15	793.900	1240.150			

## **Pad Description**

Pad Name	I/O	Internal Connection	Function
PA0~PA7	I/O	Pull-High or None	Bidirectional 8-bit Input/Output port
PB0~PB7	I/O	Pull-High or None	Bidirectional 8-bit Input/Output port
PC0~PC3	I/O	Pull-High or None	Bidirectional 8-bit Input/Output port
VSS			Negative power supply, ground
VSSA			Negative power supply of DAC, ground
VDD			Digital power supply
VDDA			DAC power supply
AUD	0		Audio output
TEST			No connection (open)
RES	I		Reset input, active low (no pull-high)
OSC1 OSC2	I O		OSC1 and OSC2 are connected to an RC network or a crystal (by mask op- tion) for the internal system clock. In the case of RC operation, OSC2 is the output terminal for 1/8 system clock. The system clock may come from the crystal, the two pins cannot be floating.



## **Absolute Maximum Ratings**

Supply Voltage	V_{SS}=0.3V to V_{SS}+6V	Storage Temperature	.–50°C to 125°C
Input Voltage	$V_{SS}$ –0.3V to $V_{DD}$ +0.3V	Operating Temperature	–25°C to 70°C

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

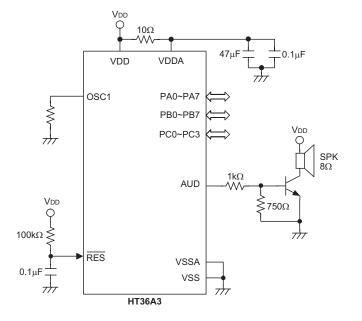
## **D.C. Characteristics**

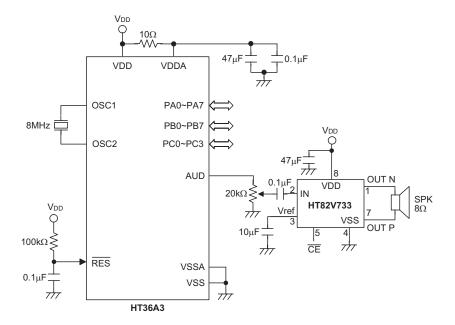
Ta=25°C

Symbol	Parameter	Test Conditions		Min.	<b>T</b>	Maria	11
Symbol	Parameter	V <sub>DD</sub>	Conditions	Win.	Тур.	Max.	Unit
V <sub>DD</sub>	Operating Voltage	_		2.2	4.5	5.5	V
I <sub>DD</sub>	Operating Current	4.5V	No load, f <sub>OSC</sub> =8MHz	_	8	16	mA
I <sub>STB</sub>	Standby Current (WDT Disabled)	4.5V	No load, System HALT	_	1	3	μA
I <sub>OH</sub>	I/O Ports Source Current	4.5V	V <sub>OH</sub> =4.5V	2	3	_	mA
I <sub>OL</sub>	I/O Ports Sink Current	4.5V	V <sub>OL</sub> =0.5V	2	3	_	mA
VIH	Input High Voltage for I/O Ports	4.5V		0.8V <sub>DD</sub>		V <sub>DD</sub>	V
V <sub>IL</sub>	Input Low Voltage for I/O Ports	4.5V		0		0.2V <sub>DD</sub>	V



## **Application Circuit**

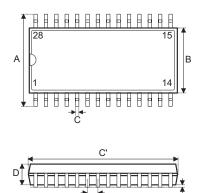






# Package Information

28-pin SOP (300mil) Outline Dimensions

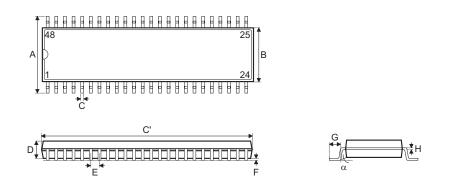




Symbol		Dimensions in mil	
Symbol	Min.	Nom.	Max.
A	394	—	419
В	290		300
С	14		20
C'	697		713
D	92		104
E		50	—
F	4		_
G	32		38
Н	4		12
α	0°		10°



## 48-pin SSOP (300mil) Outline Dimensions

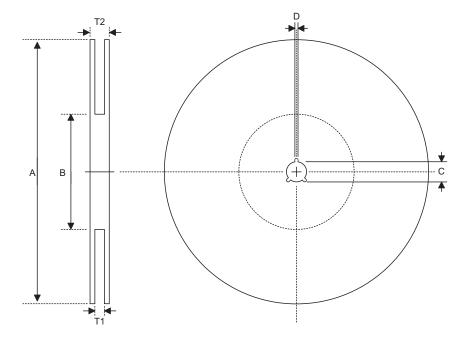


Symbol		Dimensions in mil	
Symbol	Min.	Nom.	Max.
A	395	—	420
В	291		299
С	8	—	12
C′	613	_	637
D	85	—	99
E		25	
F	4		10
G	25		35
Н	4		12
α	0°		8°



# **Product Tape and Reel Specifications**

## **Reel Dimensions**



## SOP 28W (300mil)

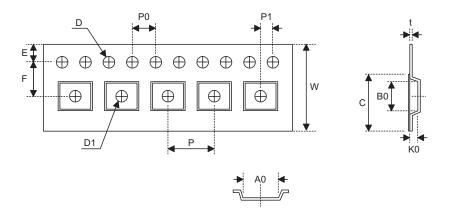
Symbol	Description	Dimensions in mm
А	Reel Outer Diameter	330±1.0
В	Reel Inner Diameter	62±1.5
с	Spindle Hole Diameter	13.0+0.5 _0.2
D	Key Slit Width	2.0±0.5
T1	Space Between Flange	24.8+0.3 0.2
T2	Reel Thickness	30.2±0.2

### SSOP 48W

Symbol	Description	Dimensions in mm
А	Reel Outer Diameter	330±1.0
В	Reel Inner Diameter	100±0.1
с	Spindle Hole Diameter	13.0+0.5 0.2
D	Key Slit Width	2.0±0.5
T1	Space Between Flange	32.2+0.3 0.2
T2	Reel Thickness	38.2±0.2

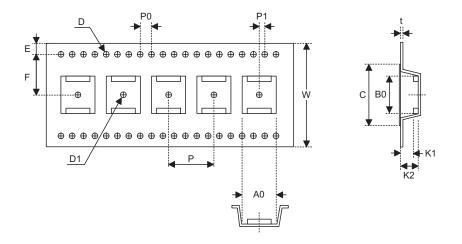


### **Carrier Tape Dimensions**



Symbol	Description	Dimensions in mm
W	Carrier Tape Width	24.0±0.3
Р	Cavity Pitch	12.0±0.1
E	Perforation Position	1.75±0.1
F	Cavity to Perforation (Width Direction)	11.5±0.1
D	Perforation Diameter	1.5+0.1
D1	Cavity Hole Diameter	1.5+0.25
P0	Perforation Pitch	4.0±0.1
P1	Cavity to Perforation (Length Direction)	2.0±0.1
A0	Cavity Length	10.85±0.1
B0	Cavity Width	18.34±0.1
K0	Cavity Depth	2.97±0.1
t	Carrier Tape Thickness	0.35±0.01
С	Cover Tape Width	21.3





### SSOP 48W

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	32.0±0.3
Р	Cavity Pitch	16.0±0.1
E	Perforation Position	1.75±0.1
F	Cavity to Perforation (Width Direction)	14.2±0.1
D	Perforation Diameter	2.0 Min.
D1	Cavity Hole Diameter	1.5+0.25
P0	Perforation Pitch	4.0±0.1
P1	Cavity to Perforation (Length Direction)	2.0±0.1
A0	Cavity Length	12.0±0.1
В0	Cavity Width	16.20±0.1
K1	Cavity Depth	2.4±0.1
K2	Cavity Depth	3.2±0.1
t	Carrier Tape Thickness	0.35±0.05
С	Cover Tape Width	25.5

#### Holtek Semiconductor Inc. (Headquarters)

No.3, Creation Rd. II, Science-based Industrial Park, Hsinchu, Taiwan Tel: 886-3-563-1999 Fax: 886-3-563-1189 http://www.holtek.com.tw

#### Holtek Semiconductor Inc. (Sales Office)

11F, No.576, Sec.7 Chung Hsiao E. Rd., Taipei, Taiwan Tel: 886-2-2782-9635 Fax: 886-2-2782-9636 Fax: 886-2-2782-7128 (International sales hotline)

Holtek Semiconductor (Shanghai) Inc. 7th Floor, Building 2, No.889, Yi Shan Rd., Shanghai, China Tel: 021-6485-5560 Fax: 021-6485-0313 http://www.holtek.com.cn

#### Holtek Semiconductor (Hong Kong) Ltd.

Block A, 3/F, Tin On Industrial Building, 777-779 Cheung Sha Wan Rd., Kowloon, Hong Kong Tel: 852-2-745-8288 Fax: 852-2-742-8657

Holmate Semiconductor, Inc. 46712 Fremont Blvd., Fremont, CA 94538 Tel: 510-252-9880 Fax: 510-252-9885 http://www.holmate.com

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