

Silicon P Channel MOS FET

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter and motor driver

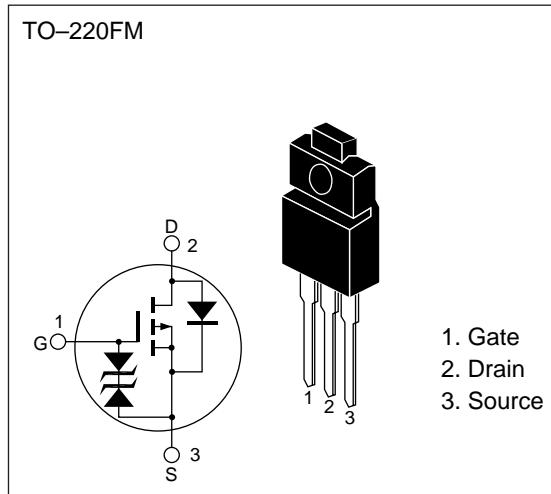


Table 1 Absolute Maximum Ratings (Ta = 25°C)

| Item | Symbol | Ratings | Unit |
|--|-------------------------|-------------|------|
| Drain to source voltage | V _{DSS} | -200 | V |
| Gate to source voltage | V _{GSS} | ±20 | V |
| Drain current | I _D | -6 | A |
| Drain peak current | I _{D(pulse)} * | -24 | A |
| Body-drain diode reverse drain current | I _{DR} | -6 | A |
| Channel dissipation | P _{ch} ** | -30 | W |
| Channel temperature | T _{ch} | 150 | °C |
| Storage temperature | T _{stg} | -55 to +150 | °C |

* PW ≤ 10 µs, duty cycle ≤ 1 %

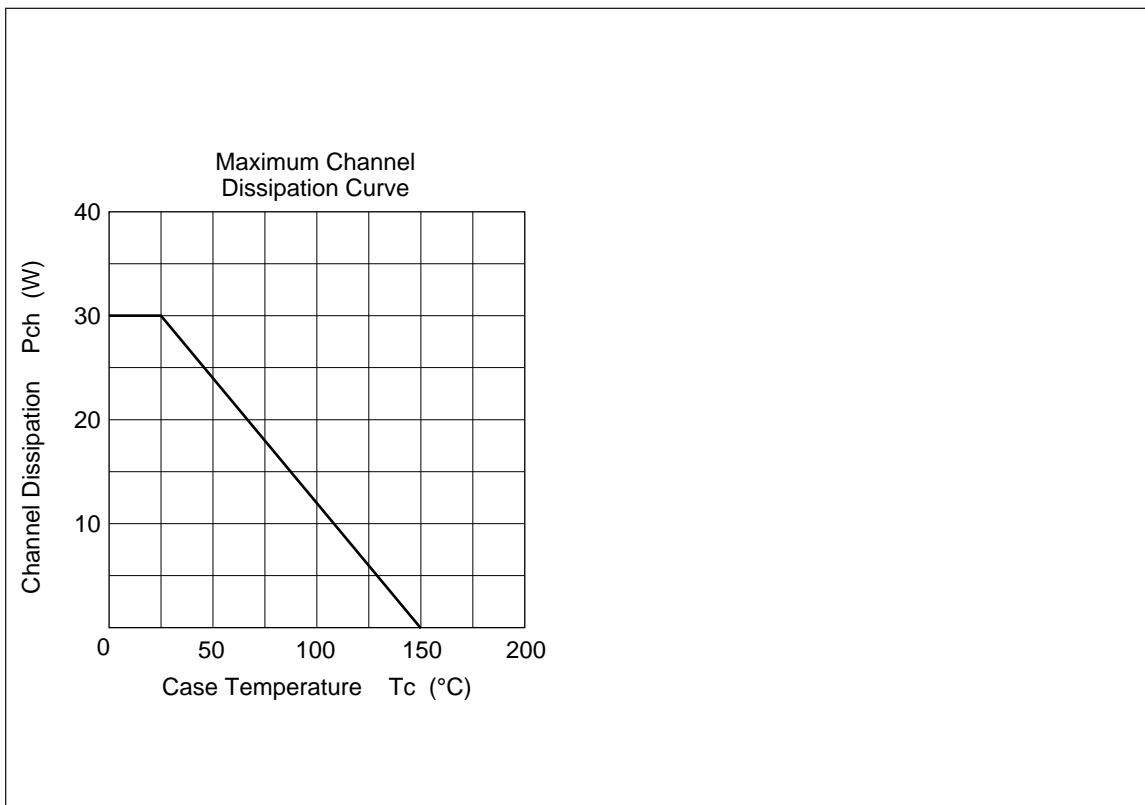
** Value at T_c = 25 °C

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Table 2 Electrical Characteristics (Ta = 25°C)

| Item | Symbol | Min | Typ | Max | Unit | Test conditions |
|--|----------------------|------|-------|------|------|---|
| Drain to source breakdown voltage | V _{(BR)DSS} | -200 | — | — | V | I _D = -10 mA, V _{GS} = 0 |
| Gate to source breakdown voltage | V _{(BR)GSS} | ±20 | — | — | V | I _G = ±100 µA, V _{DS} = 0 |
| Gate to source leak current | I _{GSS} | — | — | ±10 | µA | V _{GS} = ±16 V, V _{DS} = 0 |
| Zero gate voltage drain current | I _{DSS} | — | — | -250 | µA | V _{DS} = -160 V, V _{GS} = 0 |
| Gate to source cutoff voltage | V _{GS(off)} | -2.0 | — | -4.0 | V | I _D = -1 mA, V _{DS} = -10 V |
| Static drain to source on state resistance | R _{DS(on)} | — | 0.7 | 0.9 | Ω | I _D = -3 A V _{GS} = -10 V * |
| Forward transfer admittance | y _{fs} | 2.0 | 3.5 | — | S | I _D = -3 A V _{DS} = -10 V * |
| Input capacitance | C _{iss} | — | (920) | — | pF | V _{DS} = -10 V |
| Output capacitance | C _{oss} | — | (190) | — | pF | V _{GS} = 0 |
| Reverse transfer capacitance | C _{rss} | — | (70) | — | pF | f = 1 MHz |
| Turn-on delay time | t _{d(on)} | — | (17) | — | ns | I _D = -3 A |
| Rise time | t _r | — | (40) | — | ns | V _{GS} = -10 V |
| Turn-off delay time | t _{d(off)} | — | (85) | — | ns | R _L = 6Ω |
| Fall time | t _f | — | (45) | — | ns | |
| Body-drain diode forward voltage | V _{DF} | — | -1.0 | — | V | I _F = -6 A, V _{GS} = 0 |
| Body-drain diode reverse recovery time | t _{rr} | — | (170) | — | ns | I _F = -6 A, V _{GS} = 0, dI _F / dt = 50 A / µs |

* Pulse Test



Package Dimensions

Unit : mm

