

FC11

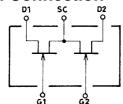
N-Channel Junction Silicon FET

Low-Frequency General-Purpose Amp, **Differential Amp Applications**

Features

- · Adoption of FBET process.
- · Composite type with 2 transistors contained in the CP package currently in use, improving the mounting efficiency greatly.
- · The FC11 is formed with two chips, being equivalent to the 2SK771, placed in one package.
- · Excellent in the thermal equilibrium and pair capability and suitable for use in differential amp.
- · Common source.

Electrical Connection



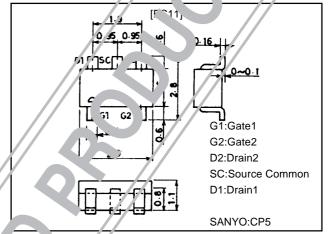
Specifications

Absolute Maximum Ratings at Ta = 25%

Package Dimensions

unit:mm

2070



Parameter	Sy nbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSX}		40	V
Gate-to-Drain Voltage	V _{GD}		-40	V
Gate Current	7	//	10	mA
Drain Current	D	//	10	mA
Allowable Power Dissipation	J. J	//	200	mW
Total Dissipation	PT	/	300	mW
Junction Temperature	Tj //		150	°C
Storage Temperature			−55 to +150	°C

Electrical Characteristics 25 C

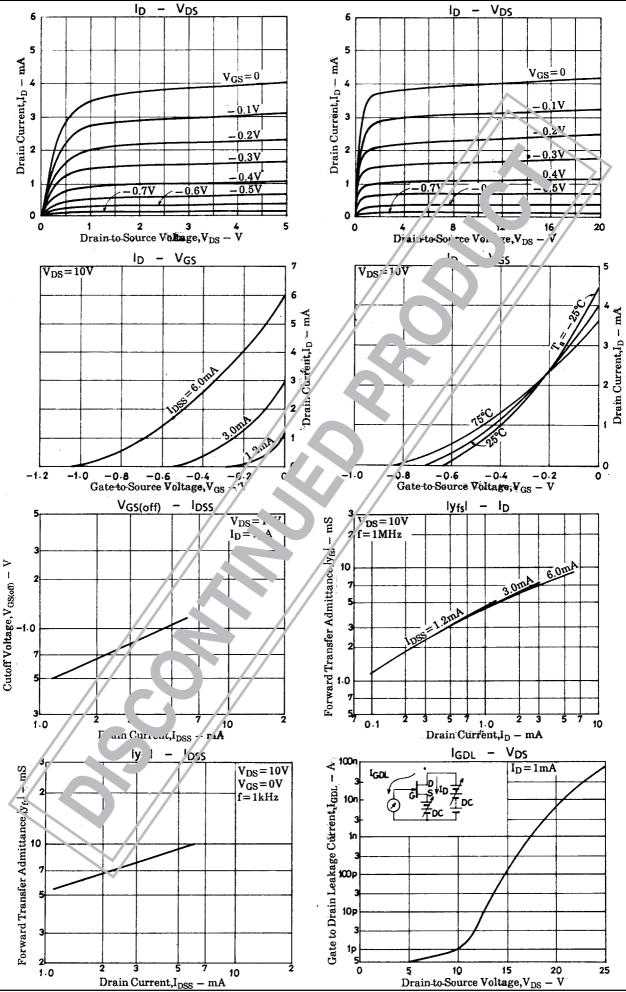
Parameter	Symbol	Conditions	Ratings			Unit
Parameter			min	typ	max	J OI III
Gate-to-Drain Breakduwn Voltag	V(BF)GDS	I _G =10μA, V _{DS} =0	-40			V
Gate Cutoff Curver	GSS	V _{GS} =-20V, V _{DS} =0V			-1.0	nA
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1μA	-0.3	-0.9	-1.8	V
Gate-to-Source Voltage no	ΔVGS	V _{GS} large – V _{GS} small , V _{DS} =10V, I _D =1mA			30	mV
Drain Current	IDSS	V _{DS} =10V, V _{GS} =0V	1.2*		6.0*	mA
Drair, Cirrent Ro		V _{DS} =10V, I _{DSS} small/I _{DSS} large	0.9			
Forward Transfer Adn. 27 e	Yfs	V _{DS} =10V, V _{GS} =0V, f=1kHz	4.5	9.0		mS
Forward Transfer Admittance Ratio		V _{DS} =10V, Y _{fS} small / Y _{fS} large	0.9			
Input Capacitacnoc	Ciss	V _{DS} =10V, V _{GS} =0V, f=1MHz		9.0		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, V _{GS} =0V, f=1MHz		2.1		pF
Noise Figure	NF	V_{DS} =10V, R_0 =1k Ω , I_D =1mA, f=1kHz		1.5		dB

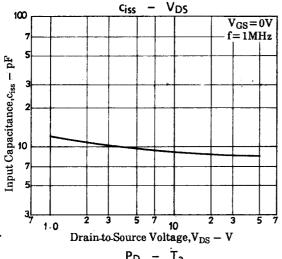
Note*:The FC11 is classified by I_{DSS} as follows (unit:mA)

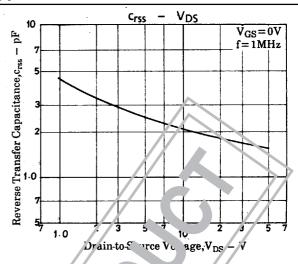
1.2 D 3.0 2.5 E 6.0 Marking:11

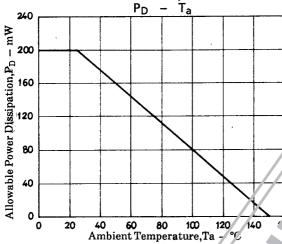
I_{DSS} rank:D,E

The Specifications shown above are for each individual transistor.









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