2SK3124

Silicon N-Channel Power F-MOS FET

Features

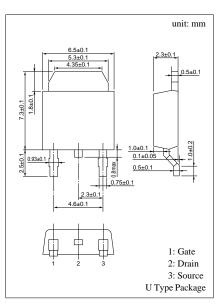
- Avalanche energy capacity guaranteed
- High-speed switching
- No secondary breakdown
- High electrostatic breakdown voltage

Applications

- High-speed switching (switching power supply)
- For high-frequency power amplification

Absolute Maximum Ratings $(1_c = 25 \text{ C})$								
Parameter		Symbol	Ratings	Unit				
Drain to Source breakdown voltage		V _{DSS}	400	V				
Gate to Source voltage		V _{GSS}	±20	V				
Drain current	DC	I _D	±0.5	А				
	Pulse	I _{DP}	±1	А				
Avalanche energy capacity		EAS*	0.25	mJ				
Allowable power	$T_C = 25^{\circ}C$	р	10	W				
dissipation	$Ta = 25^{\circ}C$	P _D	1					
Channel temperature		T _{ch}	150	°C				
Storage temperature		T _{stg}	-55 to +150	°C				

Absolute Maximum Ratings ($T_C = 25^{\circ}C$)



* L = 2mH, $I_L = 0.5A$, 1 pulse

Electrical Characteristics ($T_C = 25^{\circ}C$)

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I _{DSS}	$V_{DS} = 320V, V_{GS} = 0$			10	μA
Gate to Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0$			±1	μΑ
Drain to Source breakdown voltage	V _{DSS}	$I_D = 1mA, V_{GS} = 0$	400			V
Gate threshold voltage	V _{th}	$V_{DS} = 10V, I_D = 1mA$	1		3	V
Drain to Source ON-resistance	R _{DS(on)}	$V_{GS} = 10V, I_D = 0.1A$		17	23	Ω
Forward transfer admittance	Y _{fs}	$V_{DS} = 10V, I_D = 0.1A$	100	160		mS
Diode forward voltage	V _{DSF}	$I_{DR} = 0.1 A, V_{GS} = 0$			-1.5	V
Input capacitance (Common Source)	C _{iss}			48		pF
Output capacitance (Common Source)	C _{oss}	$V_{DS} = 10V, V_{GS} = 0, f = 1MHz$		10		pF
Reverse transfer capacitance (Common Source)	C _{rss}			5		pF
Turn-on time (delay time)	t _{d(on)}			65		ns
Rise time	t _r	$V_{\rm DD} = 100 V, I_{\rm D} = 0.1 A$		35		ns
Fall time	t _f	$V_{GS} = 10V, R_L = 1\Omega$		40		ns
Turn-off time (delay time)	t _{d(off)}			70		ns
Thermal resistance between channel and case	R _{th(ch-c)}				12.5	°C/W
Thermal resistance between channel and atmosphere	R _{th(ch-a)}				125	°C/W

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