

### 6W DUAL DC-DC CONVERTERS

# DAC2800D SERIES

4707 Dey Road Liverpool, N.Y. 13088

HI-REL DESIGN

- WAVE SOLDERABLE PACKAGE
- ALL CERAMIC CAPACITORS
- SURFACE MOUNT MAGNETICS

#### FEATURES - DUAL OUTPUT

- REPLACES APEX DHC2812D & DHC2815D
- BOTH OUTPUTS FULLY REGULATED
- NO DERATING -55°C TO +125°C
- ◆ HIGH ISOLATION 500V
- TRACKING OUPUT V ADJUSTMENT STANDARD
- REMOTE SHUTDOWN
- 11 TO 50V INPUT WITH 5 WATT OUTPUT
- AVAILABLE WITH ±12V OR ±15V OUTPUTS

#### **DESCRIPTION**

The DAC2800D series of DC-DC converters provides the ruggedness reliability and features required to meet the advanced design challenges of today's hi-rel market. This has been accomplished while retaining a power density of 15 W/ in<sup>3</sup> and 375 mW/gram of power/package performance. The use of advanced substrate and reflow soldering techniques during construction results in a rugged, cost-effective, and completely solderable package.

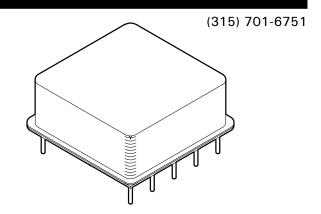
The DAC 2800D hybrid converter series utilizes all ceramic capacitors, surface mount magnetics, and ultrasonically bonded wires to provide reliable operation at all operating temperatures while surviving G forces of up to 500 G.

The DAC2800D series has two fully regulated tracking outputs. Standard features include output fault monitoring and/or turn on voltage point programming via the shutdown pin. All three functions may be implemented simultaneously with a minimum of external components. An output voltage adjustment / load compensation pin which adjusts both outputs simultaneously is also standard.

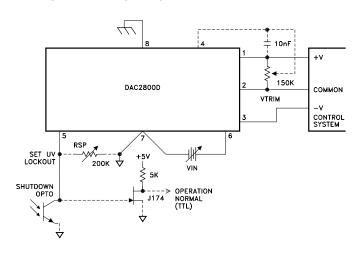
Fault tolerant design protects these converters from most external circuit faults. The + output and output adjust pins will withstand  $+\,25\,\text{V}$  while the shutdown pin will withstand  $+\,50\,\text{V}$ , protecting the converters from a variety of system or board faults e.g. solder bridges etc. Unique load fault protection circuitry allows this converter to pull up loads having difficult static load line characteristics and allows short term load excursions significantly beyond ratings in most applications.

A transformer isolated flyback converter topology operating at a switching frequency of 400 kHz allows operation over a wide input voltage range. Internal filtering of outputs eliminates the need for external capacitors in many applications.

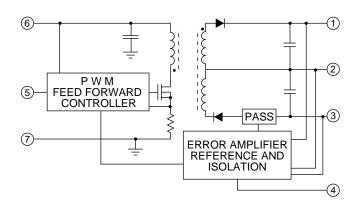
The 8-pin DIP package is hermetically sealed and isolated from the internal circuits. Heat sinking is recommended for full power operation at elevated ambient temperatures.



### TYPICAL APPLICATION



### **BLOCK DIAGRAM**



#### **EXTERNAL CONNECTIONS**

1 +OUTPUT 2 OUTPUT COMMON 8 CASE

3 -OUTPUT

7 -INPUT

4 ADJUST/COMP

5 SHUTDOWN PLUS

6 +INPUT

### DAC2812D ● DAC2815D

## ABSOLUTE MAXIMUM RATINGS SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS

INPUT VOLTAGE RANGE (Pin 7 to 6 or 5) INPUT TRANSIENT (Pin 7 to 6) OUTPUT CURRENT (Continuous) TEMPERATURE, Storage TEMPERATURE, Pin Soldering 10s DAC2812D 50Vdc 80V @ 50ms 360mA<sup>4</sup> -65°C,150°C 300°C DAC2815D 50Vdc 80V @ 50ms 288mA<sup>4</sup> -65°C, 150°C 300°C

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SPECIFICATIONS		DAC2812D			DAC2815D			
PARAMETER	TEST CONDITIONS <sup>1</sup>	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
STEADY STATE CHARACTERISTICS								
INPUT VOLTAGE RANGE	-55°C↔125°C	11	28	50	11	28	50	Vdc
OUTPUT VOLTAGE (+/-) OUTPUT CURRENT -55°C×125°C	$\begin{aligned} &+I_o = -I_o = 10\% \\ &V_{IN} = 16 \leftrightarrow 40 \\ &V_{IN} = 11 \leftrightarrow 50 \end{aligned}$	11.9	12 ±225 ±208	12.1 360 <sup>4</sup> 333 <sup>4</sup>	14.9	15 ±180 ±167	15.1 288 <sup>4</sup> 267 <sup>4</sup>	Vdc mAdc mAdc
OUTPUT POWER -55°C×125°C	$V_{IN} = 16 \leftrightarrow 40$ $V_{IN} = 11 \leftrightarrow 50$			5.4 5			5.4 5	W W
EFFICIENCY LINE REGULATION LOAD REGULATION	$+I_o = -I_o = 100\%$ $V_{IN} = 11 \leftrightarrow 50; +I_o = -I_o = 100\%$	60	64 5		61	65 5		% mV
$+ (+I_0 = 0-160\%)$ $- (-I_0 = 0-160\%)$ OPERATING TEMPERATURE, CASE	$-I_o = 40\%^2 + I_o = 40\%^2$	<b>-55</b>	5 20	125	_55	5 15	125	mV mV °C
TEMPERATURE COEFFICIENT		-55	0.000	125	-55	0.000	125	
(Vout + /-) INPUT RIPPLE CURRENT WITH 1µH SERIES INDUCTANCE	Bandwidth = $10kHz \rightarrow 1MHz$ Bandwidth = $10kHz \rightarrow 1MHz$		0.006 340 45			0.006 340 45		%/°C mArms mArms
OUTPUT RIPPLE VOLTAGE LOAD CAPACITANCE <sup>3</sup> (PER OUTPUT OVER – 55°C×125°C)	Bandwidth = $10\text{kHz} \rightarrow 1\text{MHz}$ $V_{\text{IN}} = 16 \leftrightarrow 40$ $V_{\text{IN}} = 11 \leftrightarrow 50$		15	60 100 50		15	60 100 50	mVrms μF μF
SHORT CIRCUIT DISSIPATION QUIESCENT INPUT CURRENT INHIBITED		0.9	0.5 50 1.25	70 2.5	0.09	0.5 50 1.25	70 2.5	W mA mA
ISOLATION CHARACTERISTICS (INPUT/OUTPUT/CASE)								
LEAKAGE RESISTANCE LEAKAGE CAPACITANCE	500 Vdc 10kHz	100	400	500	100	400	500	MΩ pF
DYNAMIC CHARACTERISTICS LINE STEP RESPONSE	V <sub>IN</sub> = 16 ↔ 40 Vdc							
VOLTAGE CHANGE RECOVERY TIME (99%) WITH 50 µF OUTPUT	VIN TO COO		200 250			200 250		mV μS
CAPACITORS <sup>3</sup> VOLTAGE CHANGE RECOVERY TIME (99%)			150 400			150 400		mV μS
VOLTAGE CHANGE RECOVERY TIME (99%) WITH 50 µF OUTPUT	+ I <sub>o</sub> = −I <sub>o</sub> = 50 ↔ 100%		400 200			400 200		mV μS
CAPACITORS <sup>3</sup> VOLTAGE CHANGE RECOVERY TIME (99%)			300			300 300		mV μS
START-UP OVERSHOOT SHUTDOWN DELAY SHUTDOWN RECOVERY	$V_{IN} = 0 \rightarrow 28 \text{ Vdc}$ $Pin 5 = > 10 \leftrightarrow < 8 \text{ Vdc}$ $Pin 5 = > 8 \leftrightarrow < 10 \text{ Vdc}$		0 250 40	500 60		0 250 40	500 60	mV μS mS

## ABSOLUTE MAXIMUM RATINGS SPECIFICATIONS

### DAC2812D ● DAC2815D

NOTES: 1. Unless otherwise stated  $T_{\rm C}=25\,^{\rm o}$ ,  $V_{\rm IN}=28V$ ,  $-I_{\rm o}=+I_{\rm o}=100\%$ ,

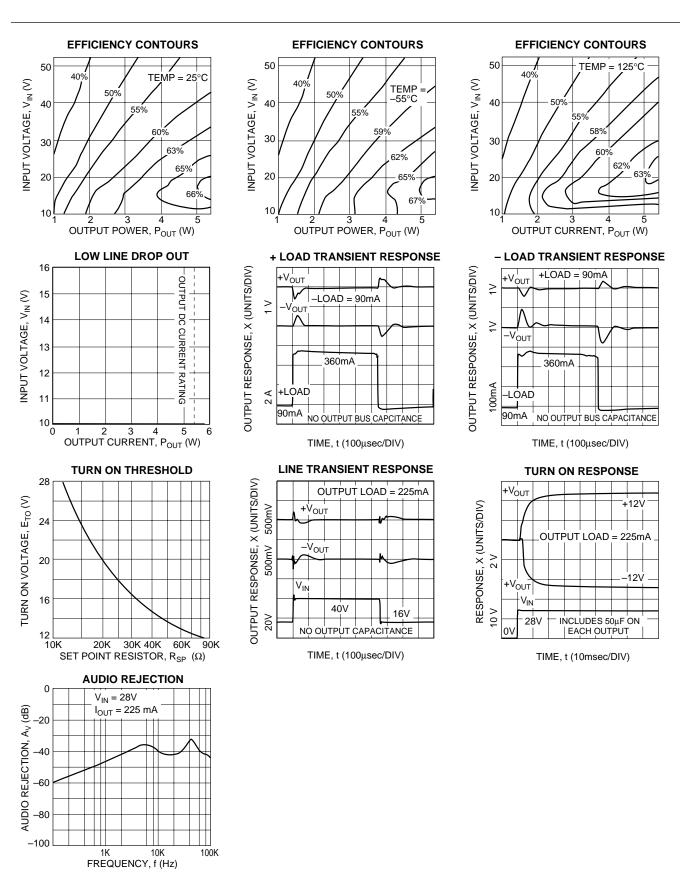
- 2. Regulation measured on output pins 1/16" from case.  $I_0 = 100\%$  means 2.5 or 2.7 watts.
- 3. Capacitors should be connected from + to common and to common only.

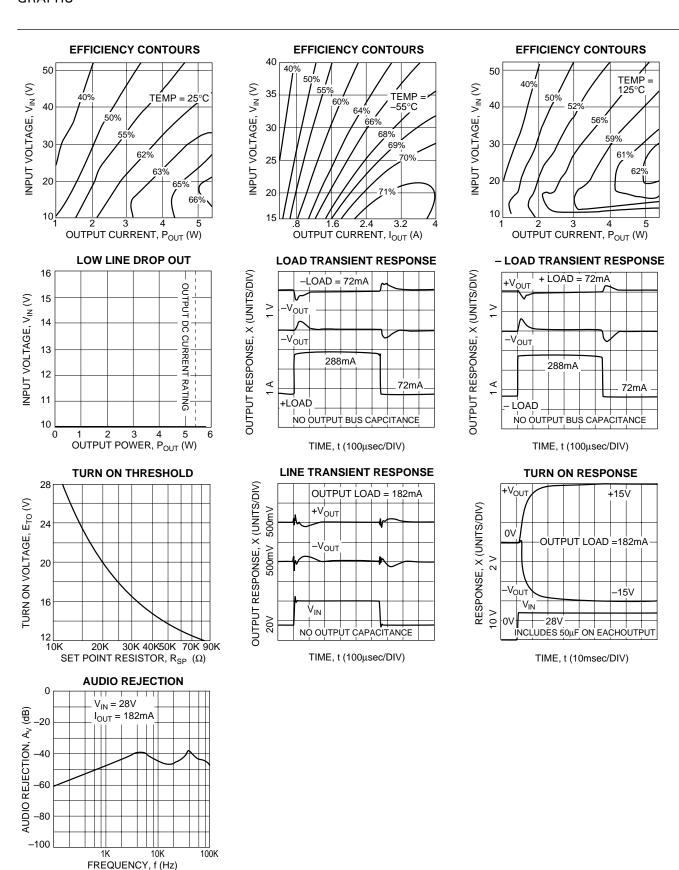
Capacitance value imbalance should not be more than 4:1.

4. Up to 80% of total load may be drawn from either output.

PACKAGE & THERMAL SPECIFICATIONS	MIN	TYP	MAX	UNITS
WEIGHT TEMPERATURE RISE, junction to case TEMPERATURE RISE, ambient		14 17 30	25	GRAMS ℃ °CW

### **DAC2812D**

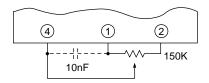




### DAC2812D ● DAC2815D

#### **OUTPUT ADJUST / COMP**

The output voltage of the DAC2800D may be adjusted from 90% to 110% of nominal value by the use of a 150K (potentiometer as shown. Adjustment beyond this range is possible, however certain characteristics of the converter such as but not limited to input voltage range, efficiency, ripple and temperature performance will change. Characterization by the user is recommended in such applications.



Adjust/comp (pin 4) may be driven by external circuitry referenced to pin 2 (-output) if desired. Grounding pin 4 causes voltage to increase (25% typically) while driving pin 4 above 1.3 V causes output voltage to decrease. Pin 4 may be driven negative without damage, however the resultant increase in converter output voltage should be considered. Pin 4 may be driven through 10K( or more if connection of the comp function is also required.

The comp function of pin 4 allows load transient response to be tailored to suit specific application requirements. This feature may be utilized by connecting a 10nF or less capacitor between pins 4 and 1.

#### SHUTDOWN PLUS

Pin 5 is used for remote shutdown, output fault detection, and/or setting the input voltage point at which the converter will turn on as shown in the typical application diagram. No connection to pin 5 is necessary for normal operation of the converter. Pin 5 is referenced to pin 7 (-input).

Shutdown may be implemented by simply connecting pin 5 to an open collector logic output or switch rated at  $2.5\,\text{mA}$ ,  $25\,\text{Vdc}$  or higher.

Input voltage turn on point is programmed with a single resistor from pin 5 to 7. An input turn on/off hysteresis (typically 3.5% of Vin) will be observed. This should be considered when making or verifying set point adjustment. The value of the setpoint resistor may be determined by the following:

$$R = \frac{210 \times 10^{3}}{E_{TO} - 9.5}$$
 (+/- 10% accuracy at 25°C)

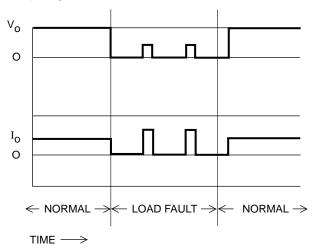
Set point temperature coefficient is typically + 400ppm/OC

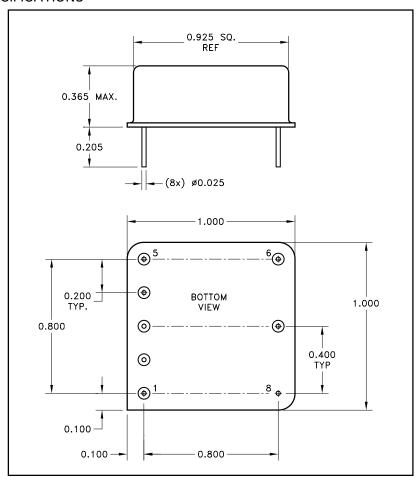
Output fault monitoring is accomplished by observing pin 5 with a high impedance monitoring circuit. Pin 5 voltage drops from over 10 V to below 1 V when a load fault causes the converter's fault protection circuitry to activate. It will remain

low for at least 100 mS and return high. If the load fault is still present pin 5 will return low and the cycle will repeat. A resistor > 400K( from pin 5 to 7 provides pull down for pin 5 if there is no input setpoint programming resistor already in place.

#### LOAD FAULT RESPONSE

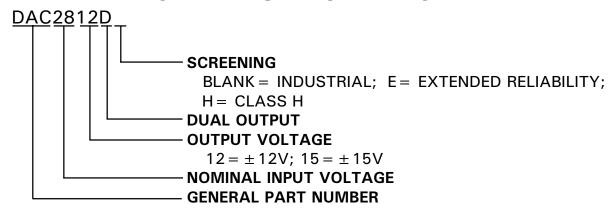
The DAC2800D series of DC-DC converters share load fault philosophies. Load fault conditions include short-circuit and severe overload conditions. The DAC2800D converter series responds to load faults by turning off all power conversion circuits for 250 mS and then attempting to restart for 10 mS (typical). The net "on" duty factor during a fault is very low resulting in low converter dissipation and immunity from overheating at  $125\,^{\circ}\text{C}$ . Current beyond rated can flow into the load at startup time. This allows the converter to bring up capacitive and other difficult load types more reliably than competing converters.





NOTE: ALL DIMENSIONS ARE  $\pm 0.010$  INCHES UNLESS OTHERWISE LABELED. ESD Triangle indicates Pin 1.

### ORDERING INFORMATION



The above example is an industrial grade  $\pm 12V$  dual output converter

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