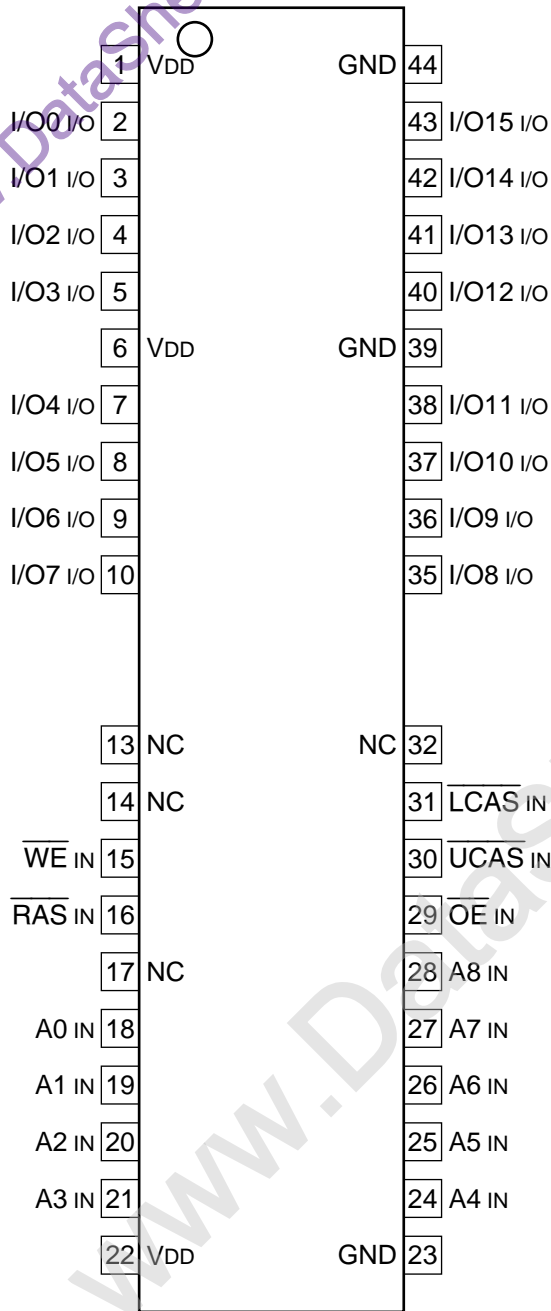


C-MOS 4 M (262,144 W × 16)-BIT DRAM

—TOP VIEW—



INPUT

- A0 - A8 : ADDRESS
- LCAS : COLUMN ADDRESS STROBE
- OE : OUTPUT ENABLE
- RAS : ROW ADDRESS STROBE
- UCAS : COLUMN ADDRESS STROBE
- WE : READ/WRITE ENABLE

INPUT/OUTPUT

I/O0 - I/O15 : DATA