# **ZDELPHI SERIES**



# Delphi Series S48SP, 35W 1x1 Brick DC/DC Power Modules: 48V in, 3.3V/10A out

The Delphi Series S48SP, 1x1 Brick, 48V input, single output, isolated DC/DC converters are the latest offering from a world leader in power systems technology and manufacturing — Delta Electronics, Inc. This product family is available in a surface mount or through-hole package and provides up to 35 watts of power or 10A of output current (3.3V and below) in a new 1x1 form factor (1.30"x0.96"x0.33"). The pinout is compatible with the industry standard 1x2 products. With creative design technology and optimization of component placement, these converters possess outstanding electrical and thermal performance, as well as extremely high reliability under highly stressful operating conditions. Typical efficiency of the 3.3V/10A module is better than 90%. All modules are fully protected from abnormal input/output voltage, current, and temperature conditions.

## **FEATURES**

- High efficiency: 90% @ 3.3V/10A
- Industry standard 1x2 pinout
- Size: 33.0 x 24.4 x 8.3mm (1.30"x0.96"x0.33")
- SMD and Through-hole versions
- Fixed frequency operation
- Input UVLO, OVP
- OTP and output OCP, OVP (default is auto-restart)
- Monotonic startup into normal and pre-biased loads
- 2250V isolation and basic insulation
- No minimum load required
- ISO 9001, TL 9000, ISO 14001, QS9000, OHSAS18001 certified manufacturing facility
- UL/cUL 60950 (US & Canada) recognized, and TUV (EN60950) certified
- CE mark meets 73/23/EEC and 93/68/EEC directive

#### **OPTIONS**

- SMD module available
- Remote On/Off
- Trim pin
- OTP and Output OVP, OCP mode, Auto-restart (default) or latch-up
- Short pin lengths
- Encapsulated case optional

#### **APPLICATIONS**

- Optical Transport
- Data Networking
- Communications, including Wireless and traditional Telecom
- Servers



# **TECHNICAL SPECIFICATIONS**

 $(T_A=25^{\circ}C, airflow \ rate=300 \ LFM, \ V_{in}=48 \ Vdc, \ nominal \ Vout \ unless \ otherwise \ noted.)$ 

PARAMETER	NOTES and CONDITIONS	S485	idard)		
		Min.	Тур.	Max.	Units
ABSOLUTE MAXIMUM RATINGS Input Voltage					
Continuous				80	Vdc
Transient (100ms)	100ms			100	Vdc
Operating Temperature	Refer to Figure 20 for the measuring point	-40		110	°C
Storage Temperature	recief to rigure 20 for the measuring point	-55		125	°C
Input/Output Isolation Voltage				2250	Vdc
INPUT CHARACTERISTICS					
Operating Input Voltage		36		75	Vdc
Input Under-Voltage Lockout		20.5	2.4	25.5	\/- -
Turn-On Voltage Threshold Turn-Off Voltage Threshold		32.5 30.5	34 32	35.5 33.5	Vdc Vdc
Lockout Hysteresis Voltage		1.0	2	3.0	Vdc
Maximum Input Current	100% Load, 36Vin	1.0		1.10	A
No-Load Input Current			40		mA
Off Converter Input Current		· ·	10		mA
Inrush Current (I <sup>2</sup> t)				0.01	A <sup>2</sup> s
Input Reflected-Ripple Current	P-P thru 12µH inductor, 5Hz to 20MHz		10		mA
Input Voltage Ripple Rejection	120 Hz		60		dB
OUTPUT CHARACTERISTICS Output Voltage Set Point	Vin=48V, Io=Io.max, Tc=25°C	3.25	3.3	3.35	Vdc
Output Voltage Set Point Output Voltage Regulation	VIII-40V, IU-IU.IIIdX, IU-20 U	3.23	3.3	3.35	vuc
Over Load	lo=lo, min to lo, max		±3	±10	mV
Over Line	Vin=36V to 75V		±3	±10	mV
Over Temperature	Tc=-40°C to 100°C		±33		mV
Total Output Voltage Range	Over load, line and temperature	3.2		3.4	V
Output Voltage Ripple and Noise	5Hz to 20MHz bandwidth				
Peak-to-Peak	Full Load, 1µF ceramic, 10µF tantalum		30	60	mV
RMS Operating Output Current Range	Full Load, 1μF ceramic, 10μF tantalum	0	10	20 10	mV A
Output DC Current-Limit Inception	Output Voltage 10% Low	110		140	%
DYNAMIC CHARACTERISTICS	Output Voltage 1070 EOW	110		140	70
Output Voltage Current Transient	48V, 10μF Tan & 1μF Ceramic load cap, 0.1A/μs				
Positive Step Change in Output Current	50% Io.max to 75% Io.max	· ·	100		mV
Negative Step Change in Output Current	75% lo.max to 50% lo.max		100		mV
Settling Time (within 1% Vout nominal)			50		us
Turn-On Transient			45		
Start-Up Time, From On/Off Control Start-Up Time, From Input			15 15		ms ms
Maximum Output Capacitance	Full load; 5% overshoot of Vout at startup		13	5000	μF
EFFICIENCY	i dii lodd, o'ii ovololloot ol vodt at olallap			0000	p i
100% Load			90.0		%
60% Load			89.0		%
ISOLATION CHARACTERISTICS					
Input to Output		10		2250	Vdc
Isolation Resistance Isolation Capacitance		10	1000		MΩ pF
FEATURE CHARACTERISTICS			1000		PΓ
Switching Frequency			400		kHz
ON/OFF Control, Negative Remote On/Off logic					
Logic Low (Module On)	Von/off	-0.7		0.8	V
Logic High (Module Off)	Von/off	2		18	V
ON/OFF Control, Positive Remote On/Off logic	V			0.5	
Logic Low (Module Off)	Von/off	-0.7		0.8	V
Logic High (Module On) ON/OFF Current (for both remote on/off logic)	Von/off Ion/off at Von/off=0.0V	2	0.25	18	
Leakage Current (for both remote on/off logic)	Logic High, Von/off=15V		0.25	30	mA uA
Output Voltage Trim Range	Across Trim Pin & +Vo or -Vo, Pout≦max rated	-10%		10%	%
Output Voltage Print Kange Output Over-Voltage Protection	Over full temp range; % of nominal Vout	3.8		4.62	76 V
GENERAL SPECIFICATIONS	- Over fair temp range, 70 or frommar vout	0.0		1.02	
MTBF	lo=80% of lo, max; Ta=25°C; air flow 300LFM		1.98		M hour
Weight			10.5		grams
Over-Temperature Shutdown	Refer to Figure 20 for the measuring point		117		°C

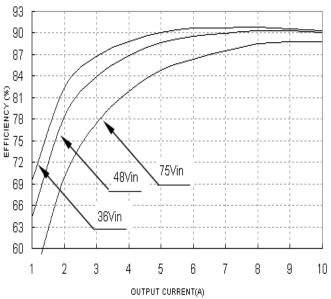


Figure 1: Efficiency vs. load current for minimum, nominal, and maximum input voltage at 25°C

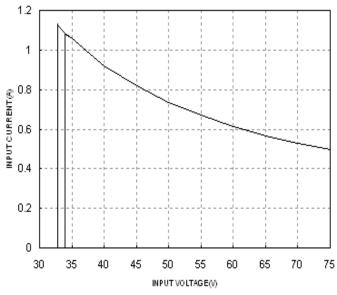


Figure 3: Typical full load input characteristics at room temperature

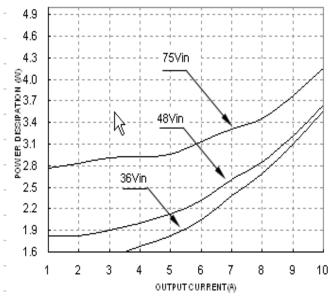
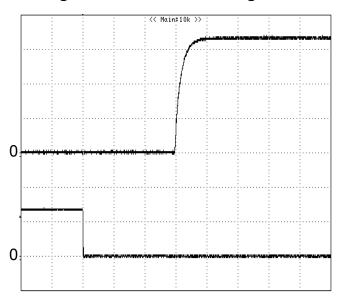


Figure 2: Power dissipation vs. load current for minimum, nominal, and maximum input voltage at 25°C.

## For Negative Remote On/Off Logic

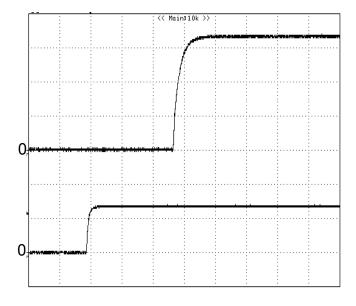


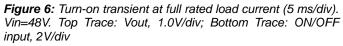
O, Main 10k >>

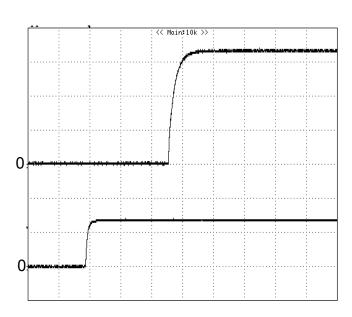
Figure 4: Turn-on transient at full rated load current (5 ms/div). Vin=48V. Top Trace: Vout, 1.0V/div; Bottom Trace: ON/OFF input, 2V/div

Figure 5: Turn-on transient at zero load current (5 ms/div). Vin=48V. Top Trace: Vout, 1.0V/div, Bottom Trace: ON/OFF input, 2V/div

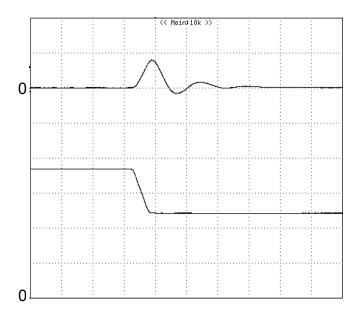
# For Positive Remote On/Off Logic



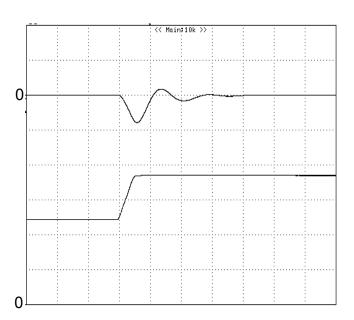




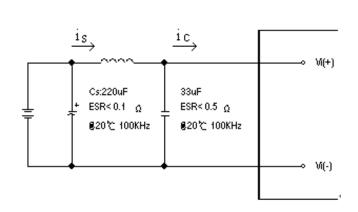
**Figure 7:** Turn-on transient at zero load current (5 ms/div). Vin=48V. Top Trace: Vout, 1.0V/div; Bottom Trace: ON/OFF input, 2V/div



**Figure 8:** Output voltage response to step-change in load current (75%-50% of Io, max; di/dt =  $0.1A/\mu$ s). Load cap:  $10\mu$ F tantalum capacitor and  $1\mu$ F ceramic capacitor. Top Trace: Vout (100mV/div, 50us/div), Bottom Trace: lout (2A/div). Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module

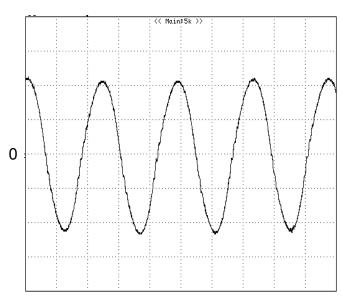


**Figure 9:** Output voltage response to step-change in load current (50%-75% of lo, max; di/dt =  $0.1A/\mu$ s). Load cap:  $10\mu$ F tantalum capacitor and  $1\mu$ F ceramic capacitor. Top Trace: Vout (100mV/div, 50us/div), Bottom Trace: lout (2A/div). Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module

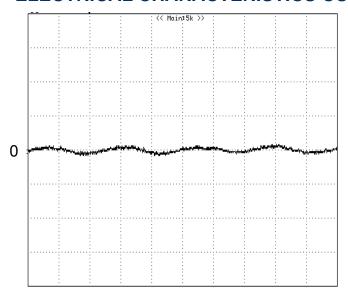


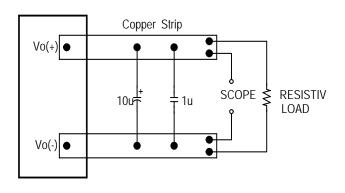
**Figure 10:** Test set-up diagram showing measurement points for Input Terminal Ripple Current and Input Reflected Ripple Current.

Note: Measured input reflected-ripple current with a simulated source Inductance ( $L_{TEST}$ ) of 12  $\mu$ H. Capacitor Cs offset possible battery impedance. Measure current as shown below



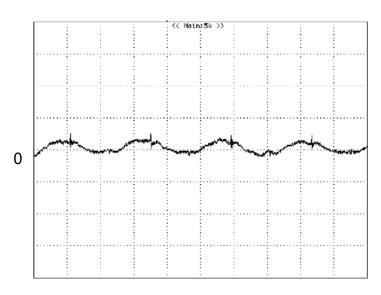
**Figure 11:** Input Terminal Ripple Current, i<sub>c</sub>, at full rated output current and nominal input voltage with 12μH source impedance and 33μF electrolytic capacitor (50 mA/div, 1us/div)

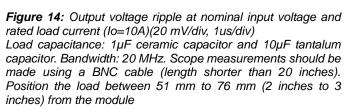




**Figure 12:** Input reflected ripple current,  $i_s$ , through a 12 $\mu$ H source inductor at nominal input voltage and rated load current (20 mA/div, 1us/div)

Figure 13: Output voltage noise and ripple measurement test setup





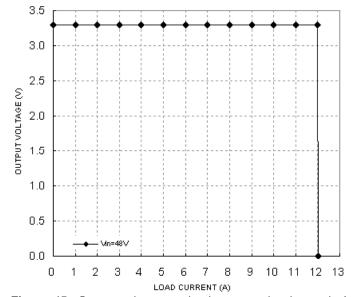


Figure 15: Output voltage vs. load current showing typical current limit curves and converter shutdown points

#### **DESIGN CONSIDERATIONS**

## **Input Source Impedance**

The impedance of the input source connecting to the DC/DC power modules will interact with the modules and affect the stability. A low ac-impedance input source is recommended. If the source inductance is more than a few  $\mu H,$  we advise adding a 10 to 100  $\mu F$  electrolytic capacitor (ESR < 0.7  $\Omega$  at 100 kHz) mounted close to the input of the module to improve the stability.

## **Layout and EMC Considerations**

Delta's DC/DC power modules are designed to operate in a wide variety of systems and applications. For design assistance with EMC compliance and related PWB layout issues, please contact Delta's technical support team. An external input filter module is available for easier EMC compliance design. Application notes to assist designers in addressing these issues are pending release.

#### **Safety Considerations**

The power module must be installed in compliance with the spacing and separation requirements of the end-user's safety agency standard, i.e., UL60950, CAN/CSA-C22.2 No. 60950-00 and EN60950:2000 and IEC60950-1999, if the system in which the power module is to be used must meet safety agency requirements.

Basic insulation based on 75 Vdc input is provided between the input and output of the module for the purpose of applying insulation requirements when the input to this DC-to-DC converter is identified as TNV-2 or SELV. An additional evaluation is needed if the source is other than TNV-2 or SELV.

When the input source is SELV circuit, the power module meets SELV (safety extra-low voltage) requirements. If the input source is a hazardous voltage which is greater than 60 Vdc and less than or equal to 75 Vdc, for the module's output to meet SELV requirements, all of the following must be met:

- The input source must be insulated from the ac mains by reinforced or double insulation.
- The input terminals of the module are not operator accessible.
- If the metal baseplate is grounded, one Vi pin and one Vo pin shall also be grounded.
- A SELV reliability test is conducted on the system where the module is used, in combination with the module, to ensure that under a single fault, hazardous voltage does not appear at the module's output.

When installed into a Class II equipment (without grounding), spacing consideration should be given to the end-use installation, as the spacing between the module and mounting surface have not been evaluated.

The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

This power module is not internally fused. To achieve optimum safety and system protection, an input line fuse is highly recommended. The safety agencies require a fuse with 3A maximum rating to be installed in the ungrounded lead. A lower rated fuse can be used based on the maximum inrush transient energy and maximum input current.

## **Soldering and Cleaning Considerations**

Post solder cleaning is usually the final board assembly process before the board or system undergoes electrical testing. Inadequate cleaning and/or drying may lower the reliability of a power module and severely affect the finished circuit board assembly test. Adequate cleaning and/or drying is especially important for un-encapsulated and/or open frame type power modules. For assistance on appropriate soldering and cleaning procedures, please contact Delta's technical support team.

#### FEATURES DESCRIPTIONS

#### **Over-Current Protection**

The modules include an internal output over-current protection circuit, which will endure current limiting for an unlimited duration during output overload. If the output current exceeds the OCP set point, the modules will automatically shut down, and enter hiccup mode or latch mode, which is optional.

For hiccup mode, the module will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is corrected.

For latch mode, the module will latch off once it shutdown. The latch is reset by either cycling the input power or by toggling the on/off signal for one second.

#### **Over-Voltage Protection**

The modules include an internal output over-voltage protection circuit, which monitors the voltage on the output terminals. If this voltage exceeds the over-voltage set point, the module will shut down, and enter in hiccup mode or latch mode, which is optional.

For hiccup mode, the module will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is corrected.

For latch mode, the module will latch off once it shutdown. The latch is reset by either cycling the input power or by toggling the on/off signal for one second.

#### **Over-Temperature Protection**

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the temperature exceeds the over-temperature threshold the module will shut down, and enter hiccup mode or latch mode, which is optional.

For hiccup mode, the module will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is corrected.

For latch mode, the module will latch off once it shutdown. The latch is reset by either cycling the input power or by toggling the on/off signal for one second.

#### Remote On/Off

The remote on/off feature on the module can be either negative or positive logic. Negative logic turns the module on during a logic low and off during a logic high. Positive logic turns the modules on during a logic high and off during a logic low.

Remote on/off can be controlled by an external switch between the on/off terminal and the Vi(-) terminal. The switch can be an open collector or open drain.

For negative logic if the remote on/off feature is not used, please short the on/off pin to Vi(-). For positive logic if the remote on/off feature is not used, please leave the on/off pin floating.

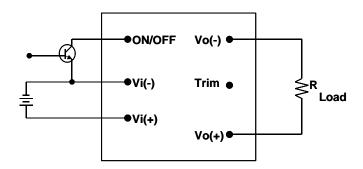


Figure 16: Remote on/off implementation

# FEATURES DESCRIPTIONS (CON.)

#### **Output Voltage Adjustment**

To increase or decrease the output voltage set point, the modules may be connected with an external resistor between the TRIM pin and either the Vo(+) or Vo(-). The TRIM pin should be left open if this feature is not used.

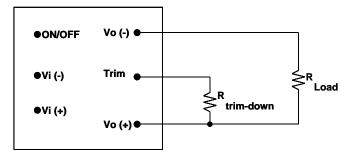


Figure 17: Circuit configuration for trim-down (decrease output voltage)

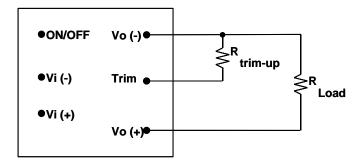
If the external resistor is connected between the TRIM and Vo(+) pins, the output voltage set point decreases (Fig. 17). The external resistor value required to obtain an output voltage change from 3.3V to the desired Vo\_adj is defined as:

Rtrim\_down = 
$$\frac{(\text{Vo}\_\text{adj} - 2.5) \cdot 5110}{3.3 - \text{Vo}\_\text{adj}} - 2050$$

Ex. When Trim-down -10%

Rtrim\_down = 
$$\frac{(2.97 - 2.5) \cdot 5110}{3.3 - 2.97} - 2050$$

Rtrim\_down = 
$$5.228 \times 10^3$$
 ohm



**Figure 18:** Circuit configuration for trim-up (increase output voltage)

If the external resistor is connected between the TRIM and Vo(-) the output voltage set point increases (Fig. 18). The external resistor value required to obtain an output voltage change from 3.3V to the desired Vo\_adj is defined as:

Rtrim\_up = 
$$\frac{2.5 \cdot 5110}{\text{Vo adj} - 3.3} - 2050$$

Ex. When Trim-up +10%

Rtrim\_up = 
$$\frac{2.5 \cdot 5110}{3.63 - 3.3} - 2050$$

Rtrim\_up = 
$$3.666 \times 10^4$$
 ohm

When using trim function, the output voltage of the module is usually increased, which increases the power output of the module with the same output current.

Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

## THERMAL CONSIDERATIONS

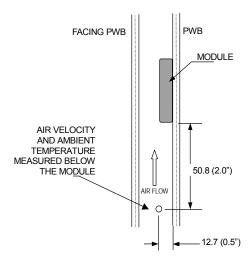
Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

#### Thermal Testing Setup

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module is constantly kept at 6.35mm (0.25").



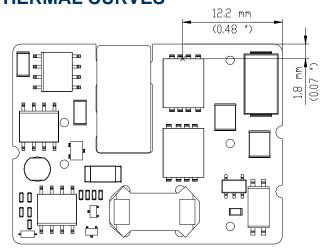
Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inches)

Figure 19: Wind tunnel test setup

#### **Thermal Derating**

Heat can be removed by increasing airflow over the module. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.

## THERMAL CURVES



**Figure 20:** Hot spot temperature measured point \*The allowed maximum hot spot temperature is defined at 110  $\mathcal C$ 

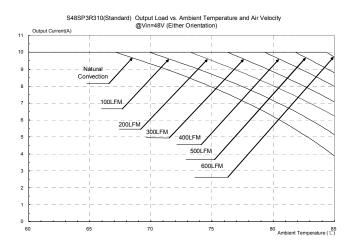
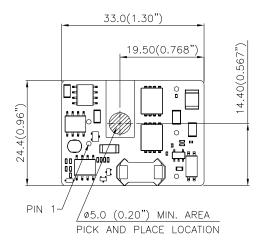


Figure 21: Output load vs. ambient temperature and air velocity @Vin=48V (Either Orientation)

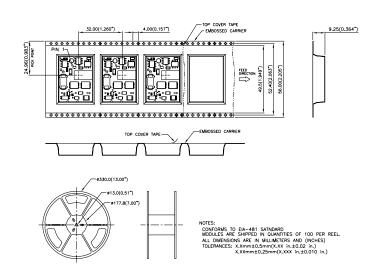
## PICK AND PLACE LOCATION



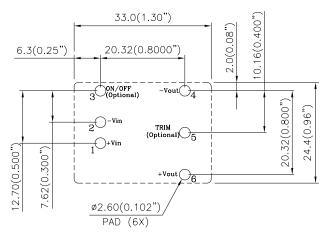
#### NOTES:

DIMENSIONS ARE IN MILLIMETERS AND (INCHES)
TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.)
X.XXmm±0.25mm(X.XXX in.±0.010 in.)

## **SURFACE-MOUNT TAPE & REEL**



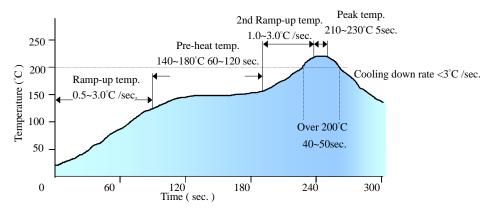
# RECOMMENDED PAD LAYOUT (SMD)



#### RECOMENDED P.W.B PAD LAYOUT

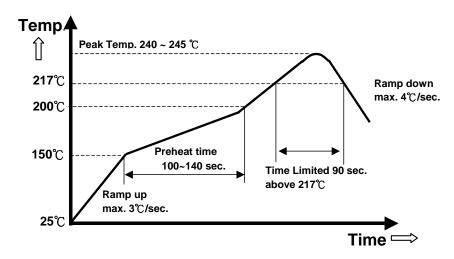
#### NOTES:

# LEADED (Sn/Pb) PROCESS RECOMMEND TEMP. PROFILE



Note: The temperature refers to the pin of S48SP, measured on the pin +Vout joint.

# LEAD FREE (SAC) PROCESS RECOMMEND TEMP. PROFILE

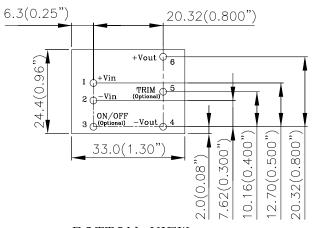


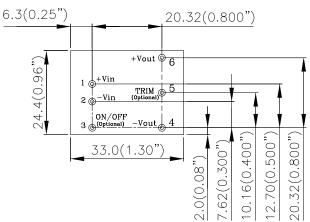
Note: The temperature refers to the pin of S48SP, measured on the pin +Vout joint.

## **MECHANICAL DRAWING**

## Surface-mount module

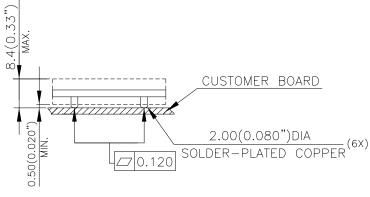
# **Through-Hole module**

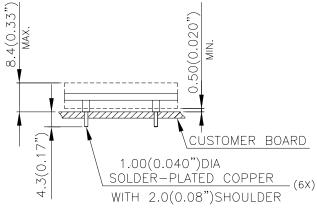




BOTTOM VIEW

BOTTOM VIEW





SIDE VIEW

SIDE VIEW

NOTES:

DIMENSIONS ARE IN MILLIMETERS AND (INCHES) TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.) X.XXmm±0.25mm(X.XXX in.±0.010 in.)

Pin No.	<u>Name</u>	<b>Function</b>
1	+Vin	Positive input voltage
2	-Vin	Negative input voltage
3	ON/OFF (Optional)	Remote ON/OFF (Optional)
4	-Vout	Negative output voltage
5	TRIM (Optional)	Output voltage trim (Optional)
6	+Vout	Positive output voltage

## PART NUMBERING SYSTEM

S	48	S	Р	3R3	10	N	R	F	Α
Product Type	Input Voltage	Number of Outputs	Product Series	Output Voltage	Output Current	ON/OFF Logic	Pin Length/Type		Option Code
S - Small	48-	S - Single	1x1, 10A	3R3 - 3.3V	10 - 10A	N – Negative	R - 0.170"	F- RoHS 6/6	A - No trim pin
Power	36V~75V					(Default)	(Default)	(Lead Free)	(Default)
						P - Positive	N - 0.145"		B - With trim pin
						- No remote	K - 0.110"		
						on/off control	M - SMD		
						function			

#### **MODEL LIST**

MODEL NAME	INPUT		OUTPUT		EFF @ 100% LOAD	
S48SP3R310NRFA	36V~75V	1.1A	3.3V	10A	90.0%	
S48SP05007NRFA	36V~75V	1.2A	5.0V	7A	90.0%	
S48SP12003NRFA	36V~75V	1.2A	12V	3A	90.0%	
S48SP15002NRFA	36V~75V	1A	15V	2A	90.0%	

#### Note:

- 1. Default remote on/off logic is negative;
- 2. Default Pin length is 0.170";
- 3. Default OTP and output OVP, OCP mode is auto-restart;
- 4. Default is no trim pin;
- 5. For different option, please refer to part numbering system above or contact your local sales office.

#### CONTACT: www.delta.com.tw/dcdc

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