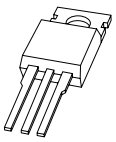


BTA312 series CT and ET

12 A Three-quadrant triacs high commutation high temperature

Rev. 01 — 6 April 2007 Product data sheet

1. Product profile

1.1 General description

Passivated, new generation, high commutation, high temperature triacs in a SOT78 plastic package

1.2 Features

- High operating junction temperature
- Very high commutation performance maximized at each gate sensitivity
- High immunity to dV/dt

1.3 Applications

- High temperature, high power motor control - e.g. vacuum cleaners
- Refrigeration and air conditioning compressors
- Heating and cooking appliances
- Non-linear rectifier-fed motor loads
- Electronic thermostats for heating and cooling loads
- Solid state relays

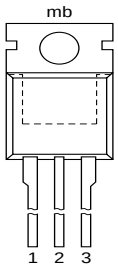
1.4 Quick reference data

- $V_{DRM} \leq 600$ V (BTA312-600CT)
- $V_{DRM} \leq 800$ V (BTA312-800ET)
- $I_{TSM} \leq 95$ A (t = 20 ms)
- $I_{GT} \leq 35$ mA (BTA312-600CT)
- $I_{GT} \leq 10$ mA (BTA312-800ET)
- $I_{T(RMS)} \leq 12$ A

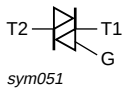
2. Pinning information

Table 1. Pinning

Pin	Description	Simplified outline	Symbol
1	main terminal 1 (T1)		
2	main terminal 2 (T2)		
3	gate (G)		
mb	mounting base; main terminal 2 (T2)		



SOT78 (TO-220AB)



founded by Philips

3. Ordering information

Table 2. Ordering information

Type number	Package		
	Name	Description	Version
BTA312-600CT	SC-46	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78
BTA312-800ET			

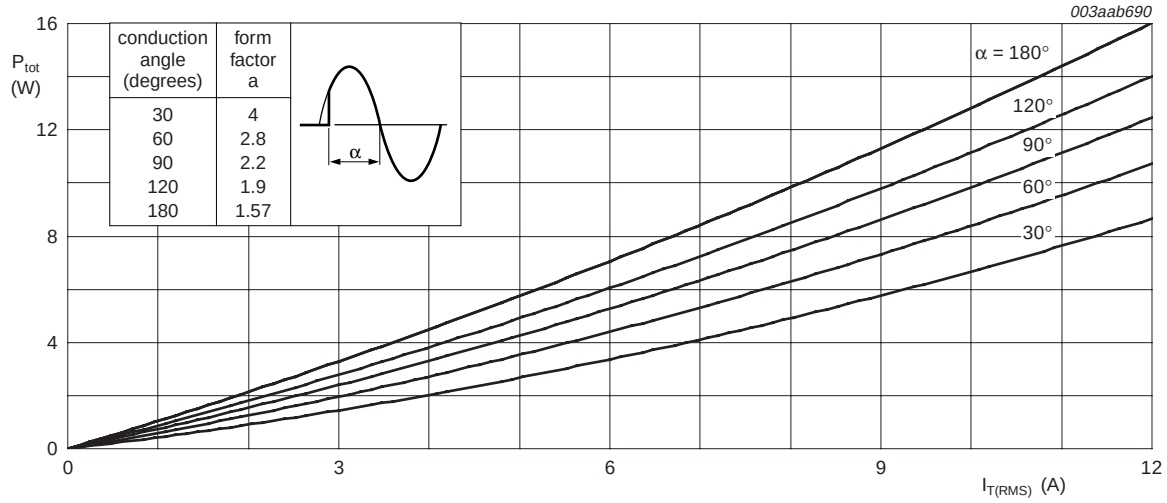
4. Limiting values

Table 3. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

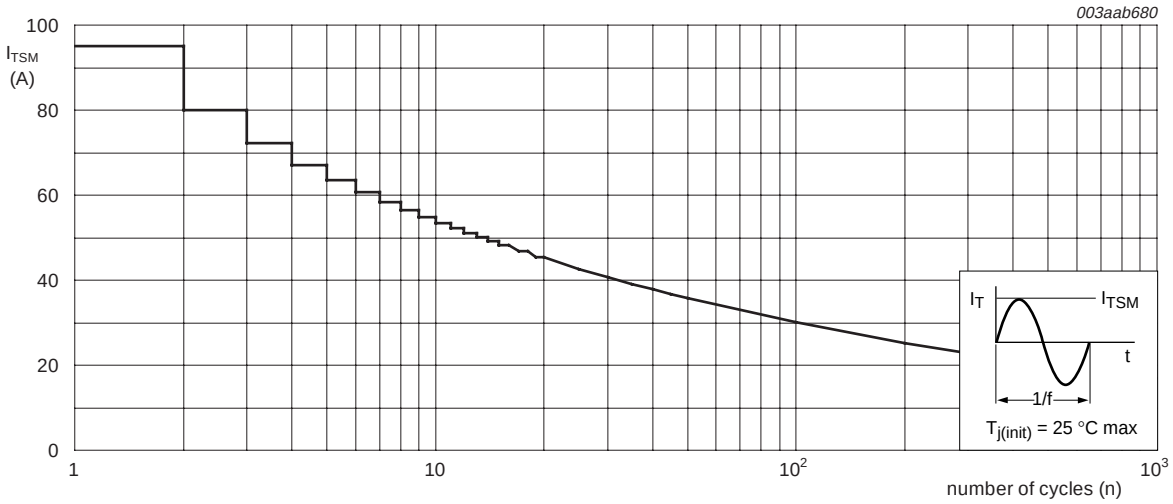
Symbol	Parameter	Conditions		Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage	BTA312-600CT	[1]	-	600	V
		BTA312-800ET		-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 126$ °C; see Figure 4 and 5		-	12	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_j = 25$ °C prior to surge; see Figure 2 and 3				
		t = 20 ms		-	95	A
		t = 16.7 ms		-	105	A
I^2t	I^2t for fusing	t = 10 ms		-	45	A^2s
dI_T/dt	rate of rise of on-state current	$I_{TM} = 20$ A; $I_G = 0.2$ A; $dI_G/dt = 0.2$ A/µs		-	100	A/µs
I_{GM}	peak gate current			-	2	A
P_{GM}	peak gate power			-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period		-	0.5	W
T_{stg}	storage temperature			–40	+150	°C
T_j	junction temperature			-	150	°C

[1] Although not recommended, off-state voltages up to 800 V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/µs.



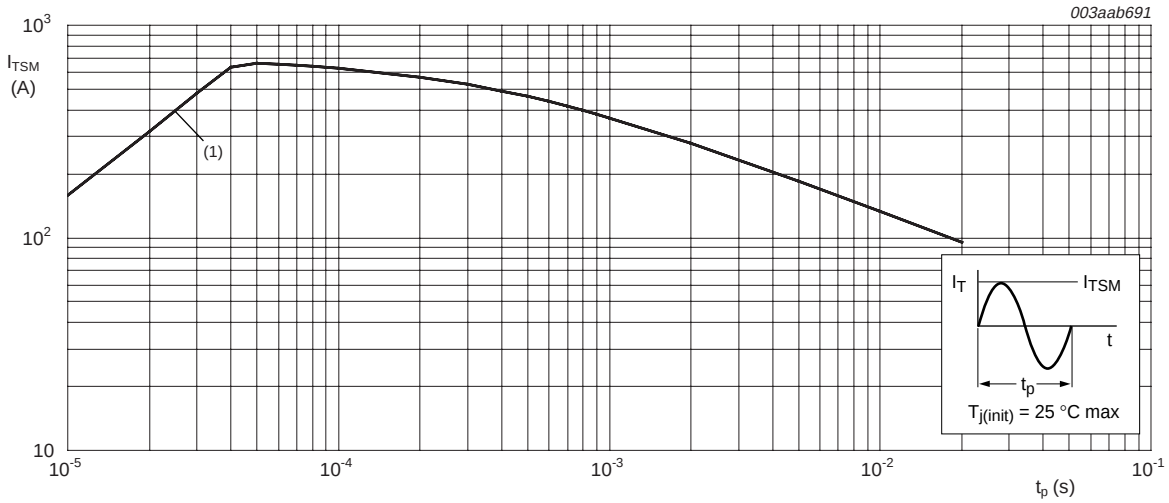
α = conduction angle

Fig 1. Total power dissipation as a function of RMS on-state current; maximum values



f = 50 Hz

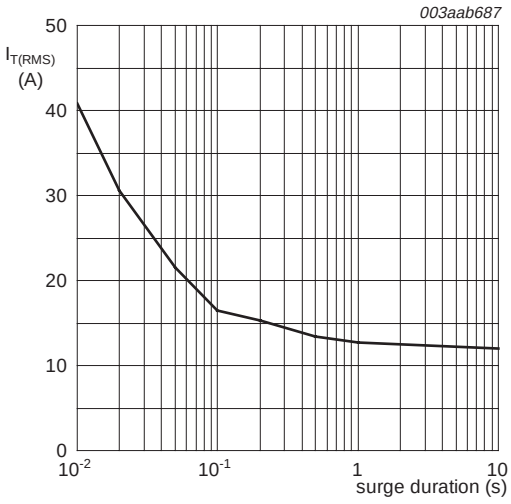
Fig 2. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



$t_p \leq 20$ ms

(1) dI_T/dt limit

Fig 3. Non-repetitive peak on-state current as a function of pulse duration; maximum values



f = 50 Hz

T_{mb} = 126 °C

Fig 4. RMS on-state current as a function of surge duration; maximum values

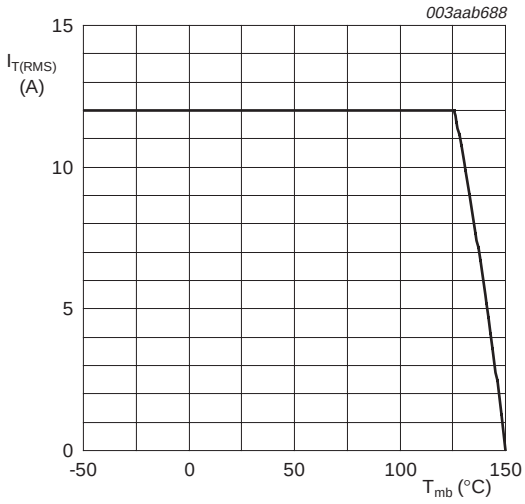
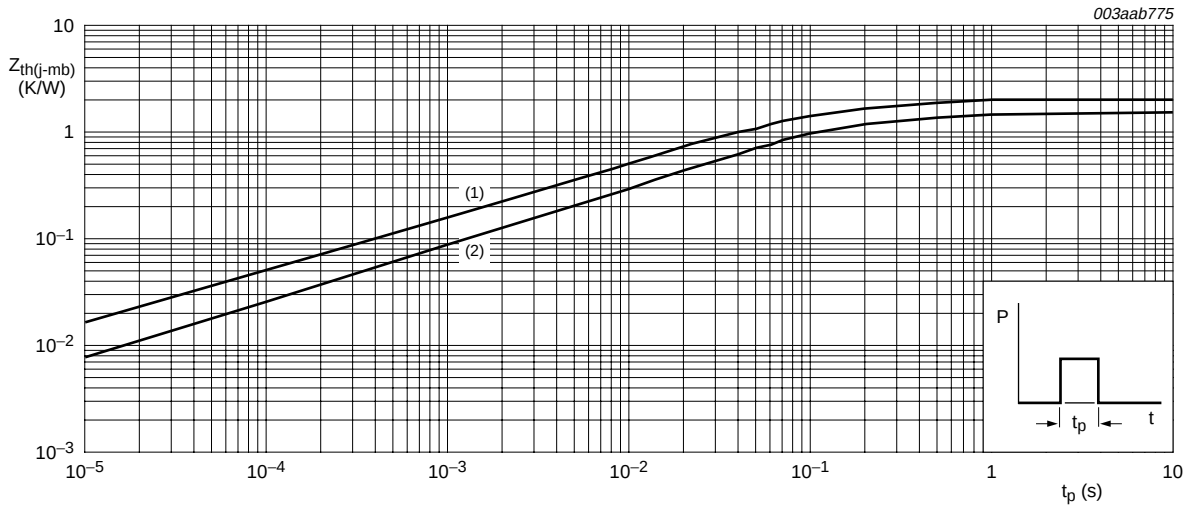


Fig 5. RMS on-state current as a function of mounting base temperature; maximum values

5. Thermal characteristics

Table 4. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	half cycle; see Figure 6	-	-	2.0	K/W
		full cycle; see Figure 6	-	-	1.5	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	60	-	K/W



(1) Unidirectional (half cycle)

(2) Bidirectional (full cycle)

Fig 6. Transient thermal impedance from junction to mounting base as a function of pulse duration

6. Static characteristics

Table 5. Static characteristics

T_j = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	BTA312-600CT			BTA312-800ET			Unit
			Min	Typ	Max	Min	Typ	Max	
I_{GT}	gate trigger current	V_D = 12 V; I_T = 0.1 A; see Figure 8							
		T2+ G+	2	-	35	-	-	10	mA
		T2+ G–	2	-	35	-	-	10	mA
		T2– G–	2	-	35	-	-	10	mA
I_L	latching current	V_D = 12 V; I_{GT} = 0.1 A; see Figure 10							
		T2+ G+	-	-	50	-	-	25	mA
		T2+ G–	-	-	60	-	-	30	mA
		T2– G–	-	-	50	-	-	25	mA
I_H	holding current	V_D = 12 V; I_{GT} = 0.1 A; see Figure 11	-	-	35	-	-	15	mA
V_T	on-state voltage	I_T = 15 A; see Figure 9	-	1.3	1.6	-	1.3	1.6	V
V_{GT}	gate trigger voltage	V_D = 12 V; I_T = 0.1 A; see Figure 7	-	0.8	1.5	-	0.7	1.5	V
		V_D = 400 V; I_T = 0.1 A; T_j = 150 °C	0.25	-	-	0.25	-	-	V
I_D	off-state current	V_D = $V_{DRM(max)}$; T_j = 150 °C	-	0.4	2	-	0.4	2	mA

7. Dynamic characteristics

Table 6. Dynamic characteristics

Symbol	Parameter	Conditions	BTA312-600CT			BTA312-800ET			Unit
			Min	Typ	Max	Min	Typ	Max	
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 0.67 \times V_{DRM(max)}$; T_j = 150 °C; exponential waveform; gate open circuit	300	-	-	30	-	-	V/µs
dI_{com}/dt	rate of change of commutating current	V_{DM} = 400 V; T_j = 150 °C; $I_{T(RMS)}$ = 12 A; without snubber; gate open circuit	8	-	-	2	-	-	A/ms
		V_{DM} = 400 V; T_j = 150 °C; $I_{T(RMS)}$ = 12 A; dV/dt = 10 V/µs; gate open circuit	13	-	-	3.5	-	-	A/ms
		V_{DM} = 400 V; T_j = 150 °C; $I_{T(RMS)}$ = 12 A; dV/dt = 1 V/µs; gate open circuit	20	-	-	5	-	-	A/ms
t_{gt}	gate-controlled turn-on time	I_{TM} = 20 A; $V_D = V_{DRM(max)}$; I_G = 0.1 A; dI_G/dt = 5 A/µs	-	2	-	-	2	-	µs

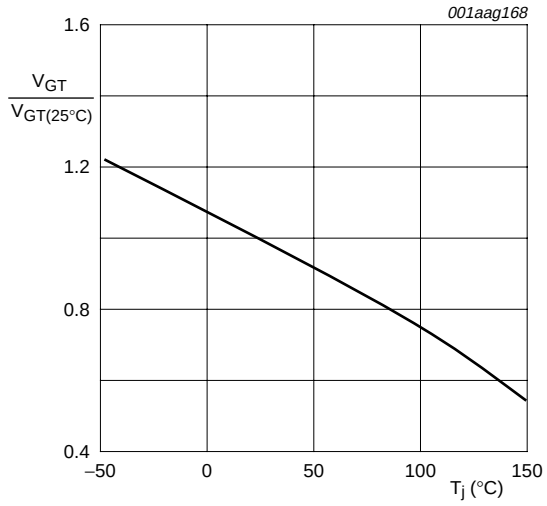
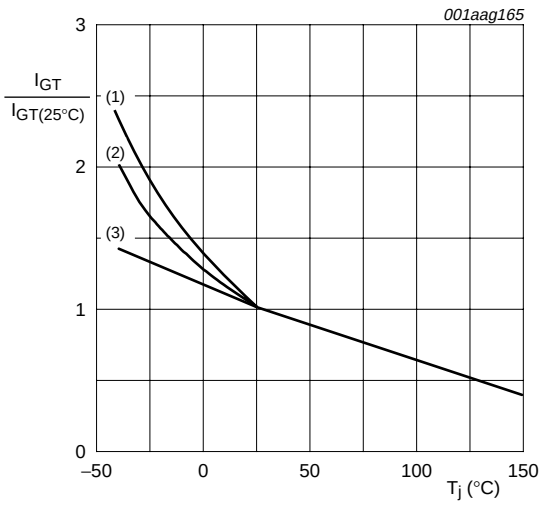
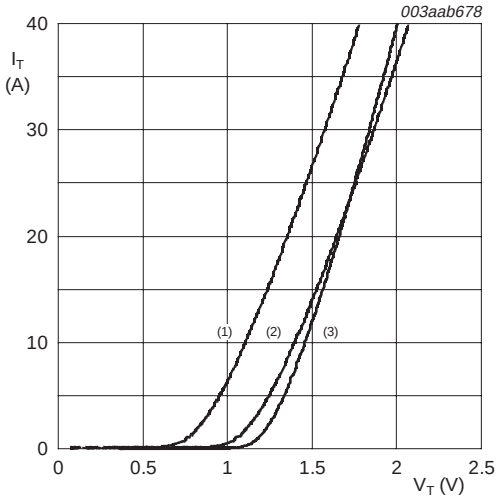


Fig 7. Normalized gate trigger voltage as a function of junction temperature



(1) T2– G–
(2) T2+ G–
(3) T2+ G+

Fig 8. Normalized gate trigger current as a function of junction temperature



V_o = 1.127 V

R_s = 0.027 Ω

(1) T_j = 150 °C; typical values

(2) T_j = 150 °C; maximum values

(3) T_j = 25 °C; maximum values

Fig 9. On-state current as a function of on-state voltage

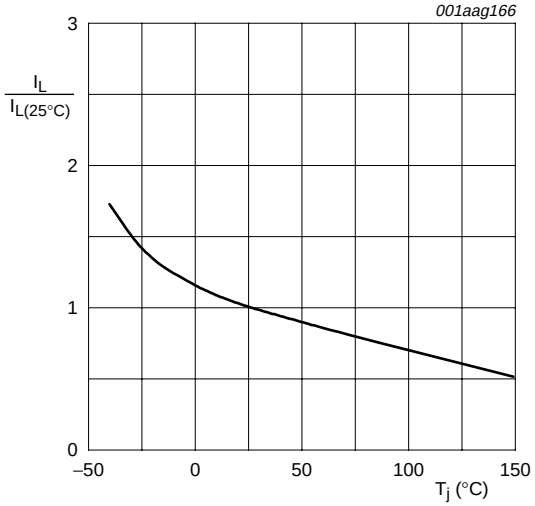


Fig 10. Normalized latching current as a function of junction temperature

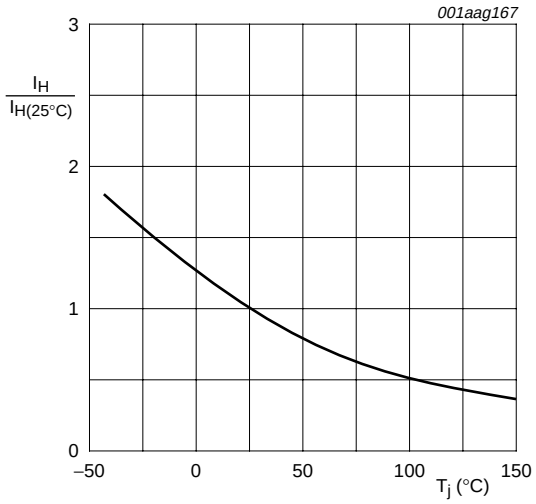
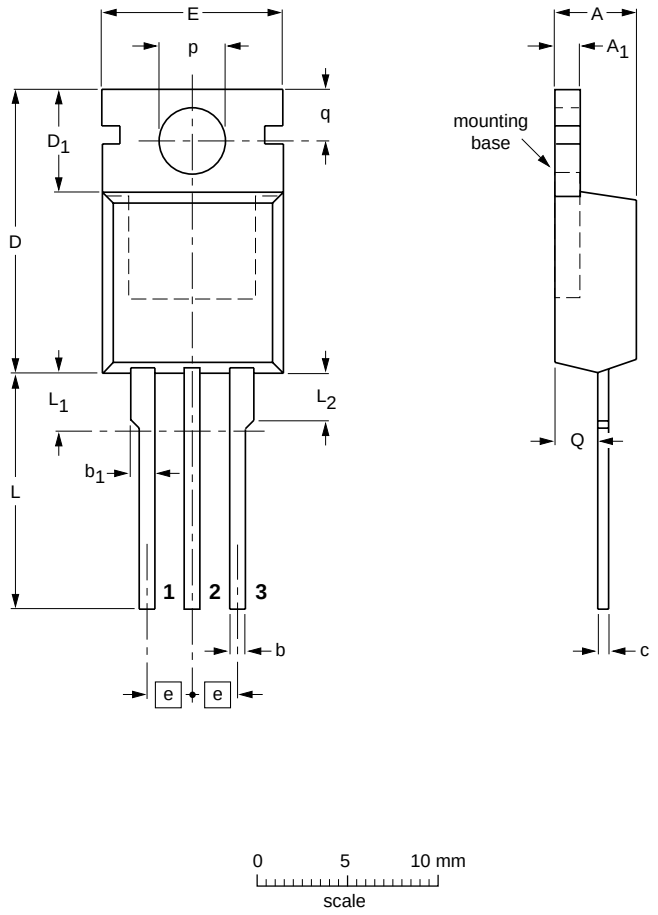


Fig 11. Normalized holding current as a function of junction temperature

8. Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB **SOT78**



DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1	b	b_1	c	D	D_1	E	e	L	L_1	L_2 max.	p	q	Q
mm	4.7 4.1	1.40 1.25	0.9 0.6	1.45 1.00	0.7 0.4	16.0 15.2	6.6 5.9	10.3 9.7	2.54	15.0 12.8	3.30 2.79	3.0	3.8 3.5	3.0 2.7	2.6 2.2

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT78		3-lead TO-220AB	SC-46			~~05-03-22~~ 05-10-25

Fig 12. Package outline SOT78 (3-lead TO-220AB)

9. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BTA312_SER_CT_ET_1	20070406	Product data sheet	-	-

10. Legal information

10.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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