# Low Vce (sat) Digital transistors (with built-in resistors)

# DTD543EE / DTD543EM

#### Applications

Inverter, Interface, Driver

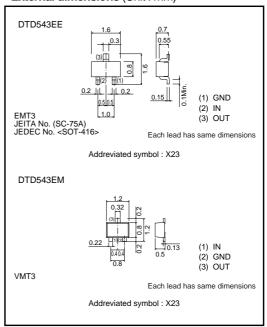
#### Structure

NPN digital transistor (Built-in resistor type)

#### Feature

- 1) VCE (sat) is lower than conventional products.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 4) Only the on / off conditions need to be set for operation, making device design easy.

#### External dimensions (Unit : mm)



#### ◆Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
r arameter	Symbol	DTD543EE DTD543EM	
Supply voltage	Vcc	12	V
Input voltage	Vin	-10 to +12	V
Collector current *1	IC (max)	500	mA
Power dissipation *2	PD	150	mW
Junction temperature	Tj	150	ဗ
Storage temperature	Tstg	-55 to +150	°

<sup>\*1</sup> Characteristics of built-in transistor. \*2 Each terminal mounted on a recommended land.

#### Packaging specifications

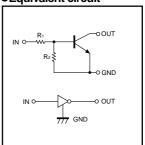
	Package	EMT3	VMT3
	Packaging type	Taping	Taping
	Code	TL	T2L
Part No.	Basic ordering unit (pieces)	3000	8000
DTD543EE		0	-
DTD543EM		-	0

## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	V <sub>I(off)</sub>	-	-	0.5	٧	Vcc=5V, Io=100μA
	V <sub>I(on)</sub>	2.5	-	_		Vo=0.3V, Io=20mA
Output voltage	Vo(on)	-	60	300	mV	Io/I:=100mA / 5mA
Input current	lı	-	-	1.4	mA	Vi= 5V
Output current	IO(off)	-	-	0.5	μΑ	Vcc= 12V, Vi=0V
DC current gain	Gı	115	-	-	-	Vo=2V, Io=100mA
Transition frequency *	fτ	-	260	-	MHz	Vc= 10V, I=-5mA, f=100MHz
Input resistance	R <sub>1</sub>	3.29	4.7	6.11	kΩ	_
Resistance ratio	R2/R1	0.8	1.0	1.2	-	-

<sup>\*</sup> Characteristics of built-in transistor

#### ●Equivalent circuit



 $R_1=4.7k\Omega / R_2=4.7k\Omega$ 

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