# NUF2450MU

# 2 Line EMI Filter with ESD Protection in UDFN8 Package

This device is a 2 line EMI filter array for audio applications. Greater than -30 dB attenuation is obtained at frequencies from 800 MHz to 5.0 GHz. The NUF2450MU has a cut-off frequency of 20 MHz and minimal line resistance, making it ideal for applications requiring low bandpass attenuation. This UDFN package is specifically designed to enhance EMI filtering for low-profile or slim design electronics especially where space and height is a premium. It also offers ESD protection-clamping transients from static discharges. ESD protection is provided across all capacitors.

### Features

- EMI Filtering and ESD Protection
- Integration of 10 Discrete Components
- Compliance with IEC61000-4-2 (Level 4) 20 kV (Contact)
- UDFN Package, 1.2 x 1.8 mm
- Moisture Sensitivity Level 1
- ESD Ratings: Machine Model = C Human Body Model = 3B
- This is a Pb-Free Device\*

### **Benefits**

- Reduces EMI/RFI Emissions on Audio Lines
- Low Profile Package; Typical Height of 0.5 mm
- Design-Friendly and Easy-to-Use Pin Configurations, Particularly for Portable Electronics
- Integrated Solution Offers Cost and Space Savings in UDFN Package
- Reduces Parasitic Inductances Which Offer a More "Ideal" Low Pass Filter Response
- Integrated Solution Improves System Reliability
- Excellent ESD Performance with Large GND Pad

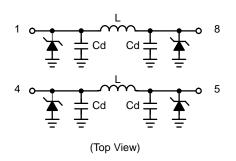
### **Applications**

- Headsets, MP3 Players, and PDAs
- Portable DVDs
- Hands-Free Interface



# ON Semiconductor®

### http://onsemi.com





MARKING DIAGRAM 1 24 M=

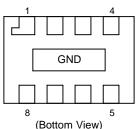
24 = Specific Device Code

M = Month Code

■ = Pb-Free Package

(Note: Microdot may be in either location)

### **PIN CONNECTIONS**



## ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
NUF2450MUT2G	UDFN8 (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

### NUF2450MU

### **MAXIMUM RATINGS**

Parameter	Symbol	Value	Unit
ESD Discharge IEC61000-4-2  Contact Discharge Machine Model Human Body Model	V <sub>PP</sub>	20 1.6 16	kV
Operating Temperature Range	T <sub>OP</sub>	-40 to 85	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	°C
Maximum Lead Temperature for Soldering Purposes (1.8 in from case for 10 s)	T <sub>L</sub>	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

# **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Reverse Working Voltage	$V_{RWM}$		-	-	5.0	V
Breakdown Voltage	$V_{BR}$	I <sub>R</sub> = 1.0 mA	6.0	7.0	8.0	V
Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 3.3 V	-	-	100	nA
Inductance	L		-	2.3	-	nH
Series Resistance	R <sub>S</sub>		0.9	1.3	1.7	Ω
Capacitance (Note 1)	C <sub>line</sub>	V <sub>R</sub> = 0 V, f = 1.0 MHz	190	240	290	pF
Cut-Off Frequency (Note 2)	f <sub>3dB</sub>	Above this frequency, Appreciable Attenuation Occurs	-	20	-	MHz

<sup>1.</sup> Measured at 25°C.

<sup>2.</sup>  $50~\Omega$  source and  $50~\Omega$  load termination.

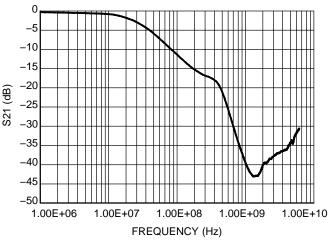


Figure 1. Typical Insertion Loss Characteristics (S21 Measurement)

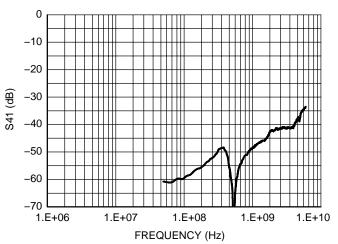
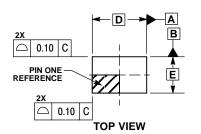


Figure 2. Analog Crosstalk Curve (S41 Measurement)

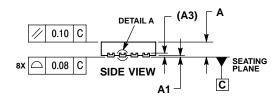
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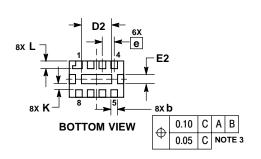
### PACKAGE DIMENSIONS

**UDFN8, 1.8x1.2, 0.4P**CASE 517AD-01
ISSUE O







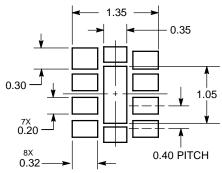


### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
  3. DIMENSION & APPLIES TO PLATED TERMINAL
- DIMENSION b APPLIES TO PLATED TERMINA AND IS MEASURED BETWEEN 0.15 AND 0.20 mm FROM TERMINAL.
- 0.20 mm FROM TERMINAL.
   COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	0.45	0.50	0.55		
A1	0.00	0.03	0.05		
А3	0.127 REF				
b	0.15	0.20	0.25		
D	1.80 BSC				
D2	0.90	1.00	1.10		
Е	1.20 BSC				
E2	0.20	0.30	0.40		
е	0.40 BSC				
K	0.20				
L	0.20	0.25	0.30		

### **SOLDERING FOOTPRINT\***



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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