

## NOTE:

1. UNUSED PINS P7&P8 ARE OMITTED.

## **ELECTRICAL SPECIFICATIONS:**

1.0 TURNS RATIO: (P4-P6) : (J3-J6) : 1CT : 1CT ± 3% (P3-P1) : (J1-J2) : 1CT : 1CT ± 3%

2.0 INDUCTANCE: (P4-P6) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias (P3-P1) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 DC RESISTANCE: (J6-J3)=(J2-J1) : 1.2 ohms Max.



Bel Stewart Connector

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http://www.stewartconnector.com

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SHEET DRAWING NO. SI - 50204 - F

| REV. | 1

4.0 RETURN LOSS:

: 18dB MIN. 1MHz TO 30MHz 60MHz TO 80MHz

NOTE: 100 OHMS CONNECTED TO (P1- P3) AND (P4 - P6).

5.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3) (J3, J6) TO (P4, P6) : 1500 VAC

: 1500 VAC

6.0 INSERTION LOSS: RS=RL=100 ohms

100KHz TO 100MHz : 1.1 dB TYP

7.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS

OUTPUT VOLTAGE = 1 V peak: 3.0 nS MAX PULSE WIDTH= 112nS : 3.0 nS MAX

8.0 CROSS TALK: 1 MHz -100 MHz : 40 dB TYP

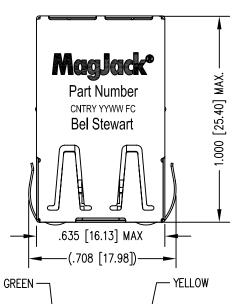
9.0 COMMON TO COMMON MODE ATTENUATION: 30MHz TO 100MHz : 35dB TYP

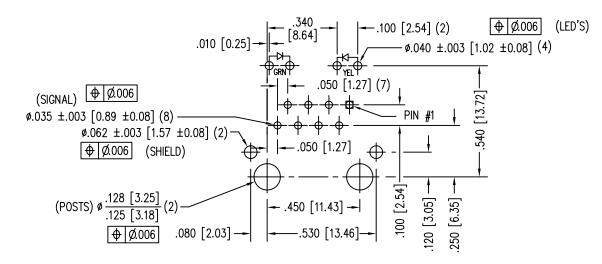
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SHEET

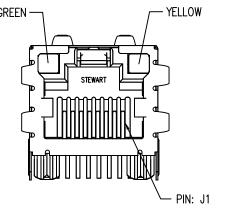


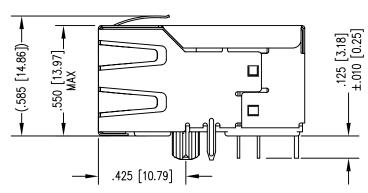


P.C.B. RECOMMENDED HOLE LAYOUT SEEN FROM COMPONENT SIDE

ALL CENTERLINE DIMENSIONS ARE BASIC.







N0	TES:
1.	CON

1. CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: SELECTIVE GOLD,

50 MICRO-INCHES MIN. IN CONTACT AREA.

- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE  $\pm .005$  [0.13]
- 5. REFLOW AND WAVE SOLDER COMPATIBLE -260°C FOR 10 SECONDS MAX.

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	FORWARD V (MAX)	*(TYP)
GREEN	565 nm	2.5 V	2.2 V
YELLOW	590 nm	2.5 V	2.1 V

\*WITH A FORWARD CURRENT OF 20 mA (TYP)

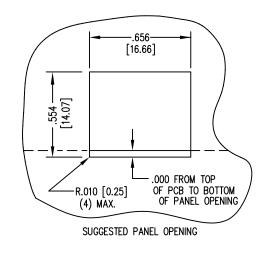
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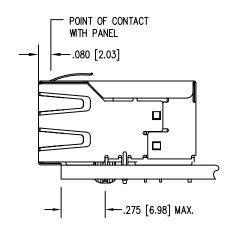
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SHEET 3 OF 4 SI-50204-F REV. 03





- 1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]

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SHEET 4 OF 4 SI-50204-F REV. 03