

## KVR8X72V84-50EG EDO Memory Module

### DESCRIPTION:

This document describes ValueRAM's 8M x 72-bit (64MB) 168-Pin DIMM (Dual In-line Memory Module). The components on this module include nine EDO (Extended Data Output) 8M x 8-bit DRAM (4K Refresh) in TSOP packages and two 16-bit drivers in SSOP packages. This 50ns DIMM is a +3.3 volt design with gold contact fingers. The electrical and mechanical specifications are as follows:

### FEATURES:

- |   |                        |
|---|------------------------|
| <input checked="" type="checkbox"/> Timing Parameter                        | 50ns                   |
| <input checked="" type="checkbox"/> RAS Access Time ( $t_{RAC}$ )           | 50ns                   |
| <input checked="" type="checkbox"/> Column Address Access Time ( $t_{AA}$ ) | 25ns                   |
| <input checked="" type="checkbox"/> Cycle Time ( $t_{RC}$ )                 | 84ns                   |
| <input checked="" type="checkbox"/> Hyper Page Cycle Time ( $t_{HPC}$ )     | 20ns                   |
| <input checked="" type="checkbox"/> Pulse Width ( $t_{RAS}$ )               | 50ns (min.)            |
|   | 10,000ns (max.)        |
| <input checked="" type="checkbox"/> Single Power Supply of                  | +3.3V (+/- 0.3V)       |
| <input checked="" type="checkbox"/> Low Power                               | 4.110 W (operating)    |
|   | 31.5 mW (max. standby) |
| <input checked="" type="checkbox"/> UL Rating                               | 94 V - 0               |

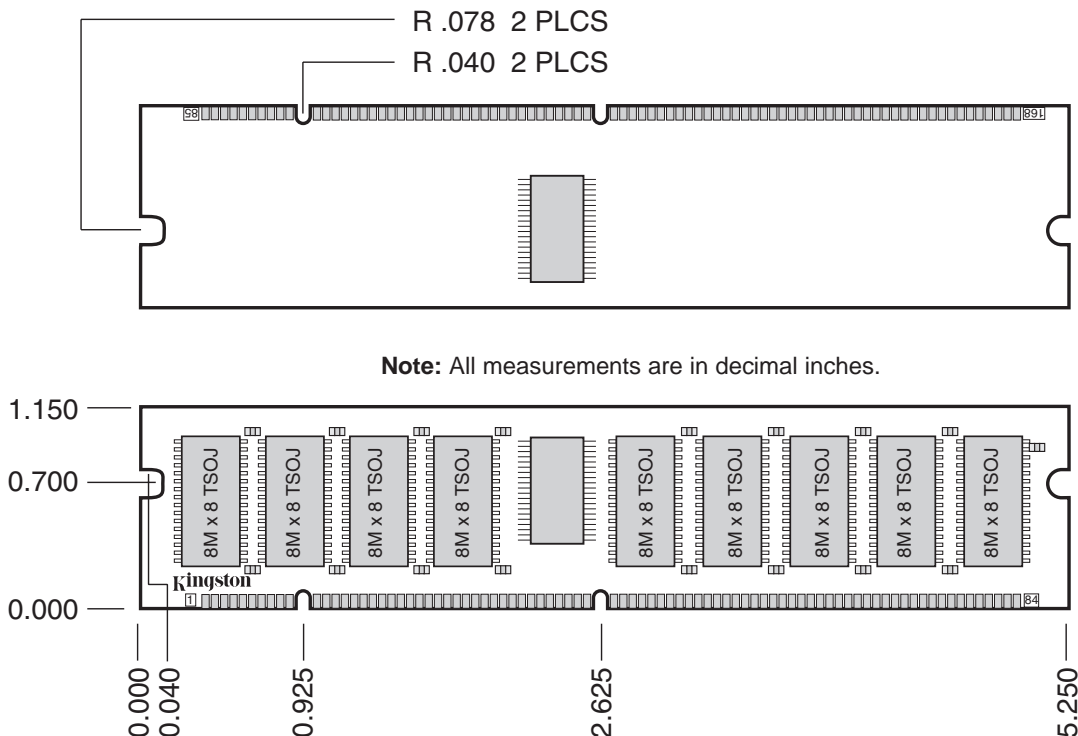


Figure 1: KVR8X72V84-50EG