

SUR532H

Epitaxial planar PNP silicon transistor

Descriptions

• Dual chip digital transistor

Features

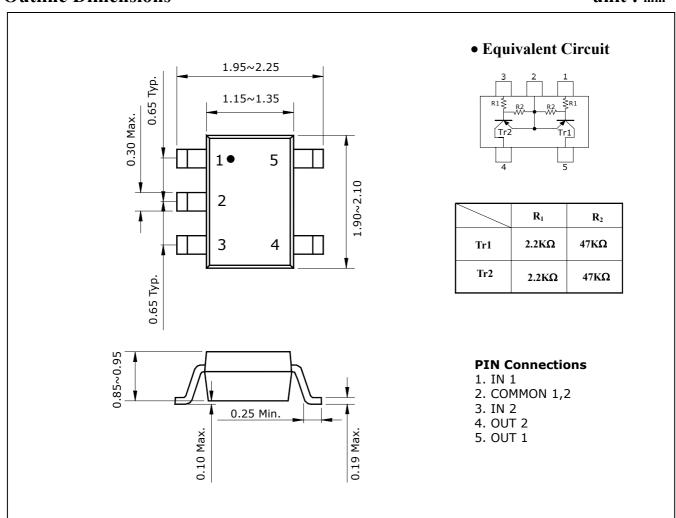
- Two SRA2205 chips in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

Ordering Information

| Type NO. | Marking | Marking Package Code | | |
|----------|---------|----------------------|--|--|
| SUR532H | 32H | SOT-353 | | |

Outline Dimensions





Absolute Maximum Ratings [Tr1,Tr2]

(Ta=25°C)

| Characteristic | Symbol | Rating | Unit |
|---------------------------|-----------------------------|-----------|------|
| Output voltage | Vo | -50 | V |
| Input voltage | $V_{\rm I}$ | -15, 5 | V |
| Output current | I_{O} | -100 | mA |
| Power dissipation | P _D [∗] | 200 | mW |
| Junction temperature | T _J | 150 | °C |
| Storage temperature range | T_{stg} | -55 ~ 150 | °C |

*: Total rating

Electrical Characteristics [Tr1,Tr2]

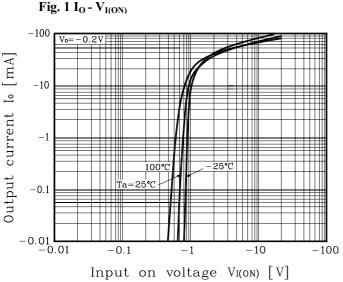
(Ta=25°C)

| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|---------------------------------|--------------------|---|------|------|------|------------|
| Output cut-off current | $I_{O(OFF)}$ | V _O =-50V, V _I =0 | - | - | -500 | nA |
| DC current gain | G_{I} | V _O =-5V, I _O =-10mA | 80 | 200 | - | - |
| Output voltage | V _{O(ON)} | I_O =-10mA, I_I =-0.5mA | - | -0.1 | -0.3 | V |
| Input voltage (ON) | $V_{I(ON)}$ | $V_0 = -0.2V$, $I_0 = -5mA$ | - | - | -1.1 | V |
| Input voltage (OFF) | $V_{I(OFF)}$ | V _O =-5V, I _O =-0.1mA | -0.5 | - | - | V |
| Transition frequency | f_T^* | V_O =-10V, I_O =-5mA, f=1MHz | - | 200 | - | MHz |
| Input current | I_{I} | V_{I} =-5 V , I_{O} =0 | - | - | -3.6 | mA |
| Input resistor (Input to base) | R_1 | - | 1.54 | 2.2 | 2.86 | K Ω |
| Input resistor (Base to common) | R ₂ | - | 33 | 47 | 61 | KΩ |

^{* :} Characteristic of transistor only

Electrical Characteristic Curves

[Tr1,Tr2]



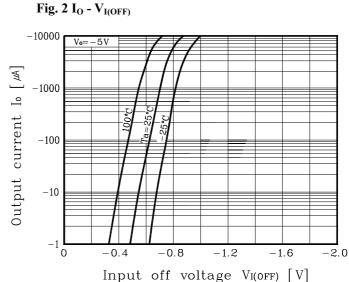
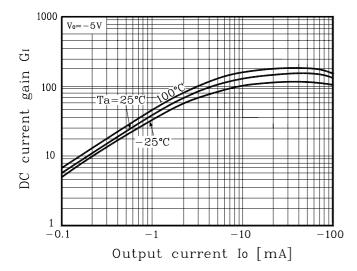


Fig. 3 G₁ - I₀



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