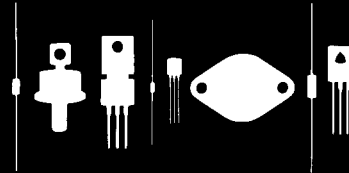


Central
 Semiconductor Corp.
**Central
 Semiconductor Corp.**
**Central™
 Semiconductor Corp.**
 145 Adams Avenue
 Hauppauge, New York 11788



CQ48-25B
 CQ48-25D
 CQ48-25M
 CQ48-25N

25 AMP TRIAC
 200 THRU 800 VOLTS

JEDEC TO-48 CASE

DESCRIPTION

The CENTRAL SEMICONDUCTOR CQ48-25B series types are Power Triacs, utilizing a Glass Passivated Process, mounted in a hermetically sealed metal case, designed for AC control applications featuring gate triggering in all four (4) quadrants.

MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

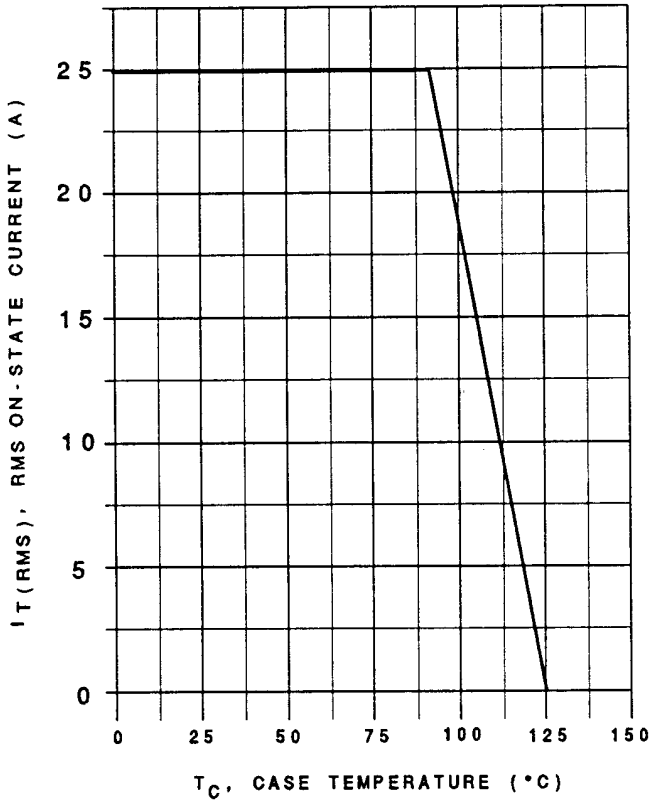
	SYMBOL	CQ48 -25B	CQ48 -25D	CQ48 -25M	CQ48 -25N	UNITS
Peak Repetitive Off-State Voltage	V _{DRM}	200	400	600	800	V
RMS On-State Current (T _C = 90°C)	I _{T(RMS)}			25		A
Peak One Cycle Surge (t = 10ms)	I _{TSM}			250		A
I ² t Value for Fusing (t = 10ms)	I ² t			312.5		A ² s
Peak Gate Power (tp = 10μs)	P _{GM}			40		W
Average Gate Power Dissipation	P _{G(AV)}			1.0		W
Peak Gate Current (tp = 10μs)	I _{GM}			6.0		A
Peak Gate Voltage (tp = 10μs)	V _{GM}			16		V
Critical Rate of Rise of On-State Current						
Repetitive (F = 50Hz)	di/dt			20		A/μs
Storage Temperature	T _{stg}		-40 to +150			°C
Junction Temperature	T _J		-40 to +125			°C
Thermal Resistance	θ _{J-C}			1.2		°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

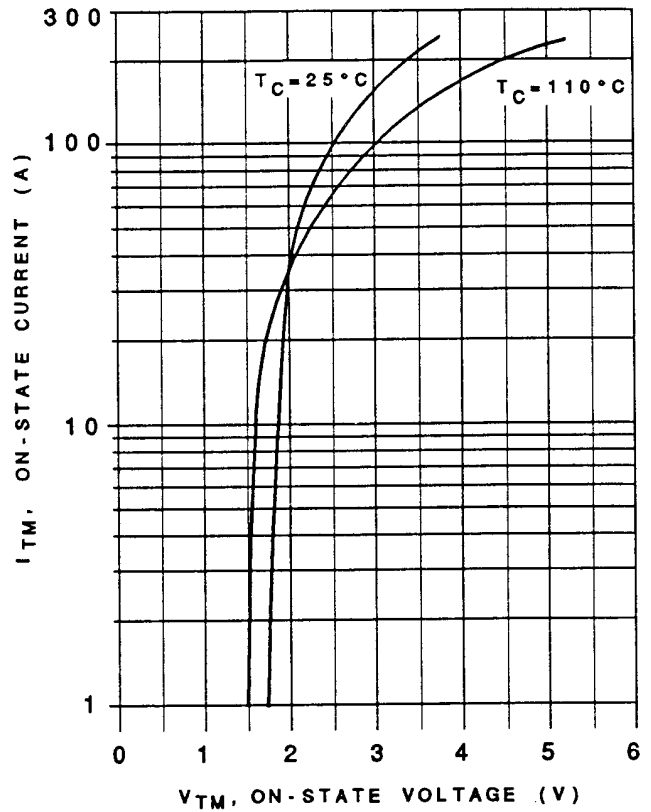
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{DRM}	Rated V _{DRM}			0.02	mA
I _{DRM}	Rated V _{DRM} , T _C = 110°C			5.00	mA
I _{GT}	V _D = 12V, R _L = 33Ω, QUAD I,II,III			100	mA
I _{GT}	V _D = 12V, R _L = 33Ω, QUAD IV			150	mA
I _H	I _T = 500mA			100	mA
V _{GT}	V _D = 12V, R _L = 33Ω, QUAD I,II,III,IV			1.50	V
V _{TM}	I _{TM} = 35A, tp = 10ms			2.00	V
dv/dt	V _D = 2/3 V _{DRM} , R _{GK} = ∞, T _C = 110°C	250			V/μs

CQ48-25B SERIES RATING AND CHARACTERISTIC CURVES

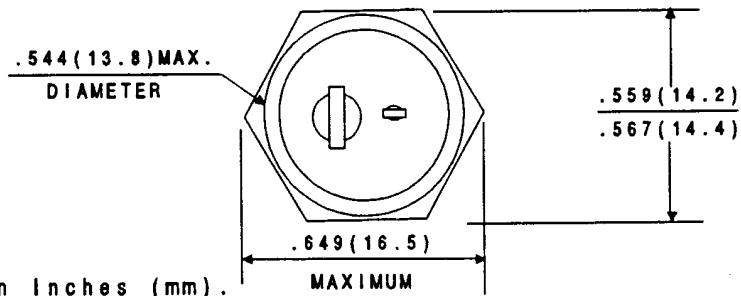
RMS ON-STATE CURRENT vs. CASE TEMPERATURE



MAXIMUM ON-STATE CHARACTERISTICS



MECHANICAL DIMENSIONS



All Dimensions in inches (mm).

