

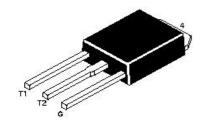
Continental Device India Limited

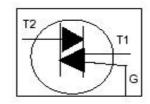
An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company





TRIAC





TO-251 (IPAK) Plastic Package

CJDX136D

For use in high bidirectional transient and blocking voltage applications, and for high thermal cycling performance. Typical Applications include Motor Control, Industrial and Domestic Lighting, Heating and Static Switching.

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITION	VALUE	UNIT
Repetitive Peak Off State Voltage	*V _{DRM}		600	V
RMS on State Current	I _{T (RMS)}	full sine wave, T _{mb} ≤107°C	4.0	Α
Non Repetitive Peak on State Current	, I _{TSM}	full sine wave, T _J =25°C prior to t=20ms t=16.7ms	25 27	A A
I ² t for Fusing	l ² t	t=10ms	3.1	A ² s
Repetitive Rate of Rise of on State Current After Triggering	dl _⊤ /dt	I _{TM} =6A, I _G =0.2A, dI _G /dt=0.2A/μs T2+ G+ T2+ G-	50 50	A/µs A/µs
		T2- G- T2- G+	50 10	A/μs A/μs
Peak Gate Current	I _{GM}		2.0	Α
Peak Gate Voltage	V_{GM}		5.0	V
Peak Gate Power	P_{GM}		5.0	W
Average Gate Power	P _{G (AV)}	Over any 20ms period	0.5	W
Storage Temperature	T _{stg}		- 40 to +150	°C
Operating Junction Temperature	Tj		125	°C

^{*}The rate of rise of current should not excees 3A/ms

THERMAL RESISTANCE

Junction to Mounting Base	R _{th (j-mb)}	full cycle half cycle	3.0 max 3.7 max	K/W K/W
Junction to Ambient (typical)	R _{th (j-a)}	in free air	60 typ	K/W

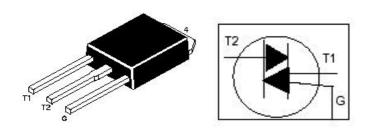
ELECTRICAL CHARACTERISTICS (T_{.J}=25°C unless specified otherwise)

PARAMETER	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Gate Trigger Current	I _{GT}	$V_D = 12V, I_T = 0.1A$			
		T2+ G+		5.0	mΑ
		T2+ G-		5.0	mΑ
		T2- G-		5.0	mΑ
		T2- G+		10	mΑ

MARKING	CJDX	
	136D	
	XY MX	
XY= Date Code		

CJDX136DRev100706E

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ELECTRICAL CHARACTERISTICS (T_J=25°C unless specified otherwise)

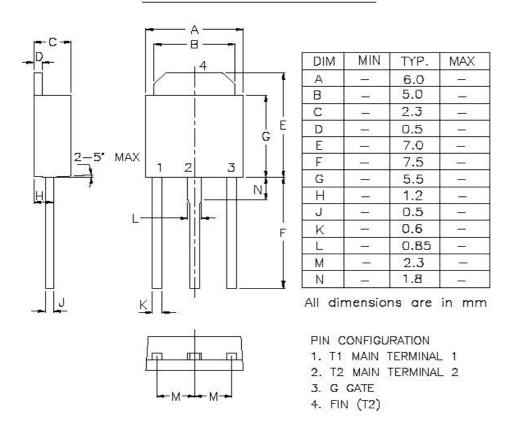
PARAMETER	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Latching Current	ار	V _D =12V, I _{GT} =0.1A			
		T2+ G+		10	mA
		T2+ G-		15	mΑ
		T2- G-		10	mA
		T2- G+		30	mA
Holding Current	I _H	$V_{D}=12V, I_{GT}=0.1A$		10	mA
On State Voltage	V_{T}	I _T =5A		1.7	V
Gate Trigger Voltage	V_{GT}	$V_{D}=12V, I_{T}=0.1A$		1.5	V
		$V_D = 400V, I_T = 0.1A, T_J = 125$ °C	0.25		V
Off State Leakage Current	I _D	V _D =max, V _{DRM} =max, T _J =125°C		0.5	mA

DYNAMIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Critical Rate of Rise of off State Voltage	d _{VD} /dt	V _{DM} =67% V _{DRM} =max, T _J =125°C, exponential waveform, gate open circuit		5.0		V/μs
Gate Controlled turn on time	t _{gt}	I_{TM} =6A, V_D = V_{DRM} max, I_G =0.1A, dI_G / dt =5A/ μ s		2.0		μs

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PACKAGE TO-251 IPAK



PACKING:- 80 Pcs/TUBE

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

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Customer Notes CJDX136D

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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