

# 2.125 Gb/s Integrated 850nm MSM-TIA DATA SHEET - Rev 2.0

## **FEATURES**

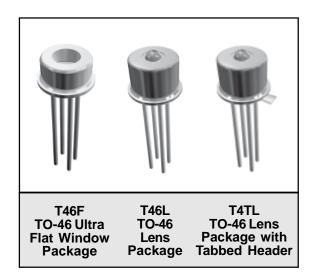
- Differential Output TIA
- 3.3V Operation
- Automatic Gain Control
- Integrated 850nm MSM Detector & TIA
- TO-46 Ultra Flat-Window or Lens Hermetic Package
- High Reliability

### **APPLICATIONS**

2x Fibre Channel (2.125 Gb/s)

## PRODUCT DESCRIPTION

The ANADIGICS AMT8304 is a 3.3V monolithically integrated Metal-Semiconductor-Metal (MSM) photodetector and transimpedance amplifier (TIA) used to convert an 850nm input optical signal into a differential output voltage, and is manufactured in ANADIGICS' 6" GaAs wafer fabrication facility. The integrated MSM and TIA receiver maximizes the receiver performance by minimizing the photodetector input parasitics to the TIA and



internally biasing the photodetector to achieve high sensitivity, bandwidth and overload performance. As an integrated product the reliability is inherently better than a discrete solution, and both the MSM-TIA integrated circuit and TO-46 flat window and TO-46 lens packaged receiver pass stringent reliability requirements. These products are readily designed into receivers and transceivers for 2x Fibre Channel applications.

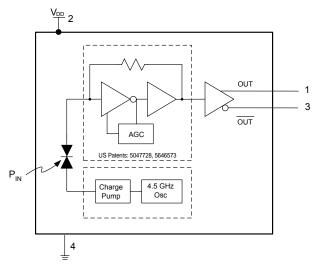


Figure 1: Equivalent Circuit

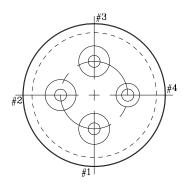


Figure 2: T46F/T46L/T4TL Pin Outline (Bottom View)

**Table 1: Package PIN Description** 

PIN	DESCRIPTION	COMMENT
1	V <sub>OUT</sub> - TIA Output Voltage (non-Inverted)	Logical '1' with optical input
2	V <sub>DD</sub> - Positive Supply Voltage	+3.3 Volts
3	V <sub>OUT</sub> - TIA Output Voltage (Inverted)	Logical '0' with optical input
4	Ground	Case is grounded

# **ELECTRICAL CHARACTERISTICS**

**Table 2: Absolute Maximum Ratings** 

V <sub>DD</sub>	6.0V
P <sub>IN</sub>	+5dBm
T <sub>s</sub>	Storage Temp65 °C to 125 °C

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

**Table 3: Electrical Characteristics** 

PARAMETER	MIN	TYP	MAX	UNIT
Wavelength (λ)	770	850	860	nm
Detector Diameter	-	100	-	μm
Small Signal Differential Responsivity (1) (@ 50 MHz)	1000	1	1	V/W
Bandwidth (1)	1400	1900	-	MHz
Low Frequency Cutoff	1	1	300	KHz
Output Resistance	-	40	-	Ω
Optical Overload (2)	0	-	-	dBm
Optical Sensitivity (2)	-19	-	-	dBm
Differential Output Voltage (3)	-	750	-	mV
$T_{\text{RISE}}$ and $T_{\text{FALL}}$ (20-80%) $^{(3)}$	-	140	-	ps
Duty Cycle Distortion (3)	-	5	-	%
Total Jitter (3),(4)	-	50	-	ps
Supply Current	-	35	55	mA
Operating Voltage Range	+3.0	+3.3	+3.6	Volts
Operating Case Temperature Range	0	-	80	°C

### Notes:

- Measured at -14 dBm optical input power with output connected into R<sub>L</sub> = 100Ω (differential)
   Measured at 10<sup>-10</sup> BER with a 2<sup>7</sup>-1 PRBS at 2.125 Gb/s
   Measured with a 2<sup>7</sup>-1 PRBS at 2.125 Gb/s, an input optical power of -3dBm and R<sub>L</sub> = 100 Ω (differential)
   6σ about the center eye crossing

## **PERFORMANCE DATA**

Figure 3: Eye Diagram with an Optical Input Power of -19dBm (with a 4GHz LPF)

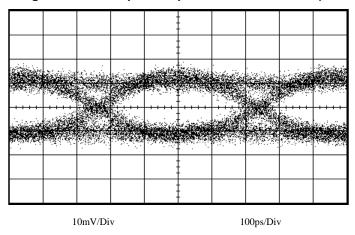


Figure 4: Eye Diagram with an Optical Input Power of -9.0dBm

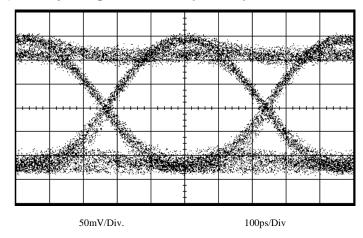


Figure 5: Eye Diagram with an Optical Input Power of 0dBm

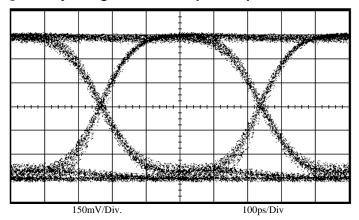


Figure 6: Supply Current vs. Temperature

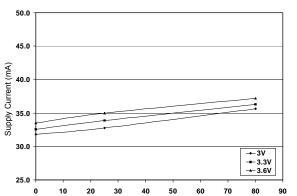


Figure 7: Bandwidth vs. Temperature

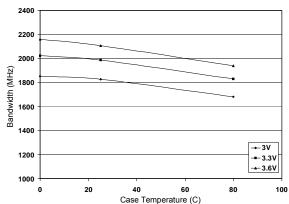


Figure 8: Differential Responsivity vs. Temperature

Case Temperature (C)

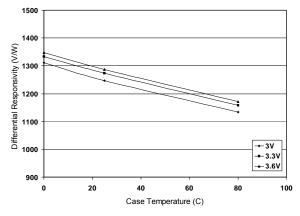
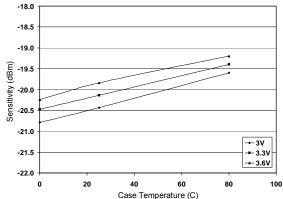


Figure 9: Sensitivity vs. Temperature



## **MEASUREMENT METHODS**

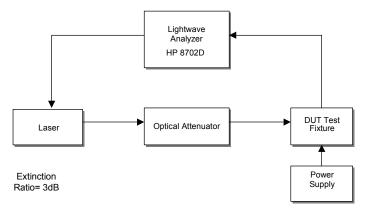


Figure 10: Test Setup for Frequency Response

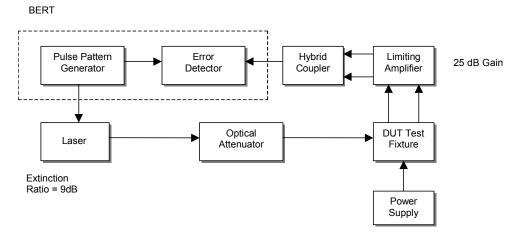


Figure 11: Test Setup for Sensitivity

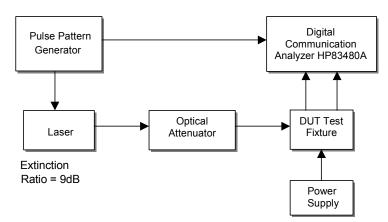


Figure 12: Test Setup for Eye Measurements

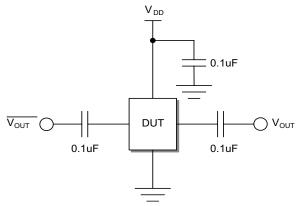


Figure 13: DUT Test Fixture Schematic

# **APPLICATION INFORMATION**

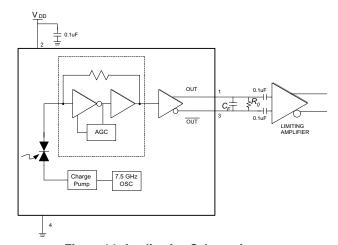


Figure 14: Application Schematic

 $C_{\scriptscriptstyle F}$  is an optional single pole noise filter

$$C_F = \frac{1}{2\pi f_c R}$$

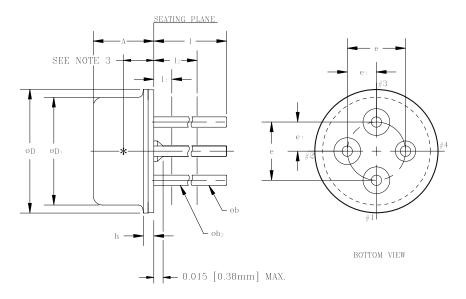
 $f_{\scriptscriptstyle c}$  is the desired cutoff frequency

$$R = 50 \Omega$$

 $R_o$  is required with high input resistance limiting amplifiers

$$R_o = 100 \Omega$$

## PACKAGE OUTLINE



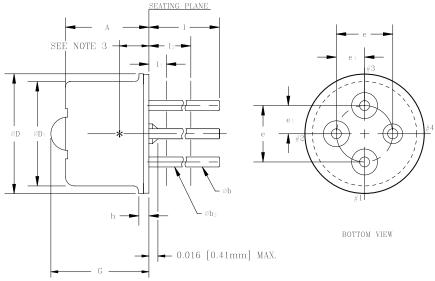
### MM CONTROLLING DIMENSIONS

S <sub>YMBO</sub>	INCHES		MILLIMETERS		NOTE
-0L	MN.	MAX.	MIN.	MAX.	
A	0.098	0.110	2.50	2.80	
øb	0.016	0.020	0.41	0.51	1
øb2	0.012	0.019	0.30	0.48	1
ØD	0.212	0.218	5.38	5.54	
ØD1	0.181	0.187	4.60	4.75	
е	0.100	T.P.	2.54 T.P.		2
e 1	0.050	T.P.	1.27 T.P.		2
h	0.014	0.022	0.36	0.56	
1	0.500	0.540	12.70	13.70	1
1 1	-	0.050	-	1.27	1
12	0.250	-	6.35	-	1

#### NOTES:

- 1. (FOUR LEADS) &bz APPLIES BETWEEN 1: AND 12. Øb APPLIES BETWEEN 1: AND 0.5 [12.70mm] FROM SEATING PLANE. DIAMETER IS UNCONTROLLED IN 1: AND BEYOND 0.5 [12.70mm] TO END OF PIN.
- 2. MAXIMUM DIAMETER LEADS AT A GAGING PLANE 0.054
  [1.37mm]+0.001 [0.025mm] -0.000 [0.000mm] BELOW
  SEATING PLANE TO BE WITHIN 0.007 [0.178mm] OF
  THEIR TRUE POSITION RELATIVE TO MAXIMUM-WIDTH TAB
  AND TO THE MAXIMUM 0.212 [5.40mm] DIAMETER
  MEASURED WITH A SUITABLE GAGE. WHEN GAGE IS
  NOT USED, MEASUREMENT WILL BE MADE AT 0.250
  [6.35mm] FROM SEATING PLANE.
- 3. INTERNAL OPTICAL HEIGHT =  $0.052\pm0.003[1.31\pm.08]$
- 4. BENT LEADS SHOULD NOT EXTEND OUTSIDE DIAMETER ( $\varnothing$ D) OF CAP OR TOUCH EACH OTHER.
- 5. ALL DIMENSIONS ARE REFERENCE ONLY-EXCEPT A, D & h.
- 6. DETECTOR DIODE PLACEMENT ACCURACY  $\bigoplus$  0.15MM[0.006] WITH RESPECT TO CENTER OF HEADER.(REF.ONLY)

Figure 15: T46F Package Outline Diagram



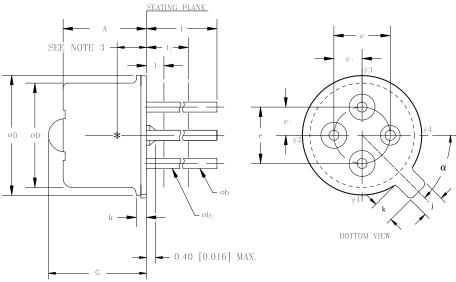
MM CONTROLLING DIMENSIONS

S <sub>YMBO</sub>	INCHES		MILLIMETERS		NOTE
	MIN.	MAX.	MIN.	MAX.	
Α		0.160		4.00	
Øb	0.016	0.020	0.41	0.51	1
Øb2	0.012	0.019	0.30	0.48	1
ØD	0.212	0.218	5.38	5.54	
ØD1	0.181	0.187	4.60	4.75	
е	0.100	) T.P.	2.54 T.P.		2
e <sub>1</sub>	0.050	T.P.	1.27 T.P.		2
h	0.014	0.022	0.36	0.56	
1	0.500	0.540	12.70	13.70	1
1 1	-	0.050	-	1.27	1
12	0.250	-	6.35	-	1
G		0.190		4.66	7

#### NOTES:

- 1. (FOUR LEADS)  $\emptyset b_2$  APPLIES BETWEEN  $l_1$  AND  $l_2$ .  $\emptyset b$  APPLIES BETWEEN  $l_2$  AND 0.5 [12.70mm] FROM SEATING PLANE, DIAMETER IS UNCONTROLLED IN  $l_1$  AND BEYOND 0.5 [12.70mm] TO END OF PIN.
- 2. MAXIMUM DIAMETER LEADS AT A GAGING PLANE 0.054
  [1.37mm]+0.001 [0.025mm] -0.000 [0.000mm] BELOW
  SEATING PLANE TO BE WITHIN 0.007 [0.178mm] OF
  THEIR TRUE POSITION RELATIVE TO MAXIMUM-WIDTH TAB
  AND TO THE MAXIMUM 0.212 [5.40mm] DIAMETER
  MEASURED WITH A SUITABLE GAGE. WHEN GAGE IS
  NOT USED, MEASUREMENT WILL BE MADE AT 0.250
  [6.35mm] FROM SEATING PLANE.
- 3. INTERNAL OPTICAL HEIGHT = 0.052±0.003[1.32±0.08] DIE PLACEMENT ACCURACY FOR REF. ONLY.
- 4. BENT LEADS SHOULD NOT EXTEND OUTSIDE DIAMETER (ØD) OF CAP OR TOUCH EACH OTHER.
- 5. ALL DIMENSIONS ARE REFERENCE ONLY-EXCEPT A, D & h.
- 6. DETECTOR DIODE PLACEMENT ACCURACY  $\mbox{\Large \slash} \pm 0.15 \mbox{mm} [0.006]$  all directions
- 7. LENS HEIGHT =  $0.65\pm0.1$  [ $0.026\pm0.004$ ]

Figure 16: T46L Package Outline Diagram



MM CONTROLLING DIMENSIONS

YMBOL	Marian Millimeters inches		HES	NOTE	
o_	MIN.	MAX.	MIN.	MAX.	
Α		4.00		0.160	
øb	0.41	0.51	0.016	0.020	1
Øb2	0.30	0.48	0.012	0.019	1
ØD	5.38	5.54	0.212	0.218	
ØD1	4.60	4.75	0.181	0.187	
е	2.54 T.P.		0.100 T.P.		2
e <sub>1</sub>	1.27	T.P.	0.050 T.P.		2
h	0.36	0.56	0.014	0.022	
j	0.92	1.12	0.036	0.044	
k	0.81	1.04	0.032	0.041	
1	12.70	13.70	0.500	0.540	1
1 1	-	1.27	-	0.050	1
12	6.35	-	0.250	-	1
G		4.66		0.190	7
α	45° T.P.		45°	T.P.	

#### NOTES:

- 1. (FOUR LEADS) 0b2 APPLIES BETWEEN 1: AND 12. 0b APPLIES BETWEEN 12 AND 12.70 [0.50] FROM SEATING PLANE. DIAMETER IS UNCONTROLLED IN 1: AND BEYOND 12.70 [0.50] TO END OF PIN.
- 2. MAXIMUM DIAMETER LEADS AT A GAGING PLANE 1.37 +0.025/-0.000 [0.054+0.001/-0.000] BELOW SEATING PLANE TO BE WITHIN 0.178 [0.007] OF THEIR TRUE POSITION RELATIVE TO MAXIMUM-WIDTH TAB AND TO THE MAXIMUM 5.40 [0.212] DIAMETER MEASURED WITH A SUITABLE GAGE. WHEN GAGE IS NOT USED, MEASUREMN WILL BE MADE AT 6.35 [0.25] FROM SEATING PLANE.
- 3. INTERNAL OPTICAL HEIGHT = 1.32 $\pm$ 0.08 [0.052 $\pm$ 0.003] DIE PLACEMENT ACCURACY FOR REF. ONLY.
- 4. BENT LEADS SHOULD NOT EXTEND OUTSIDE DIAMETER ( $\odot$ D) OF CAP OR TOUCH EACH OTHER.
- 5. ALL DIMENSIONS ARE REFERENCE ONLY-EXCEPT A, D & h.
- 6. DETECTOR DIODE PLACEMENT ACCURACY  $\bigoplus$  0.15 [0.006] all directions
- 7. LENS HEIGHT =  $0.65\pm0.1$  [ $0.026\pm0.004$ ]

Figure 17: T4TL Package Outline Diagram

**NOTES** 

### ORDERING INFORMATION

ORDER NUMBER	PACKAGE OPTION	PACKAGE DESRIPTION	
AMT8304T46F	TO-46F	Ultra Flat Window Package	
AMT8304T46L	TO-46L	Lens Package	
AMT8304T4TL	TO-46L	Lens Package with Tabbed Header	

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