TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (Ultra High speed U-MOSIII)

TPCA8005-H

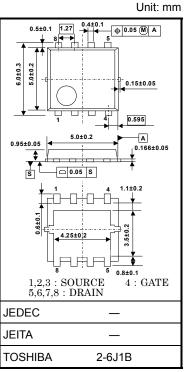
TENTATIVE

High Speed and High Efficiency DC-DC Converters Notebook PC Applications Portable Equipment Applications

- Small footprint due to small and thin package
- High speed switching
- Small gate charge: $Q_g = 24 \text{ nC (typ.)}$
- Low drain-source ON resistance: RDS (ON) = $6.8 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 46S$ (typ.)
- Low leakage current: $I_{DSS} = 10 \mu A (max) (V_{DS} = 30 \text{ V})$
- Enhancement mode: $V_{th} = 1.1 \text{ to } 2.3 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

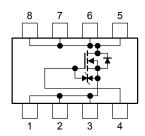
Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Drain-gate voltage (R	$k_{GS} = 20 \text{ k}\Omega$	V_{DGR}	30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	ID	30	Α	
Drain current	Pulsed (Note 1)	I_{DP}	90	A	
Drain power dissipati	on (Tc=25°C)	P_{D}	45	W	
Drain power dissipati	on $(t = 10 s)$ (Note 2a)	PD	2.8	W	
Drain power dissipati	on (t = 10 s) (Note 2b)	P _D	1.6	W	
Single pulse avalanch	ne energy (Note 3)	EAS	117	mJ	
Avalanche current		I _{AR}	30	Α	
Repetitive avalanche	energy c=25°C) (Note 4)	EAR	4.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	-55 to 150	°C	



Weight: 0.080 g (typ.)

Circuit Configuration



Note: For (Note 1), (Note 2), (Note 3), (Note 4), please refer to the next page.

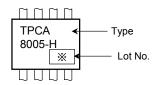
This transistor is an electrostatic sensitive device. Please handle with caution.

Thermal Characteristics

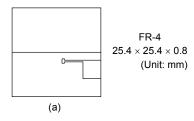
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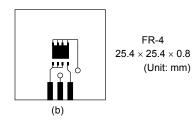
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25°C)	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient $(t=10\;\text{s}) \tag{Note 2b}$	R _{th (ch-a)}	78.1	°C/W

Marking (Note 5)

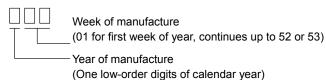


- Note 1: Please use devices on condition that the channel temperature is below 150°C.
- Note 2: (a) Device mounted on a glass-epoxy board (a)
- (b) Device mounted on a glass-epoxy board (b)





- Note 3: $V_{DD}=24~V, \quad T_{ch}=25^{\circ}C$ (initial), $L=0.1~mH, \quad R_{G}=25~\Omega, \quad I_{AR}=30~A$
- Note 4: Repetitive rating: pulse width limited by max channel temperature
- Note 5: * Weekly code: (Three digits)



Electrical Characteristics (Ta = 25°C)

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Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$		_	±10	μΑ	
Drain cut-OFF cu	rrent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μА	
Drain source bre	akdown voltage	V _{(BR) DSS}	$I_D = 10$ mA, $V_{GS} = 0$ V	30	_	_	V	
Drain-source breakdown voltage		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	v	
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.1	_	2.3	V	
Drain-source ON	rociotopo	Dec (c)	$V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$	_	9.5	13	- mΩ	
Drain-source ON	resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 15 A	_	6.8	9		
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 15 A	23	46	_	S	
Input capacitance		C _{iss}		_	1395	_		
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	140	_	pF	
Output capacitance		Coss		_	525	_		
	Rise time	t _r	ACS 10 A D = 12 Y	_	3	_	ns ns	
Cuitabina tima	Turn-ON time	t _{on}		_	9	_		
Switching time	Fall time	t _f		_	8	_		
	Turn-OFF time	t _{off}	$V_{DD} \simeq 15 \text{ V}$ Duty \leq 1%, $t_W = 10 \mu\text{s}$	_	27	_		
Total gate charge (gate-source plus gate-drain)			$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$	_	24	_		
		Q_g	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 30 \text{ A}$	_	13	_		
Gate-source charge 1		Q _{gs1}		_	4.7	_	nC	
Gate-drain ("miller") charge		Q _{gd}	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$		5.6			
Gate switch charge		Q _{SW}]	_	7.7			

Source-Drain Ratings and Characteristics ($Ta = 25^{\circ}C$)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	90	Α
Forward voltage (diode)			V_{DSF}	$I_{DR} = 30 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V

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