SPECIFICATION FOR LCD MODULE

Model No. <u>TM10656ACIG</u>

Prepared by: Date: Checked by: Date: Verified by: Date: Approved by: Date:

TIANMA MICROELECTRONICS CO., LTD

Ver.1.0

REVISION RECORD

Date	Ref. Page	Revision No.	Revision Items	Check & Approval

1. General Specifications:

1.1 Display type: FSTN

1.2 Display color*:

Display color: Blue-Black

Background: White

1.3 Polarizer mode: Transflective/Positive

1.4 Viewing Angle: 6:00

1.5 Driving Method: 1/65 Duty 1/9 Bias

1.6 Lcd operation voltage: 9.5V Vdd=3.0V

1.7 Without Backlight

1.8 Controller: S6B0724A01-B0C

1.9 Data Transfer: Serial

1.10 Operating Temperature: $0---+50^{\circ}$ C

Storage Temperature: $-20---+60^{\circ}$ C

1.11 Outline Dimensions: Refer to outline drawing on next page

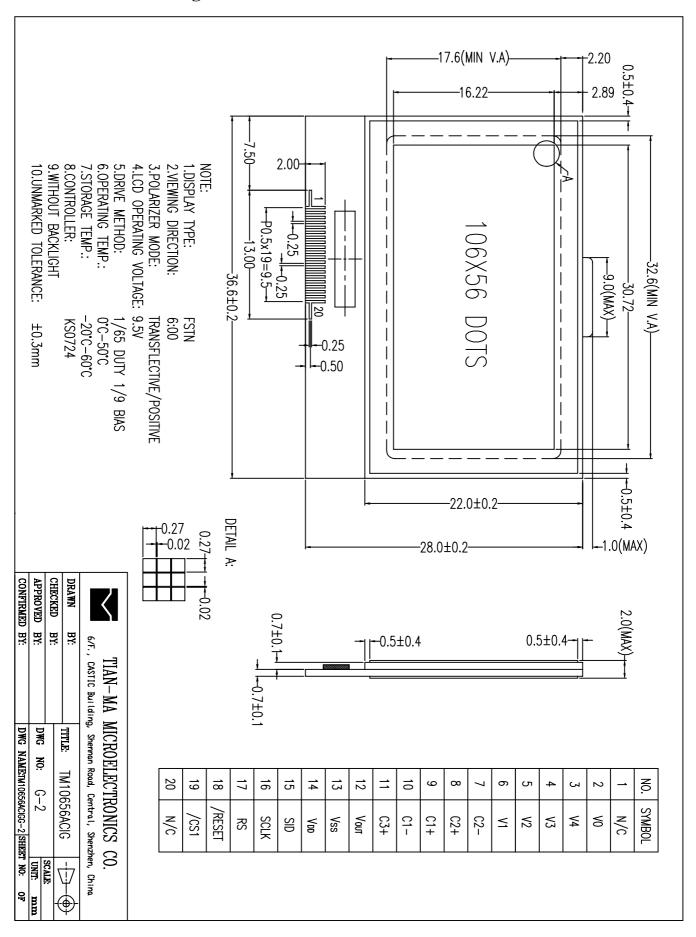
1.12 Dot Matrix: 106 X 56

1.13 Dot Size: 0.27X0.27(mm) 1.14 Dot Pitch: 0.29X0.29 (mm)

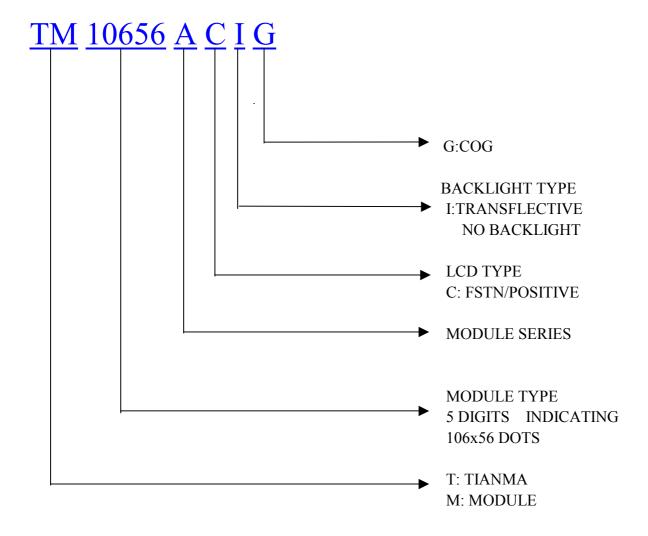
1.15 Weight: 20g

^{*} Color tone is slightly changed by temperature and driving voltage.

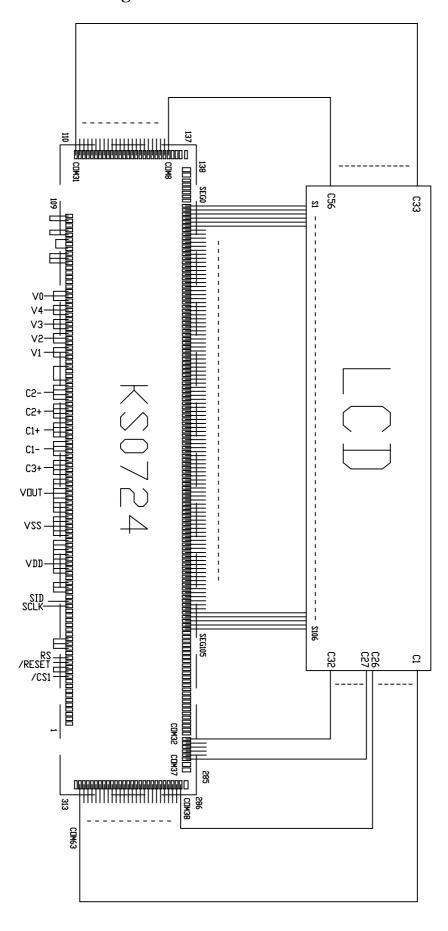
2 Outline Drawing



3 LCD Module Part Numbering System



4 Circuit Block Diagram



5 Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark	
Power Supply Voltage	V _{DD} -V _{SS}	2.4	3.6	V		
LCD Driving Voltage	VLCD	4.5	15.0	V		
Operating Temperature Range	Тор	0	+50	$^{\circ}$	No	
Storage Temperature Range	Тѕт	-20	+60		Condensation	

6 Electrical Specifications and Instruction Code

6.1 Electrical characteristics

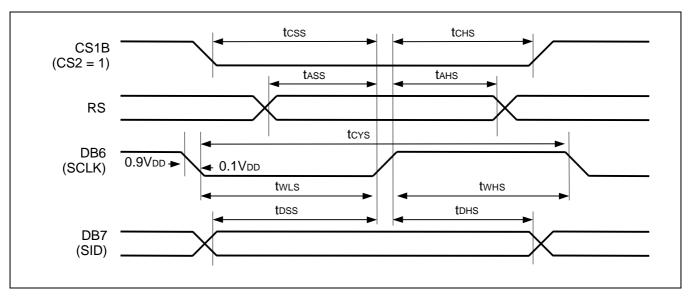
Ite	m	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage (Logic)		V _{DD} -V _{SS}	2.8	3.0	3.2	V
Supply Voltage (LCD Drive)		VLCD	9.0	9.5	10.0	V
Input Signal Voltage Low	High	V_{IH} $(V_{\text{DD}}=3.0)$	$0.8 \mathrm{V}_\mathrm{DD}$	-	$V_{ m DD}$	V
	Low	V_{IL} $(V_{\text{DD}}=3.0)$	0	-	0.2 V _{DD}	V
Supply current (Logic) (Display character)		I_{DD} $(V_{DD}-V_{SS}=3.0V)$	-	-	400	uA

6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	NC	-	No connection
2	V0	9.5V	Power supply voltage for LCD
3	V4	-	Power supply voltage for LCD
4	V3	_	Power supply voltage for LCD
5	V2	-	Power supply voltage for LCD
6	V1	-	Power supply voltage for LCD
7	C2-	-	Capacitor pin for voltage converter
8	C2+	-	Capacitor pin for voltage converter
9	C1+	-	Capacitor pin for voltage converter
10	C1-	-	Capacitor pin for voltage converter
11	C3+	-	Capacitor pin for voltage converter
12	VOUT	-	DC/DC voltage converter output
13	VSS	0V	Ground
14	VDD	3.0V	Power supply voltage for logic
15	SID	H/L	Serial data input pin
16	SCLK	H/L	Serial clock input pin
17	RS	H/L	Register select input pin
18	/RES	H/L	Reset input pin
19	CS1	H/L	Chip select input pin
20	NC	-	No connection

6.3 Interface Timing Chart

Serial Interface Characteristics



Serial Interface Characteristics

 $(VDD = 2.4 \text{ to } 3.6V, Ta = -40 \text{ to } +85^{\circ}C)$

Item	Signal	Symbol	Min.	Тур.	Max.	Unit	Remark
Serial clock cycle SCLK high pulse width SCLK low pulse width	DB6 (SCLK)	tCYS tWHS tWLS	250 100 100	1 1 1	- - -	ns	
Address setup time Address hold time	RS	tass tahs	150 150		-	ns	
Data setup time Data hold time	DB7 (SID)	tDSS tDHS	100 100	-		ns	
CS1B setup time CS1B hold time	CS1B	tcss tchs	150 150	-	-	ns	

INSTRUCTION DESCRIPTION

Instruction Table

×: Don't care

Instruction	DC	DW	DDZ	DDC	DDE	DD4	DD2	DDO	DD4	DDO	Description ()
Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	•
Display ON / OFF	0	0	1	0	1	0	1	1	1	DON	Turn on/off LCD panel When DON = 0: display OFF When DON = 1: display ON
Initial display line	0	0	0	1	ST5	ST4	ST3	ST2	ST1	ST0	Specify DDRAM line for COM0
Set page address	0	0	1	0	1	1	P3	P2	P1	P0	Set page address
Set column address MSB	0	0	0	0	0	1	Y7	Y6	Y5	Y4	Set column address MSB
Set column address LSB	0	0	0	0	0	0	Y3	Y2	Y1	Y0	Set column address LSB
Read status	0	1	BUSY	ADC	ONOFF	RESETB	0	0	0	0	Read the internal status
Write display data	1	0				Write	data				Write data into DDRAM
Read display data	1	1				Read	l data				Read data from DDRAM
ADC select	0	0	1	0	1	0	0	0	0	ADC	Select SEG output direction When ADC = 0: normal direction (SEG0→SEG131) When ADC = 1: reverse direction (SEG131→SEG0)
Reverse display ON / OFF	0	0	1	0	1	0	0	1	1	REV	Select normal / reverse display When REV = 0: normal display When REV = 1: reverse display
Entire display ON / OFF	0	0	1	0	1	0	0	1	0	EON	Select normal/entire display ON When EON = 0: normal display. When EON = 1: entire display ON
LCD bias select	0	0	1	0	1	0	0	0	1	BIAS	Select LCD bias
Set modify-read	0	0	1	1	1	0	0	0	0	0	Set modify-read mode
Reset modify-read	0	0	1	1	1	0	1	1	1	0	release modify-read mode
Reset	0	0	1	1	1	0	0	0	1	0	Initialize the internal functions
SHL select	0	0	1	1	0	0	SHL	×	×	×	Select COM output direction When SHL = 0: normal direction (COM0→COM63) When SHL = 1: reverse direction (COM63→COM0)
Power control	0	0	0	0	1	0	1	VC	VR	VF	Control power circuit operation
Regulator resistor select	0	0	0	0	1	0	0	R2	R1	R0	Select internal resistance ratio of the regulator resistor
Set reference voltage mode	0	0	1	0	0	0	0	0	0	1	Set reference voltage mode
Set reference voltage register	0	0	×	×	SV5	SV4	SV3	SV2	SV1	SV0	Set reference voltage register
Set static indicator mode	0	0	1	0	1	0	1	1	0	SM	Set static indicator mode
Set static indicator register	0	0	×	×	×	×	×	×	S1	S0	Set static indicator register
Power save	-	ı	-	-	-	-	-	-	-	-	Compound Instruction of display OFF and entire display ON

7 Optical Characteristics

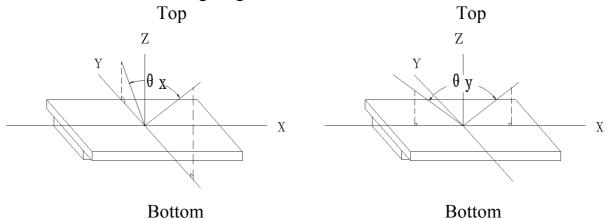
7.1 Optical Characteristics

Ta=25°C

Item		Symbol	Cone	dition	Min.	Тур.	Max.	Unit		
Viewing Angle		θ_{X}	C > 2	θ _y =0°	-30	-30		D		
		θу	Cr≥2	θ _x =0°	-30		30	Deg		
Contrast Ratio		Cr	$\theta_{x}=0^{\circ}$ $\theta_{y}=0^{\circ}$		3.0	-	-			
Response	Turn on	Ton	$\theta_{\!\scriptscriptstyle X} {=} 0^\circ$		θ _x =0°		-	-	300	
Time	Turn off	Toff	θ_{y} =	=0°	-	1	300	ms		

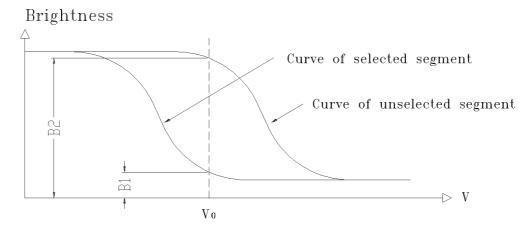
7.2 Definition of Optical Characteristics

7.2.1 Definition of Viewing Angle



Bottom

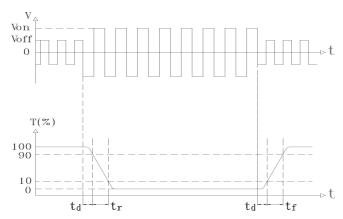
7.2.2 Definition of Contrast Ratio



Contrast Ratio =
$$B2/B1 = \frac{\text{unselected state brightness}}{\text{selected state brightness}}$$

Measuring Conditions:

1) Ambient Temperature: 25°C; 2) Frame frequency: 64Hz 7.2.3 Definition of Response time



Turn off time: $t_{off} = t_d + t_f$ Turn on time: $t_{on} = t_d + t_r$ Measuring Condition:

1) Operating Voltage: 9.5V 2) Frame frequency: 64Hz

8 Reliability

8.1 Content of Reliability Test

Ta=25°C

	· · · · · · · · · · · · · · · · · · ·		
No.	Test Item	Content of Test	Test condition
1	High Temperature	Endurance test applying the high	60℃
	Storage	storage temperature for a long time	96H
2	Low Temperature	Endurance test applying the low	-20°C
2	Storage	storage temperature for a long time	96H
		Endurance test applying the	
3	High Temperature	electric stress (voltage & current)	50 ℃
3	Operation	and the thermal stress to the	96Н
		element for a long time	90П
	Low Temperature	Endurance test applying the	$0^{\circ}\!\mathbb{C}$
4	Operation Operation	electric stress under low	96H
	Operation	temperature for a long time	7011
	High Temperature	Endurance test applying the high	40°C
5	/Humidity Storage	temperature and high humidity	90%RH
	Trummanty Storage	storage for a long time	96H
		Endurance test applying the low	
		and high temperature cycle	20%0 /60%0
6	Temperature	-20°C ←→25°C ←→60°C ←→25°C	-20°C/60°C
	Cycle	30min 5min 30min 5min ←	10 cycles
		1 cycle	
	17'1	End was to the district	10Hz~150Hz,
7	Vibration Test	Endurance test applying the	50m/s^2 ,
	(package state)	vibration during transportation	40min
	Shock Test	Endurance test applying the shock	Half- sine wave,
8	(package state)	during transportation	100m/s^2 ,
	(package state)	Ç 1	11ms
	Atmospheric	Endurance test applying the	40kPa
9	Pressure Test	atmospheric pressure during	40kPa 16H
	11000010 1000	transportation by air	IUΠ

8.2 Failure Judgment Criterion

Criterion			Te	est :	Iter	n N	0.			Failura Judgamant Critarian
Item	1	2	3	4	5	6	7	8	9	Failure Judgement Criterion
Basic Specification	1	1	1	1	1	1	1	V		Out of the basic Specification
Electrical specification	√	1	1	1	1					Out of the electrical specification
Mechanical Specification							V	V		Out of the mechanical specification
Optical Characteristic	V	V	1	1	V	V			√	Out of the optical specification
Note	For test item refer to 8.1									
Remark	Basic specification = Optical specification + Mechanical specification									

9 QUALITY LEVEL

Examination	At T _a =25°C	Inspection					
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL	
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See Ap	pendix A	II	Major 1.0 Minor 2.5		
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See Ap	pendix B		II	Major 1.0 Minor 2.5	

Note: Major defects: Open segment or common, Short, Serious damages, Leakage

Miner defects: Others

Sampling standard conforms to GB2828

10 Precautions for Use of LCD Modules

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body when handling the LCD Modules.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

- 10.2 Storage precautions
- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: ≤80%

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

Appendix A

Inspection items and criteria for appearance defects

Items	Contents	Criteria					
Protective Glue		No clear defe	cts				
Cover Tape		Covering all of	of the	chip and no clear	crimple		
Leakage		Not permitted					
Rainbow		According to the limit specimen					
	Wrong polarizer attachment	Not permitted					
Polarizer	Bubble between	Not counted		Max. 3 defects allowed			
	polarizer and glass	ф<0.3mm		0.3mm≤¢≤0.5r	nm		
	Scratches of polarizer	According to the limit specimen					
Black spot		Not counted	Max. 3 spots allowed				
(in viewing area)		X<0.2mm	0.2m	m≤X≤0.5mm	Max. 3		
	_ α	X=(a+b)/2		spots (lines)			
Black line (in viewing	1	Not counted	Max	. 3 lines allowed	allowed		
area)	b	a<0.02mm	0.021	mm <a<0.05mm< td=""><td></td></a<0.05mm<>			
				b≤2.0mm			
Progressive cracks		Not permitted					

Appendix A

Inspection item and criteria for appearance defects (continued)

Items	Contents				Criteria		
	Cracks on pads	a	b	ı	С	Max. 2	
		≤3mm	$\leq V$	V/5	≤T/2	cracks allowed	
	b- /-	≤2mm	≪V	V/5	T/2 <c<t< td=""><td>anowed</td></c<t<>	anowed	
	Cracks on contact side	a			b		
		≤3m	m		≤T/2		
		≤2m	m	7	Γ/2 <b<t< td=""><td></td><td></td></b<t<>		
Glass		C shall b	e not	reac	th the seal	Max. 2 cracks	Max. 5 cracks allowed
Cracks	Cracks on non-contact side	a		b		allowed	
		≤3mm		≪T/2			
		≤2m	≤2mm		Γ/2 <b<t< td=""><td></td><td></td></b<t<>		
	- SW -	C≤0.5m	nm				
		d≤SW/3	3				
	Corner cracks	e<2.0mn				Max. 3	
	f-r	f<2.0mm	n ²		cracks allowed		

Appendix B

Inspection items and criteria for display defects

Items		Contents	Critera		
Open segment or open common			Not permitted		
Short			Not permitted		
Wrong viewing angle			Not permitted		
Contrast radio uneven			According to the limit specimen		
Crosstalk			According to the limit specimen		
Pin holes and cracks in segment (DOT)	+ - a - i - a	- - - 	Not counted	Max.3 dots allowed	
		X<0.1mm	0.1mm≤X≤0.2mm		
		X=(a+b)/2		Max.3 dots	
	D	Not counted	Max.2 dots allowed	allowed	
		1	A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm	
Black spot (in viewing area)			Not counted	Max.3 spots allowed	
			X<0.1mm	0.1mm≤X≤0.2mm	
	- Q	X=(a+b)/2		Max.3 spots	
Black line (in viewing area)	b b		Not counted	Max.3 lines allowed	(lines) allowed
			a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm	

Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Critera				
Transfor- mation of segment	- O	Not counted	Max. 2 defects allowed			
		x<0.1mm	0.1mm≤x≤0.2mm			
		x=(a+b)/2				
				Max.3		
	D-7/1/-a	Not counted	Max. 1 defects allowed	defects allowed		
		a<0.1mm	0.1mm≤a≤0.2mm			
			D>0			
		Max.2 defects allowed 0.8W≤a≤1.2W a=measured value of width W=nominal value of width				