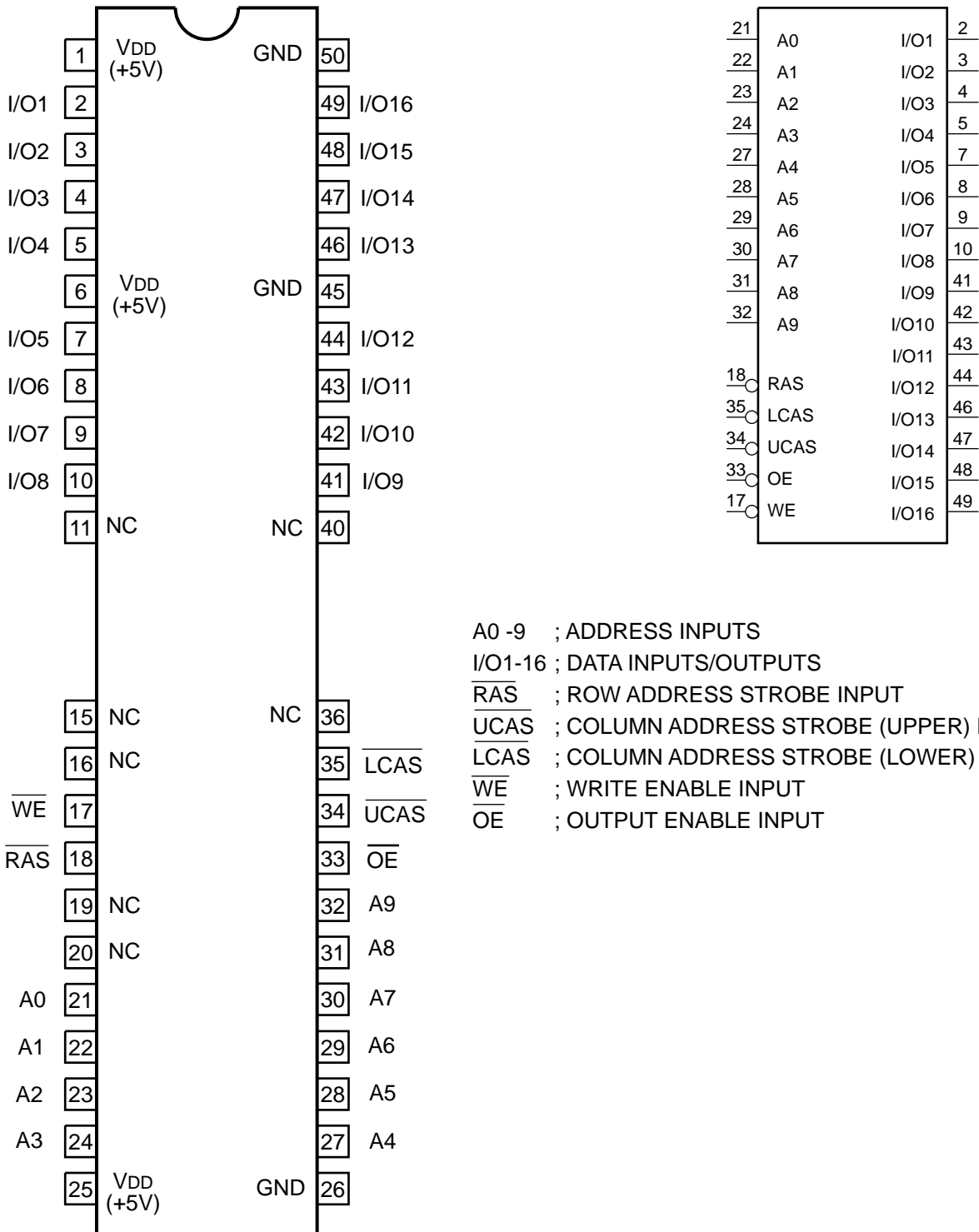

C-MOS 16M (1,048,576 x16) BIT DYNAMIC RAM
-TOP VIEW-



- A0 -9 ; ADDRESS INPUTS
- I/O1-16 ; DATA INPUTS/OUTPUTS
- $\overline{\text{RAS}}$; ROW ADDRESS STROBE INPUT
- UCAS ; COLUMN ADDRESS STROBE (UPPER) INPUT
- LCAS ; COLUMN ADDRESS STROBE (LOWER) INPUT
- $\overline{\text{WE}}$; WRITE ENABLE INPUT
- OE ; OUTPUT ENABLE INPUT

