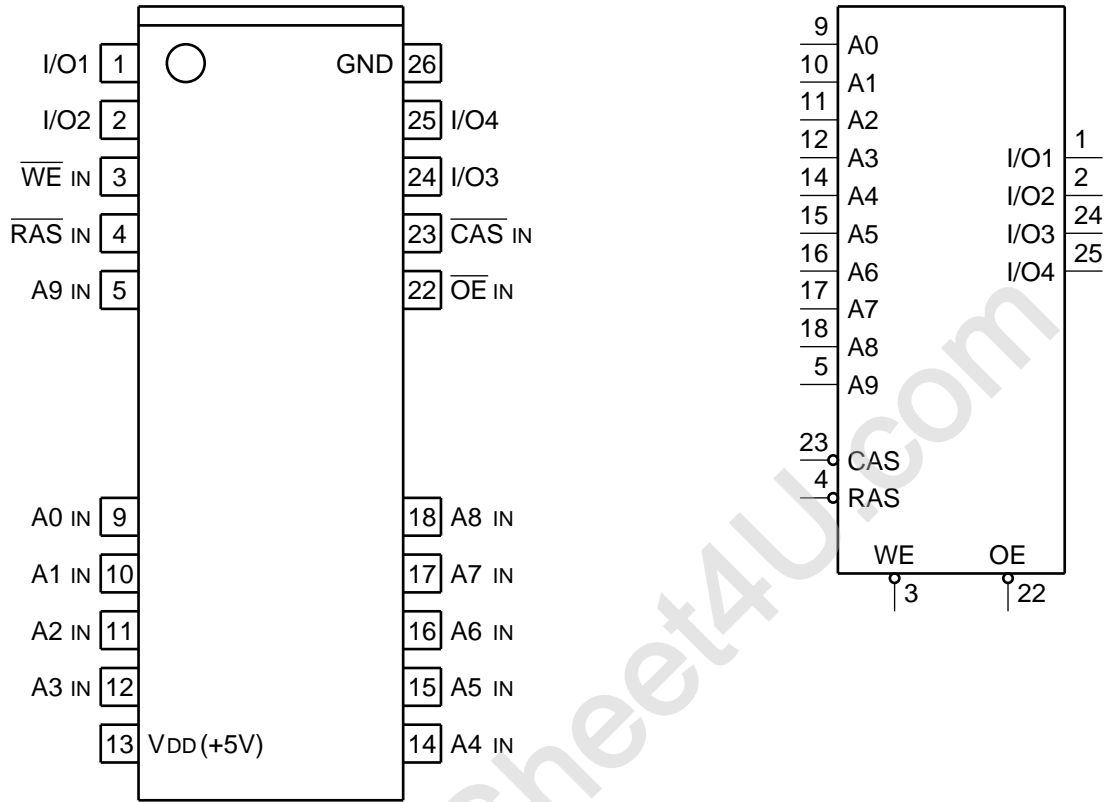

C-MOS 4M (1,048,576WORDx4)-BIT DYNAMIC RAM

- TOP VIEW -



- A0-A9 ; ADDRESS INPUTS
- $\overline{\text{CAS}}$; COLUMN ADDRESS STROBE INPUT
- I/O1-I/O4 ; DATA INPUTS/OUTPUTS
- $\overline{\text{OE}}$; OUTPUT ENABLE INPUT
- $\overline{\text{RAS}}$; ROW ADDRESS STROBE INPUT
- $\overline{\text{WE}}$; WRITE ENABLE INPUT

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