

# 2SK2329 (L), 2SK2329 (S)

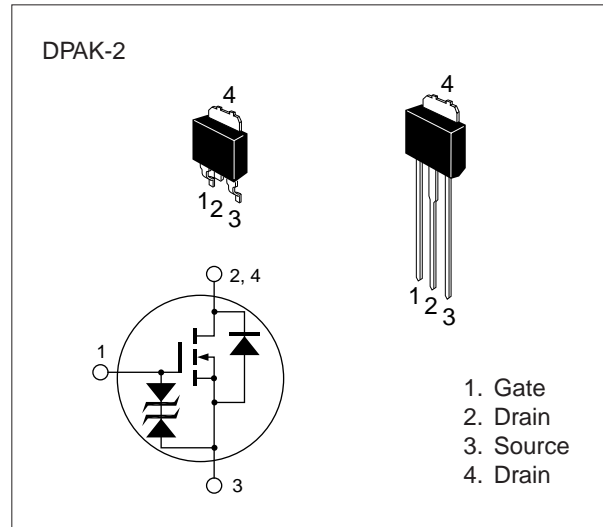
## Silicon N Channel MOS FET

### Application

High speed power switching

### Features

- Low on-resistance
- High speed switching
- Low drive current
- 2.5 V gate drive device can be driven from 3 V source
- Suitable for Switching regulator, DC – DC converter



**Table 1 Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±10	V
Drain current	I <sub>D</sub>	10	A
Drain peak current	I <sub>D(pulse)</sub> *	40	A
Body-drain diode reverse drain current	I <sub>DR</sub>	10	A
Channel dissipation	P <sub>ch</sub> **	20	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

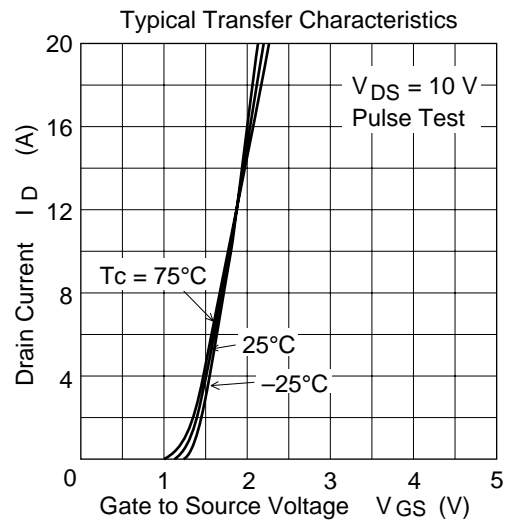
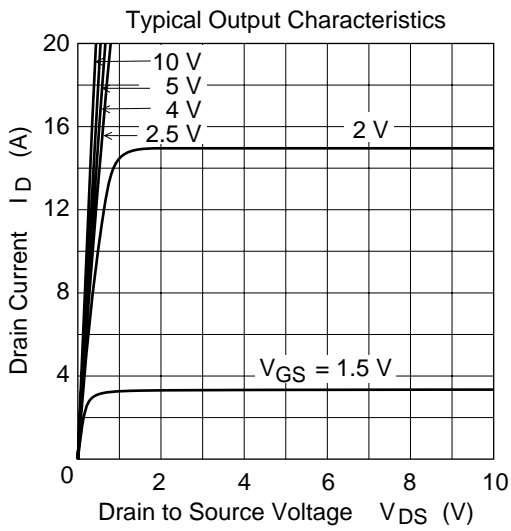
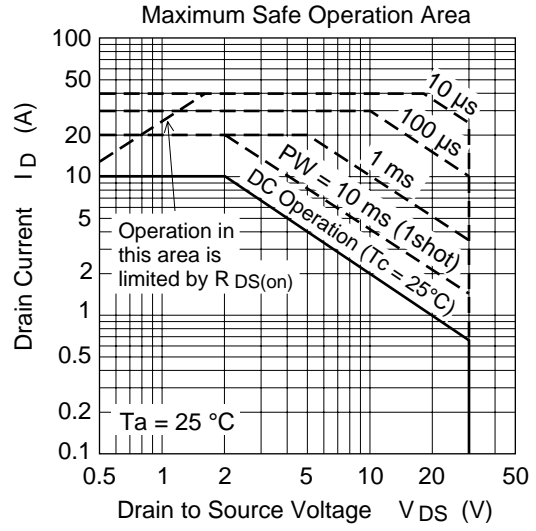
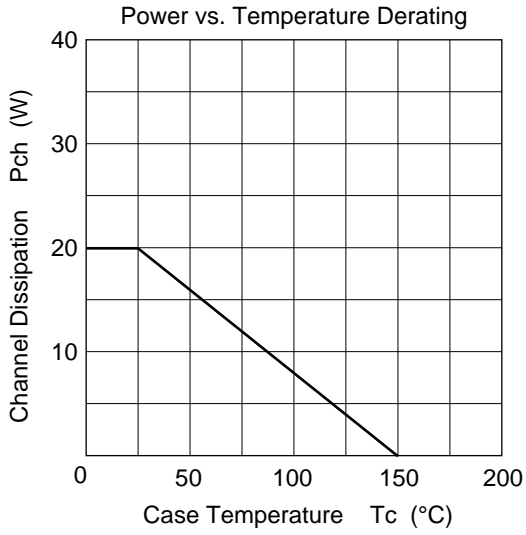
\* PW ≤ 10 μs, duty cycle ≤ 1 %

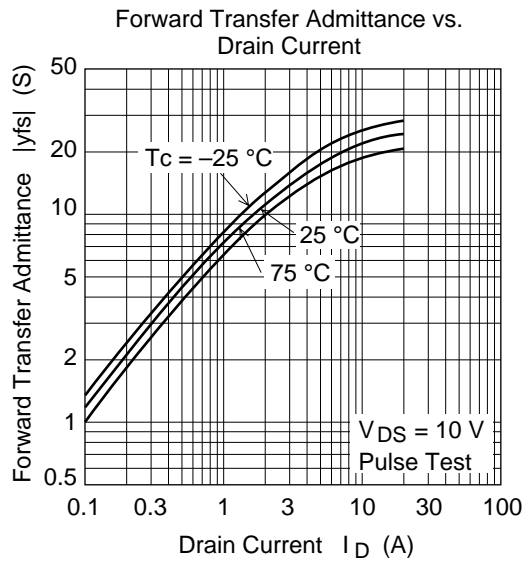
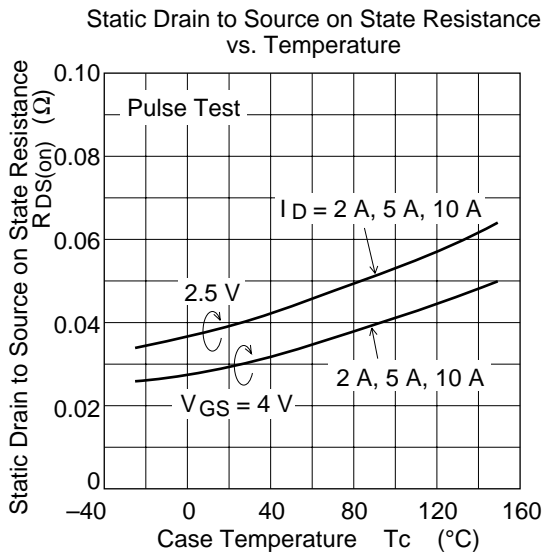
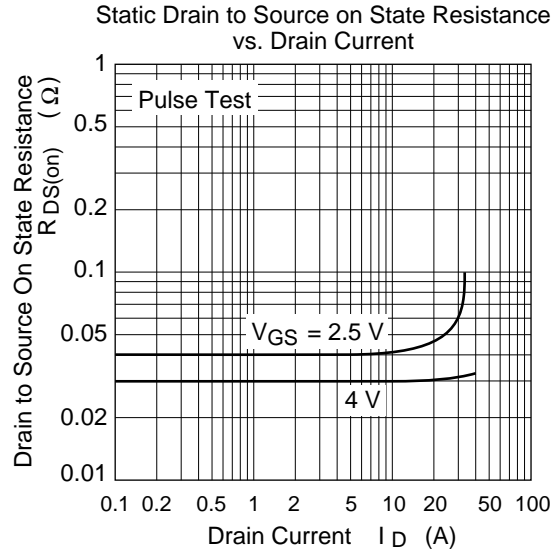
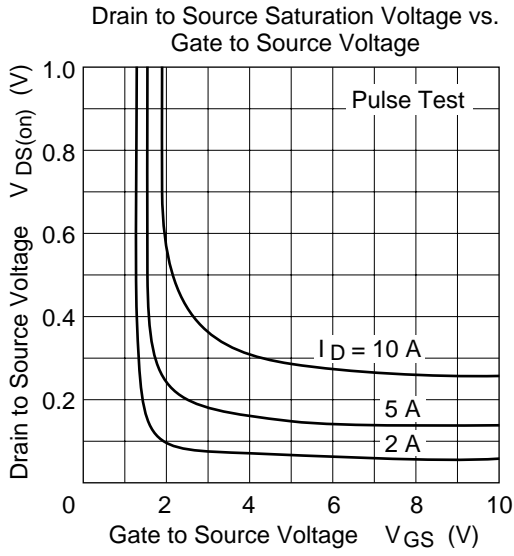
\*\* Value at T<sub>c</sub> = 25 °C

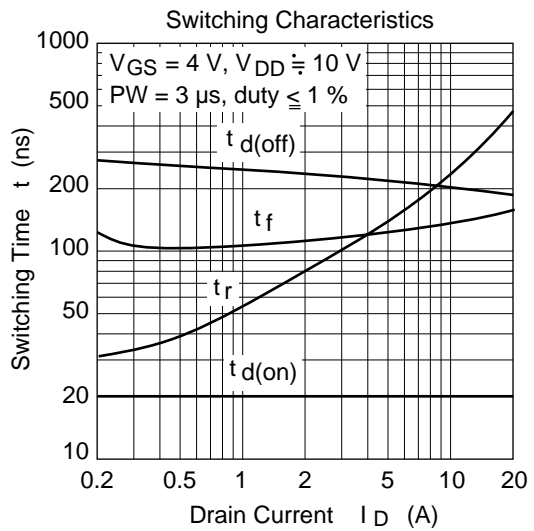
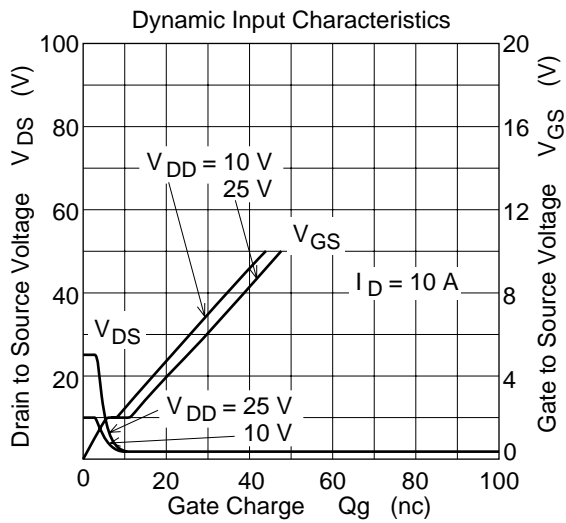
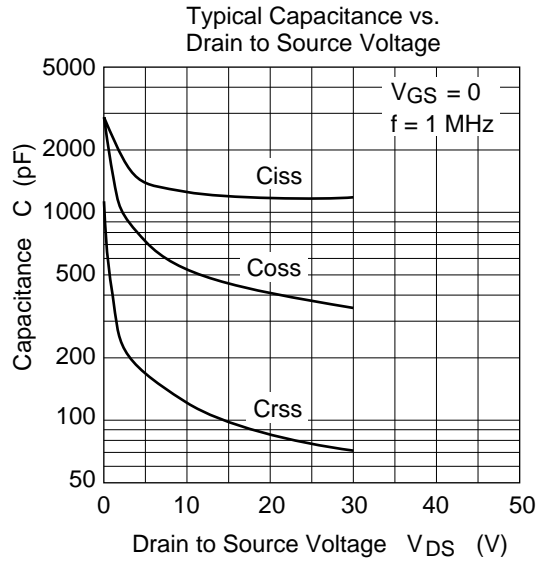
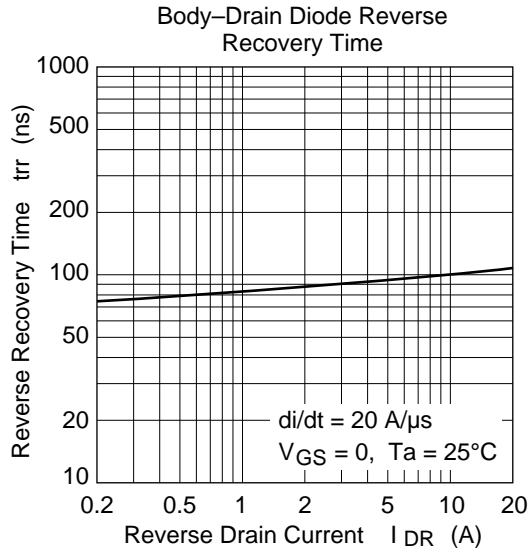
**Table 2 Electrical Characteristics** ( $T_a = 25^\circ\text{C}$ )

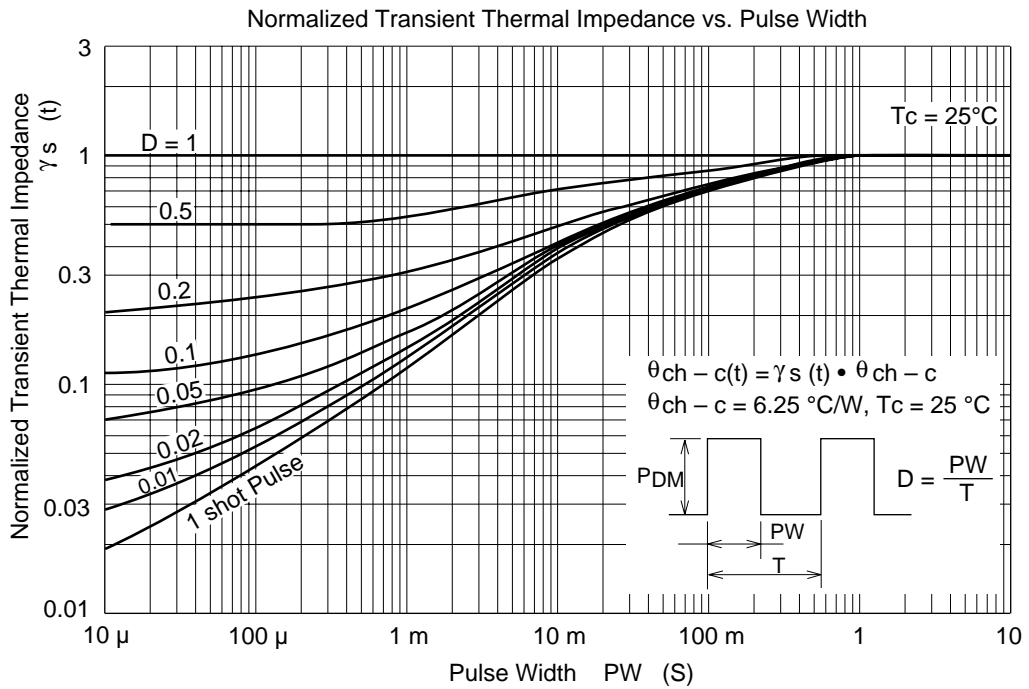
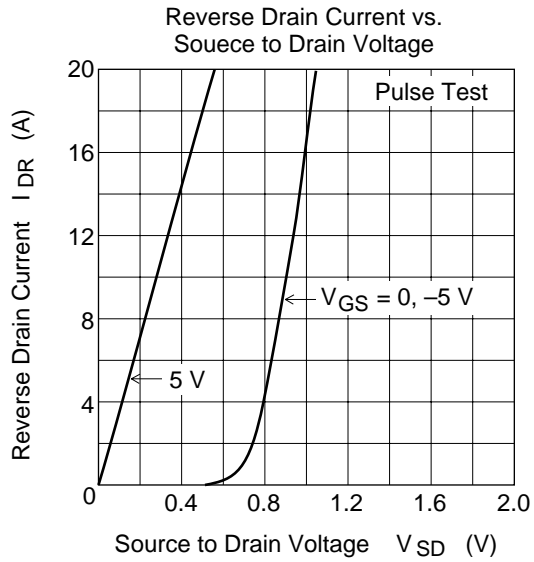
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	—	—	V	$I_D = 10\text{ mA}$ , $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	$\pm 10$	—	—	V	$I_G = \pm 200\ \mu\text{A}$ , $V_{DS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	$\pm 10$	$\mu\text{A}$	$V_{GS} = \pm 6.5\text{ V}$ , $V_{DS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	100	$\mu\text{A}$	$V_{DS} = 25\text{ V}$ , $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.4	—	1.4	V	$I_D = 1\text{ mA}$ , $V_{DS} = 10\text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.03	0.04	$\Omega$	$I_D = 5\text{ A}$ $V_{GS} = 4\text{ V}^*$
		—	0.04	0.06	$\Omega$	$I_D = 5\text{ A}$ $V_{GS} = 2.5\text{ V}^*$
Forward transfer admittance	$ y_{fs} $	10	18	—	S	$I_D = 5\text{ A}$ $V_{DS} = 10\text{ V}^*$
Input capacitance	$C_{iss}$	—	1250	—	pF	$V_{DS} = 10\text{ V}$
Output capacitance	$C_{oss}$	—	540	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	$C_{rss}$	—	120	—	pF	$f = 1\text{ MHz}$
Turn-on delay time	$t_{d(on)}$	—	20	—	ns	$I_D = 5\text{ A}$
Rise time	$t_r$	—	145	—	ns	$V_{GS} = 4\text{ V}$
Turn-off delay time	$t_{d(off)}$	—	225	—	ns	$R_L = 2\ \Omega$
Fall time	$t_f$	—	125	—	ns	
Body-drain diode forward voltage	$V_{DF}$	—	0.9	—	V	$I_F = 10\text{ A}$ , $V_{GS} = 0$
Body-drain diode reverse recovery time	$t_{rr}$	—	100	—	ns	$I_F = 10\text{ A}$ , $V_{GS} = 0$ , $di_F / dt = 20\text{ A} / \mu\text{s}$

\* Pulse Test

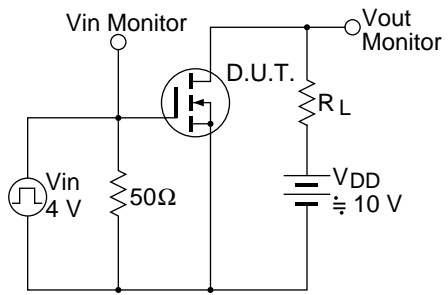








Switching Time Test Circuit



Waveform

