TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

GT60M323

Voltage Resonance Inverter Switching Application

• Enhancement mode type

• High speed : $t_f = 0.09 \mu s$ (typ.) (IC = 60 A)

- Low saturation voltage $: V_{CE (sat)} = 2.3 \text{ V (typ.)} (I_{C} = 60 \text{ A})$
- FRD included between emitter and collector
- TO-3P(LH) (Toshiba package name)

Maximum Ratings (Ta = 25°C)

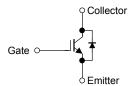
Characteristics		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	900	V	
Gate-emitter voltage		V _{GES}	±25	V	
Continuous collector current	@ Tc = 100°C	I _C	31	А	
	@ Tc = 25°C	iC	60		
Pulsed collector current		I _{CP}	120	А	
Diode forward current	DC	lF	15	А	
	Pulsed	I _{FP}	120		
Collector power dissipation	@ Tc = 100°C	Pc	80	W	
	@ Tc = 25°C	1 0	200		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Weight: 9.75 g (typ.)

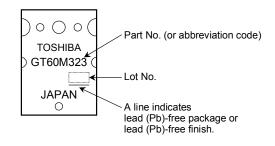
Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance (IGBT)	R _{th (j-c)}	0.625	°C/W	
Thermal resistance (diode)	R _{th (j-c)}	4.0	°C/W	

Equivalent Circuit



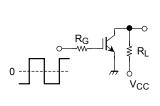
Marking

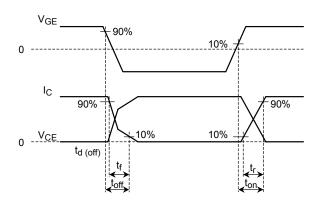


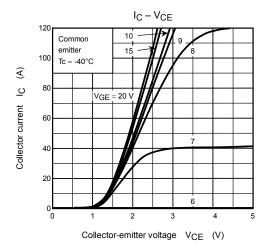
Electrical Characteristics (Ta = 25°C)

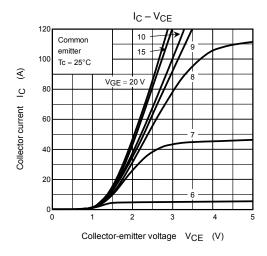
Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GES}	V _{GE} = ±25 V, V _{CE} = 0	_	_	±500	nA
Collector cut-off current		I _{CES}	V _{CE} = 900 V, V _{GE} = 0	_	_	0.1	mA
Gate-emitter cut-	off voltage	V _{GE} (OFF)	I _C = 60 mA, V _{CE} = 5 V	4.0	_	7.0	V
Collector-emitter saturation voltage		V _{CE (sat)}	I _C = 60 A, V _{GE} = 15 V	_	2.3	2.8	V
Input capacitance		C _{ies}	V _{CE} = 10 V, V _{GE} = 0, f = 1 MHz	_	4200	_	pF
Switching time	Rise time	t _r	Resistive Load	_	0.25	_	
	Turn-on time	t _{on}	V _{CC} = 600 V, I _C = 60 A	_	0.37	_	
	Fall time	t _f	V_{GG} = ±15 V, R_G = 51 Ω	1 Ω — 0.09 0	0.20	μs	
	Turn-off time	t _{off}	(Note 1)	_	0.40	_	
Diode forward voltage V _F		V _F	I _F = 15 A, V _{GE} = 0	_	1.1	1.9	V
Reverse recovery time t _n		t _{rr}	I _F = 60 A, di/dt = -20 A/μs	_	1.4	3.0	μs

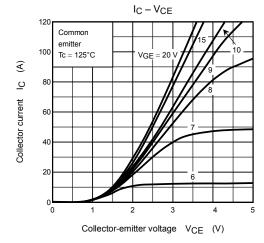
Note 1: Switching time measurement circuit and input/output waveforms

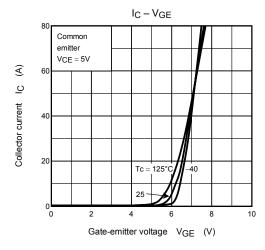


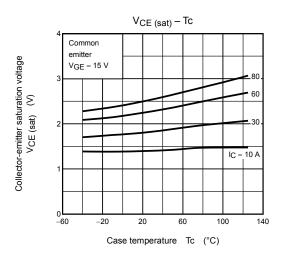




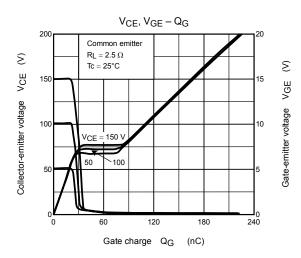


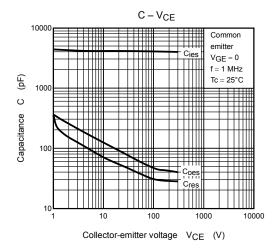


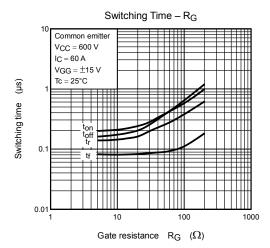


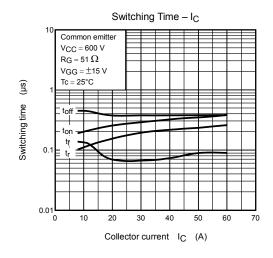


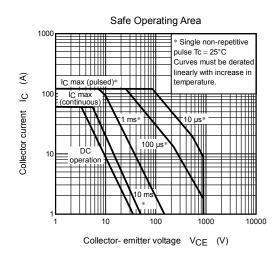
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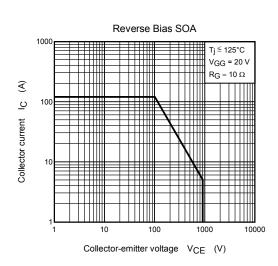


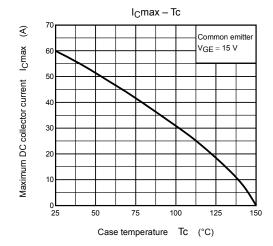


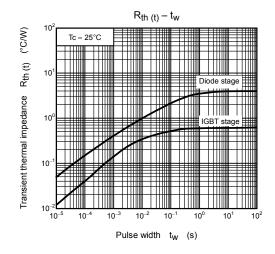


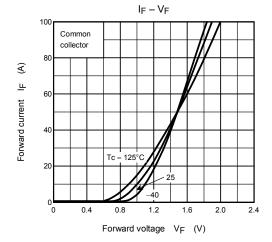


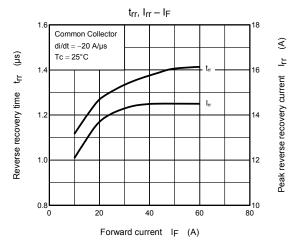


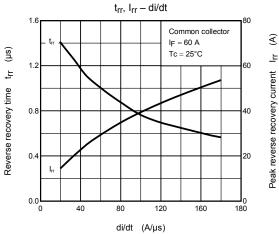












Peak reverse recovery current Irr

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