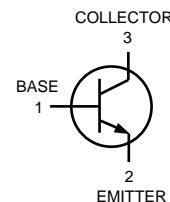
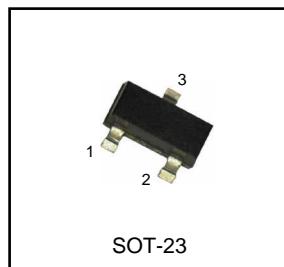




## High Voltage Transistor

### NPN Silicon

**MMBT5550**



#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CCEO</sub>	140	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	160	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	Vdc
Collector Current-Continuous	I <sub>C</sub>	600	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max.	Unit
Total Device Dissipation FR-5 Board <sup>(1)</sup> TA=25°C Derate above 25°C	P <sub>D</sub>	225 1.8	mW mW / °C
Thermal Resistance Junction to Ambient	R <sub>θJA</sub>	556	°C / W
Total Device Dissipation Alumina Substrate, <sup>(2)</sup> TA=25°C Derate above 25°C	P <sub>D</sub>	300 2.4	mW mW / °C
Thermal Resistance Junction to Ambient	R <sub>θJA</sub>	417	°C / W
Junction and Storage Temperature	T <sub>J,TSTG</sub>	-55 to +150	°C

#### DEVICE MARKING

MMBT5550=M1F

#### ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Characteristic	Symbol	Min.	Max.	Unit
OFF CHARACTERISTICS				

Collector-Emitter Breakdowne Voltage <sup>(3)</sup> ( I <sub>C</sub> =1.0mA, I <sub>B</sub> =0 )	V <sub>(BR)CEO</sub>	140	-	Vdc
Collector-Base Breakdowne Voltage ( I <sub>C</sub> =100 uA, I <sub>E</sub> =0 )	V <sub>(BR)CBO</sub>	160	-	Vdc
Emitter-Base Breakdowne Voltage ( I <sub>E</sub> =10 uA, I <sub>C</sub> =0 )	V <sub>(BR)EBO</sub>	6.0	-	Vdc
Base Cutoff Current ( V <sub>CE</sub> =100 Vdc, I <sub>E</sub> =0 ) ( V <sub>CE</sub> =100 Vdc, I <sub>E</sub> =0, TA = 100°C )	I <sub>CBO</sub>	- -	100 100	nAdc uAdc
Collector Cutoff Current ( V <sub>EB</sub> =4.0 Vdc, I <sub>C</sub> =0 )	I <sub>EBO</sub>	-	50	nAdc

(1) FR-5=1.0 x 0.75 x 0.062in.

(2) Alumina=0.4 x 0.3 x 0.024in. 99.5% alumina.

(3) Pulse Test : Pulse Width = 300 uS, Duty Cycle = 2.0%.



## ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min.	Max.	Unit
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### ON CHARACTERISTICS

DC Current Gain ( IC= 1.0 mA, VCE= 5.0 Vdc ) ( IC= 10 mA, VCE= 5.0 Vdc ) ( IC= 50 mA, VCE= 5.0 Vdc )	H <sub>FE</sub>	60 60 20	- 250 -	-
Collector-Emitter Saturation Voltage ( IC= 10 mA, IB= 1.0 mA ) ( IC= 50 mA, IB= 5.0 mA )	V <sub>CE(sat)</sub>	- -	0.15 0.25	Vdc
Base-Emitter Saturation Voltage ( IC= 10 mA, IB= 1.0 mA ) ( IC= 50 mA, IB= 5.0 mA )	V <sub>BE(sat)</sub>	- -	1.0 1.2	Vdc

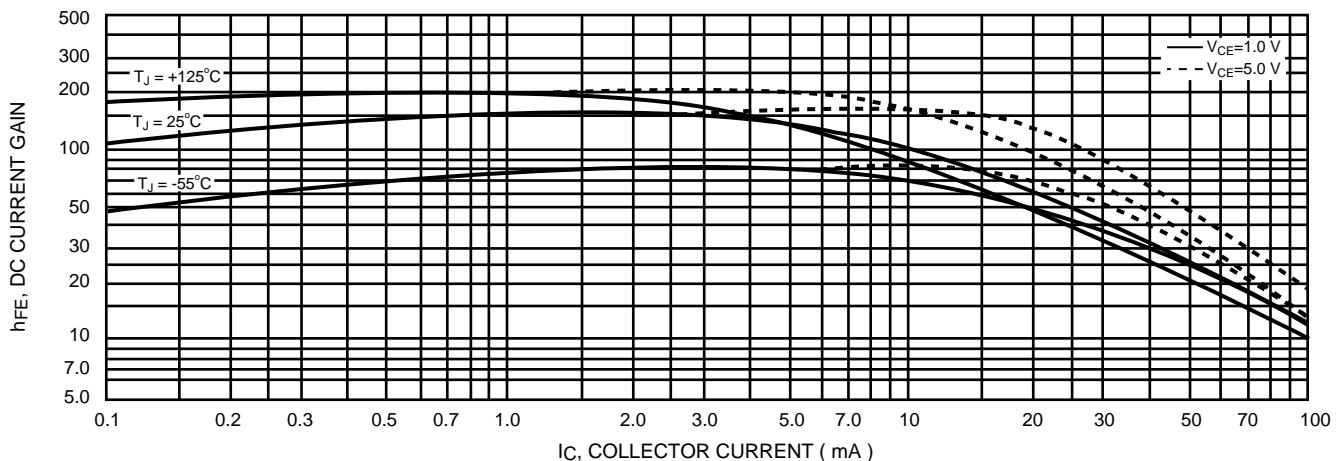


Figure 1. DC Current Gain

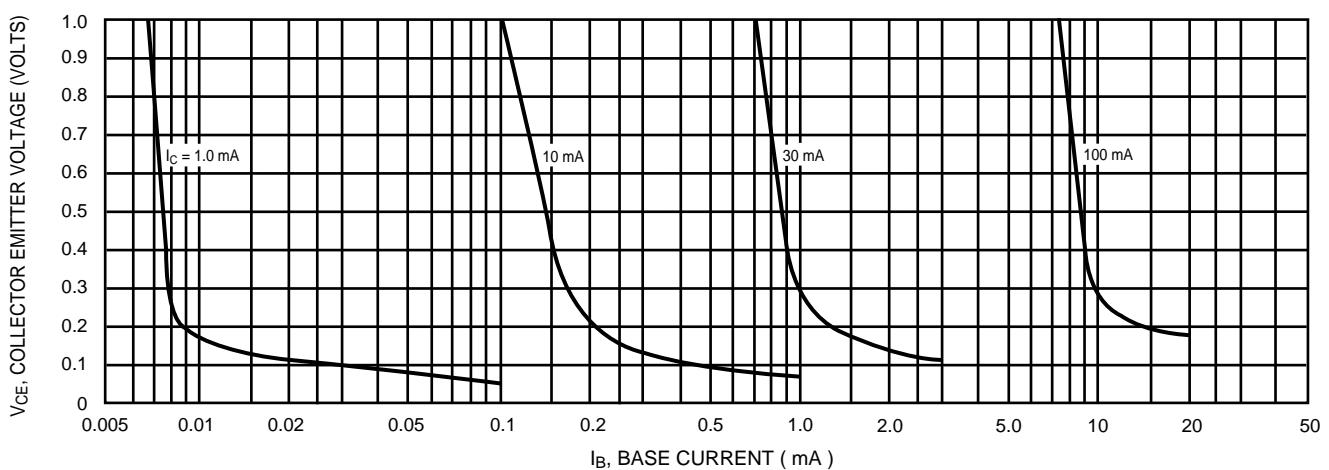


Figure 2. Collector Saturation Region

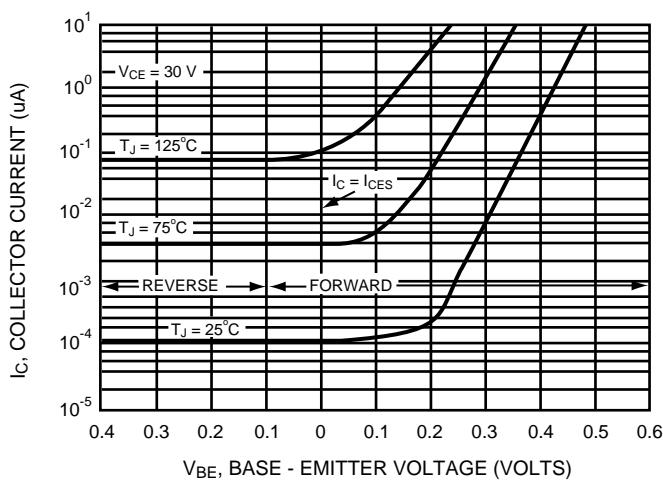


Figure 3. Collector Cut - Off Region

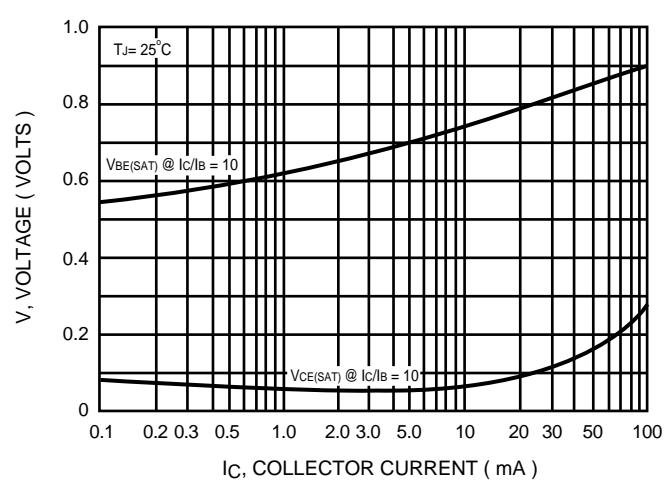


Figure 4. "On" Voltages

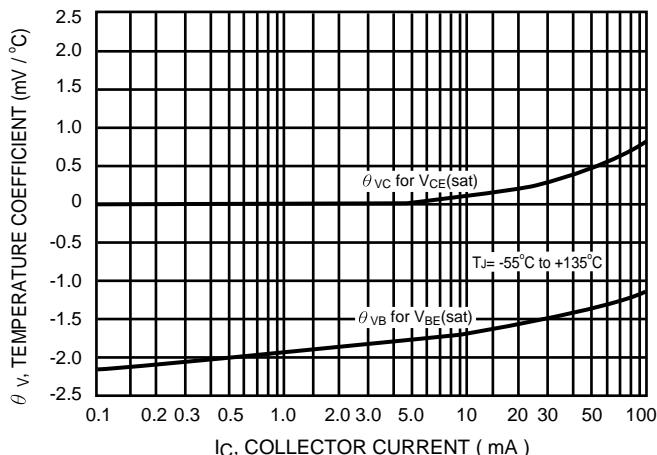
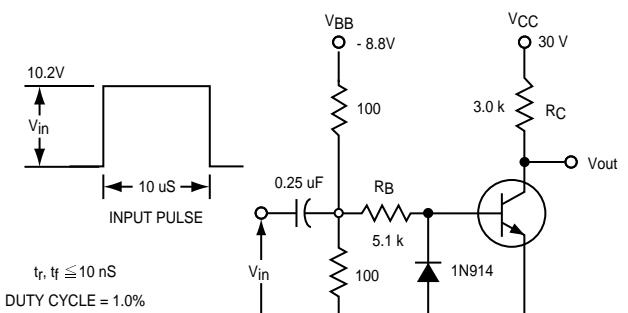


Figure 5. Temperature Coefficients



VALUES SHOWN ARE FOR IC @ 10 mA

Figure 6. Switching Time Test Circuit

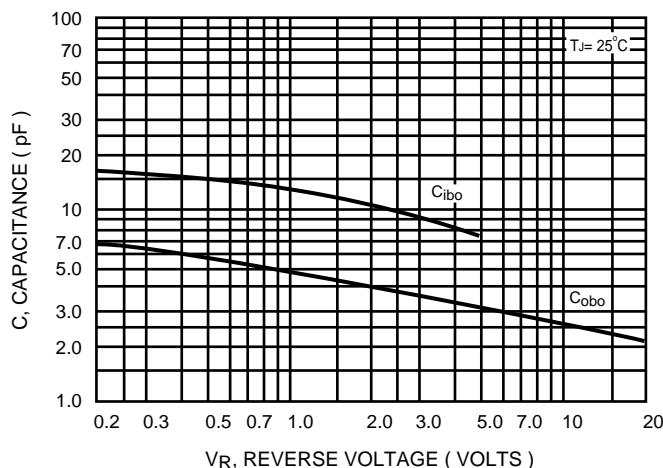


Figure 7. Capacitances

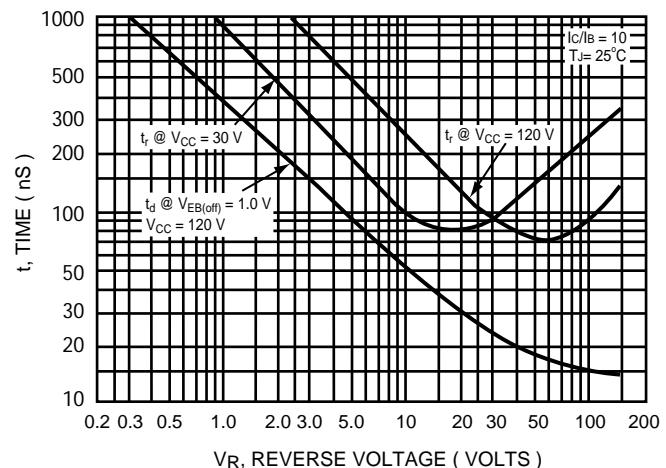


Figure 8. Turn-On Time

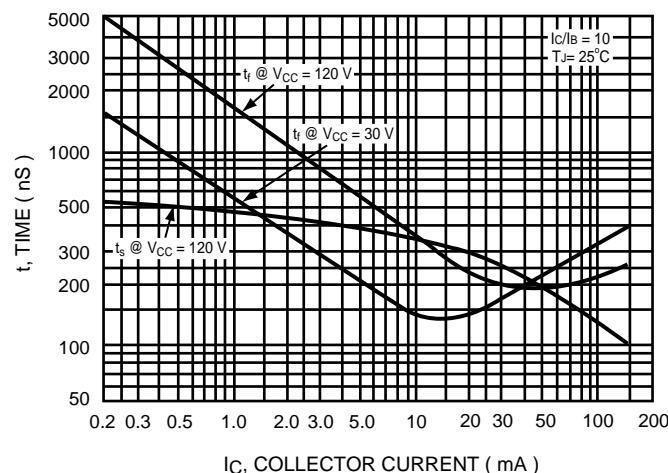


Figure 9. Turn - Off Time