# GDU 91-20221



# **Gate Drive Unit**

Replaces March 1998 version, DS4569-3.1

DS4569-4.0 January 2000

This datasheet should be used in conjunction with the application note AN4571, GDU9X-XXXXX Series, Gate Drive Unit.

# APPLICATIONS KEY PARAMETERS

■ Used with Gate Turn-Off Thyristors in high current switching applications

 $\begin{array}{ccc} I_{\text{FGM}} & 30\text{A} \\ I_{\text{G(ON)}} & 4\text{A} \\ \text{d}I_{\text{RO}}/\text{d}t & 30\text{A}/\mu\text{s} \end{array}$ 

# **CONDITIONS - (UNLESS STATED OTHERWISE)**

V <sub>1</sub> = +5V	V <sub>2</sub> = +15V		V <sub>3</sub> = -15V		
Test circuit GTO		DG408BP			
GDU connection to GTO		500mm CO - AX cable type RC5327230			
Test circuit emitter and gate drive emitter		Hewlett Packard versatile link HFBR1524			
Test circuit emitter current		30mA			
Test circuit receiver and gate drive receiver		Hewlett Packard versatile link HFBR2524			

# **ELECTRICAL CHARACTERISTICS**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I <sub>V1</sub>	+5V PSU current	500Hz, 50% duty cycle	-	-	2.2	А
I <sub>V2</sub>	+15V PSU current	500Hz	-	-	0.55	А
I <sub>V3</sub>	-15V PSU current	500Hz, I <sub>T</sub> = 1000A GTO T <sub>j</sub> = 125°C	-	-	3.0	А
V <sub>1(Min)</sub>	+5V PSU minimum	-	3.8	-	-	V
V <sub>2(Min)</sub>	+15V PSU minimum	-	14.0	-	-	V
V <sub>3(Min)</sub>	-15V PSU minimum	-	14.0	-	-	V
I <sub>FGM</sub>	Peak forward gate current	-	30	-	-	А
I <sub>G(ON)</sub>	On-state gate current	-	-	4	-	А
dl <sub>FG</sub> /dt	Rate of rise of positive gate current	Measured 10 - 75% I <sub>FGM</sub>	-	30	-	A/μs
dl <sub>gq</sub> /dt	Rate of rise of negative gate current	I <sub>T</sub> = 1000A, 90% I <sub>G(ON)</sub> - 50% I <sub>GQM</sub>	-	30	-	A/μs

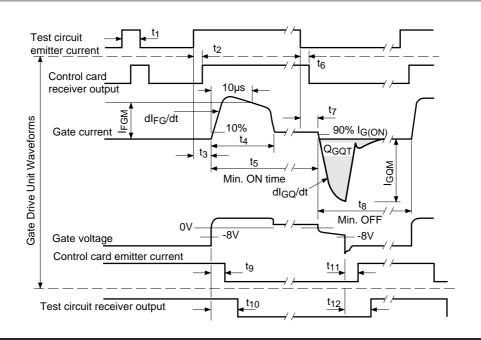
# TIMING CHARACTERISTICS

Symbol	Parameter	Conditions		Тур.	Max.	Units
t,*†	No response pulse width of input signal	Adjustable by R81 + R82	2	-	3	μs
t <sub>2</sub>	Delay time emitter current to receiver o/p	-	0.2	-	0.4	μs
t <sub>3</sub> *†	Turn-on delay emitter current to 10% I <sub>FGM</sub>	-	5.0	-	5.8	μs
t <sub>4</sub>	I <sub>FGM</sub> pulse width	-	-	25	-	μs
t <sub>5</sub> *	Minimum on time 10% I <sub>FGM</sub> to 90% I <sub>G(ON)</sub>	Adjustable by R37		-	110	μs
t <sub>6</sub>	Receiver storage time	-	0.8	-	1.2	μs
t <sub>7</sub>	Turn-off delay. Emitter current to 90% I <sub>G(ON)</sub>	-	1.5	-	2.3	μs
t <sub>8</sub> *	Minimum off time 90% $I_{G(ON)}$ to 10% $I_{FGM}$	Adjustable by R38	80	-	110	μs
t <sub>9</sub>	Delay time Gate volts to o/p emitter current	-	-	0.2	-	μs
t <sub>10</sub>	Turn-off delay Gate volts to test receiver o/p	-	-	0.8	-	μs
t <sub>11</sub>	Storage time Gate volts to o/p emitter current	Measured at low I <sub>GQM</sub>	-	0.11	-	μs
t <sub>12</sub>	Turn-on delay Gate volts to test receiver o/p	Measured at low I <sub>GQM</sub>	-	0.31	-	μs

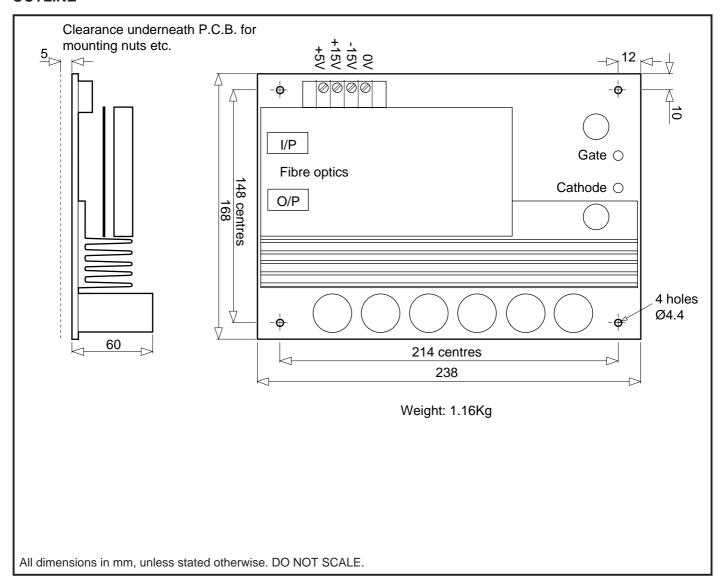
\*  $t_1$ , $t_3$ , $t_5$ , $t_8$  are factory settings.

<sup>†</sup> Adjustment of t₁ alters t₃.

1. Varies with  $\boldsymbol{I}_{\text{\tiny GQM}}$  due to gate lead impedance.



# **OUTLINE**



### POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of our semiconductors.

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