

bisplays, Hitachi, Ltd. 300 Hayano, Moh-Chiba Pref ~ TEI TEL:+81-475-25-9005 (Dial In)

- A

Date : Aug. 19, '99

CUSTOMER'S ACCEPTANCE SPECIFICATIONS SX25S003

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| | | | F | RECO | RD OF REVISION | | |
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3. MECHANICAL DATA

| (1) Part Name | SX25S003 |
|----------------------------|---|
| (2) Module Size | 236.0(W) mm \times 168.0(H) mm \times 6.3 max (D) mm |
| (3) Display Size | Diagonal size 25cm (10.0") |
| (4) Dot Pitch | 0.0845(W) mm \times 0.2535(H) mm |
| (5) Number of Dots | 800×3 (R,G,B)(W) $\times600$ (H) dots |
| (6) Duty | 1/300 |
| (7) LCD | Film type (negative type) The upper polarizer is an anti-glare type. (Hardness:3H) |
| (8) View ing Direction | 12 O'clock |
| (9) Backlight | Cold Cathode Fluorescent Lamp (CFL) $	imes$ 2 |
| (10) Weight | (330) g |
| (11) Pow er Supply Voltage | 3.3V only |
| | |
| | |

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|----------------------------|------|--------------|------------|----------------------------|------|-------|

4. ABSOLUTE MAXIMUM RATINGS

4. 1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

| 4. 1 ELECTRICAL ABSOLUTE MAX | 5 | VSS=0V:Standard | | | | |
|------------------------------|----------|-----------------|---------|------|---------|--|
| ITEM | SYMBOL | MIN | MAX | UNIT | COMMENT | |
| Pow er Supply for Logic | VDD-VSS | 0 | 4.6 | V | | |
| Contrast Adjustment Voltage | VCON-VSS | 0 | VDD | V | | |
| Input Voltage | Vi | -0.3 | VDD+0.3 | V | Note 1 | |
| Input Current | li | 0 | 1 | А | | |
| Static Electricity | - | - | - | - | Note 2 | |

DISP OFF, FLM, CL1, CL2, UD0~UD7, LD0~LD7 Note 1

Note 2 Make certains you are grounded when handling LCM

4. 2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

| | OPE | RATING | STC | DRAGE | | |
|---------------------|----------------|----------------------------------|----------------|--|----------------------|--|
| ПЕМ | MIN | MAX | MIN | MAX | COMMENT | |
| Ambient Temperature | 5°C | 40°C | -20°C | 60°C | Note 2, 3 | |
| Humidity | Note 1 | | Note 1 | | Without condensation | |
| Vibration | - | 2.45 m/s ² (0.25G) | - | 11.76 m/s ² (1.2G) Note 5 | Note 4 | |
| Shock | - | | - | 490 m/s ² (50G) Note 5 | XYZ directions 11ms | |
| Corrosive Gas | Not Acceptable | | Not Acceptable | | | |

Note 1 Ta<u><</u>40°C : 85%RH max.

Ta>40°C : Absolute humidity must be low er than the humidity of 85%RH at 40°C.

- Note 2 Ta at -20°C ----- <48h, at 60°C ----- <168h
- Note 3 Background color changes slightly depending on ambient temperature. This phenomenon is reversible.
- Note 4 5Hz~100Hz (Except resonance frequency)
- Note 5 This module should be operated normally after finish the test.
- Note 6 When LCM is operated at 5°C, the life time of CFL will be reduced. Need to make sure of value of IL and characteristics of inverter. Also the response time at 5°C will be slow er.

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|----------------------------|------|--------------|------------|----------------------------|------|-------|

5. ELECTRICAL CHARACTERISTICS

5 1 ELECTRICAL CHARACTERISTICS OF LCD

| 5. 1 ELECTRICAL CHARAC | TERISTICS OF | F LCD | VSS=0V | | | | |
|---|--------------|---------------|--------|------|--------|------|--|
| ΠΕΜ | SYMBOL | CONDITION | MIN | TYP | MAX | UNIT | |
| Pow er Supply Voltage | VDD | VDD-VSS=3.3V | 3.15 | 3.30 | 3.45 | V | |
| Contrast Adjustment Voltage (Note 1) | VCON | - | 1.2 | - | 2.4 | V | |
| Input Voltage for Logic | Vi | "H" level | 0.8VDD | - | VDD | v | |
| Circuits (Note 2) | | "L" level | 0 | - | 0.2VDD | V | |
| Pow er Supply Current (Note 3)(Note 6) | IDD | VDD-VSS=3.3V | - | 120 | 200 | mA | |
| Input Leak Current | lcon(Note4) | Vcon=0.8~2.8V | - | - | (20) | μA | |
| Input Leak Current | lin (Note2) | Vin=VDDorVSS | - | - | ±1.0 | port | |
| | | Ta= 5°C, | 1.2 | - | - | | |
| Contrast Adjustment Voltage | Vcon | Ta=25°C, | 1.5 | 1.9 | 2.3 | V | |
| (Note 7) | | Та=40°С, ф=0° | - | - | 2.4 | | |
| Frame Frequency (Note 5) | fFLM | - | 70 | 120 | 130 | Hz | |

(Note 1) In proportion as the VCON voltage decrease the brightness will increase.

(Note 2) DISP OFF, FLM, CL1, CL2, UD0~UD7, LD0~LD7

(Note 3) fFLM=120Hz, Ta=25°C, Display pattern: Checker pattern.

(Note 4) VCON

(Note 5) Need to make sure of flickering and rippling of display when setting the Frame Frequency in your set.

(Note 6) Rush Current of Pow er ON : $0.8A \times 10ms$

(Note 7) The Contrast Adjustment Voltage is specified as 1.9±0.4V under the condition, when an optimum contrast is obtained by naked eyes as the "Q" test pattern. fFLM=120Hz, 1/313Duty

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| 5. 2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT | | | | | | | | | | |
|--|----------------|-----------------|-------|-----------------|------|---------|--|--|--|--|
| ПЕМ | SYMBOL | MIN | TYP | MAX | UNIT | NOTE | | | | |
| Lamp Voltage | VL | - | (500) | - | Vrms | Ta=25°C | | | | |
| Frequency | fL | 50 | 60 | - | kHz | | | | | |
| Lamp Current (1Lamp) | L | 3.5 (Note 5) | 5 | 5.5 (Note 5) | mA | Ta=25°C | | | | |
| Starting discharge Voltage | VS (Note 2) | (1500) | - | - | Vrms | Ta=5°C | | | | |

- (Note 1) Please design your lamp driving circuit (inverter) according to the above specifications, and inform Hitachi of it.
- (Note 2) Starting discharge voltage is increased when LCM is operating at low er temperature. Please check the characteristics of your inverter before applying to your set.
- (Note 3) Average life time of CFL will be decreased when LCM is operating at low er temperature.
- (Note 4) Under low er driving frequency of an inverter, a certain backlight system (CFL & CFL reflection sheet) may generate a sound noise. Before designing the inverter, please consider the driving frequency and the noise.
- (Note 5) When ICFL is used over 5.5mA, it may cause uneven contrast near CFL location, due to heat dispersion from CFL.

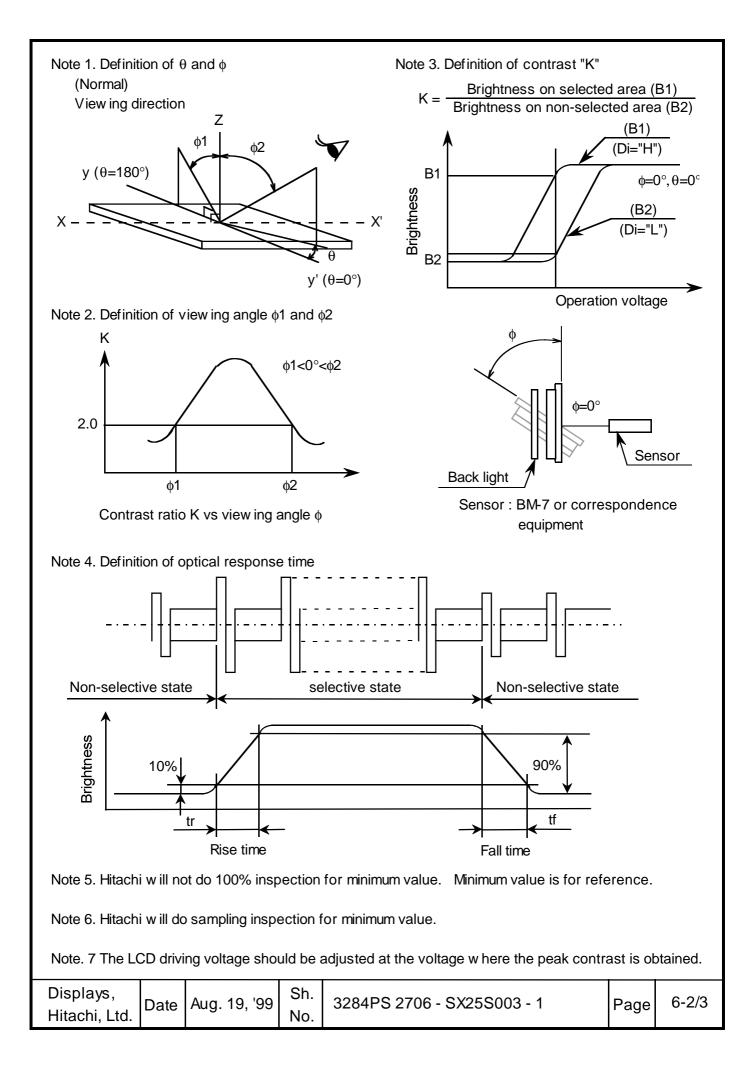
| Displays, Date Aug. 19, '99 Sin. 3284PS 2705 - SX25S003 - 1 Page 5-2/ Hitachi, Ltd. Date Aug. 19, '99 No. 3284PS 2705 - SX25S003 - 1 Page 5-2/ | Displays, Hitachi, Ltd. | Date | Aug. 19, '99 | | 3284PS 2705 - SX25S003 - 1 | Page | 5-2/2 |
|---|----------------------------|------|--------------|--|----------------------------|------|-------|
|---|----------------------------|------|--------------|--|----------------------------|------|-------|

| 6. OPTICAL CHARACTERISTICS | | | | | | | | | | |
|----------------------------|----------------------|-----------|---------------------------|------|------|------|------|----------|--|--|
| 6.1 OPTICAL CHA | RACTER | ISTICS OF | Ta=25°C (Backlight On) | | | | | | | |
| ПЕМ | | SYMBOL | CONDITION | MIN | TYP | MAX | UNIT | NOTE | | |
| View ing area | View ing area | | θ=0°, K <u>≥</u> 2.0 | - | (40) | - | deg | 1),2) | | |
| Contrast ratio | | К | φ=0° , θ=0° | 25 | 50 | - | - | 3),5),6) | | |
| Response time (ri | se) | tr | φ=0° , θ=0° | - | 170 | 225 | ms | 4) | | |
| Response time (fa | Response time (fall) | | φ=0° , θ=0° | - | 130 | 225 | ms | 4) | | |
| Color tone | Pod | х | | 0.48 | 0.53 | 0.58 | - | | | |
| (Primary Color) | Red | Rea y | у | | 0.25 | 0.30 | 0.35 | - | | |
| | Green | x | | 0.26 | 0.31 | 0.36 | - | | | |
| | Green | у | φ=0°, θ=0° | 0.46 | 0.51 | 0.56 | - | 7) | | |
| | Blue | х | φ=0, θ=0 | 0.11 | 0.16 | 0.21 | - | - 7) | | |
| | Diue | У | | 0.09 | 0.14 | 0.19 | - | | | |
| | White | х | | 0.26 | 0.31 | 0.36 | - | | | |
| | vvnite | у | | 0.27 | 0.32 | 0.37 | - | | | |

(Measurement condition : Hitachi standard)

Note 1)~7) : See next page.

| | - | | | | | |
|----------------------------|------|--------------|------------|----------------------------|------|-------|
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6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

| ПЕМ | MIN | TYP | MAX | UNIT | NOTE |
|-----------------------|-----|-----|-----|-------------------|------------------------------|
| Brightness | 150 | 200 | - | cd/m ² | IL=5.0mA Note 1),2) |
| Rise Time | - | 5 | - | Minute | IL=5.0mA Brightness 80% |
| Brightness Uniformity | - | - | ±30 | % | Undermentioned Note 1),4) |

(Measurement condition : Hitachi standard)

CFL : INITIAL, Ta=25°C

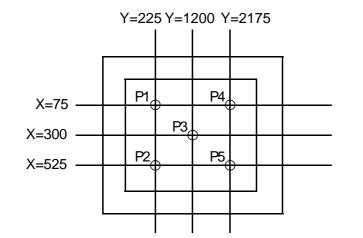
Display data should be all "ON"

The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained, when set pattern is all "Q".

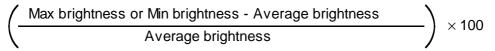
(Note 1) Measurement after 10 minutes from CFL operating. Average value of 5 points (Note 3).

(Note 2) Brightness control : 100%

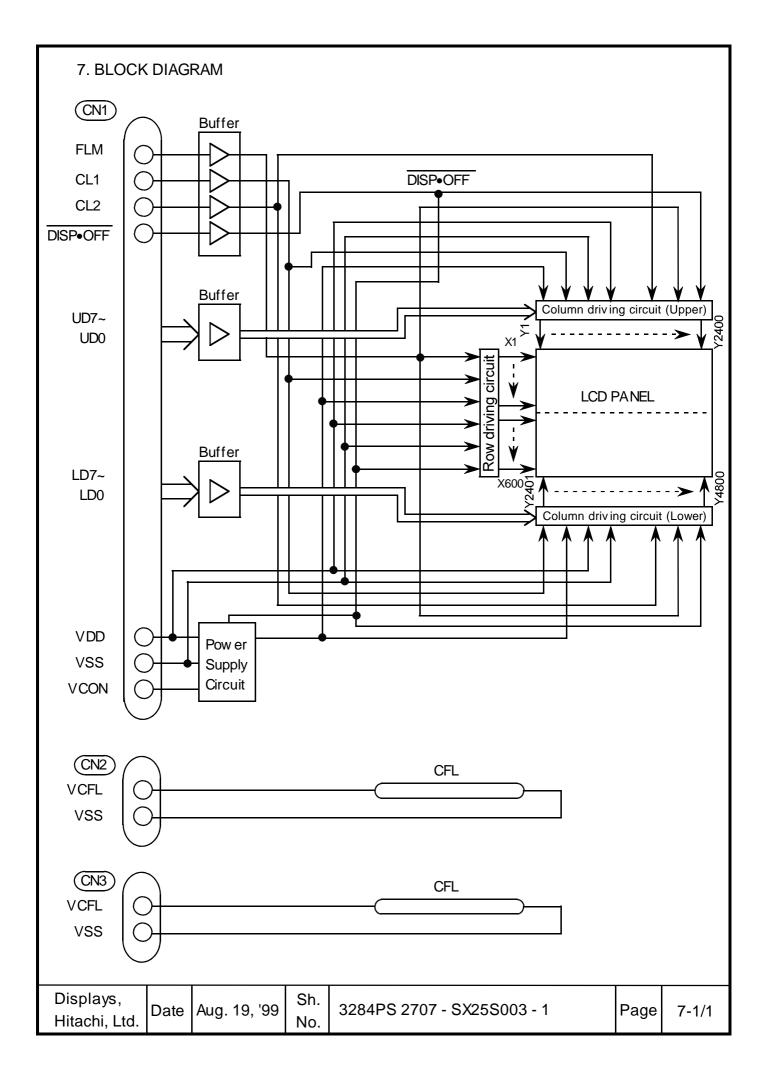
(Note 3) Measurement of the following 5 places on the display.



(Note 4) Definition of the brightness tolerance.



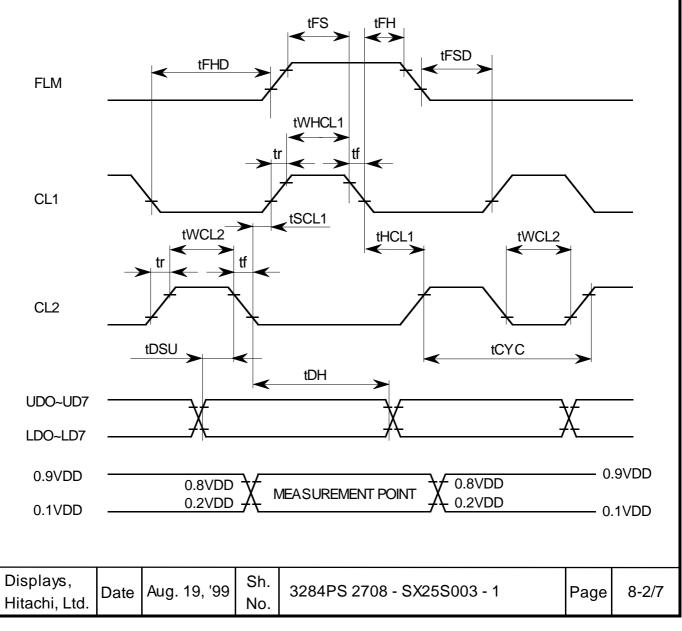
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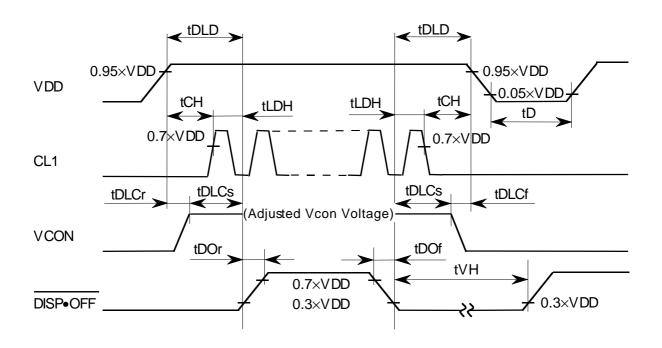
| 8. INTERFACE 8.1 TIMING CH | E TIMING CHART HART | | |
|-------------------------------|---|--|-------------|
| CL1 | _ | | |
| CL2 Dummy da | | X1 | X2 |
| UD7 | <u> </u> | X X X G X Y2393 | X_X |
| UD6 | $\begin{array}{c c} & & \\ & &$ | X X B Y2394 | <u>X_X</u> |
| UD1 | <u>R B X X X</u> Y7 Y15 | X X X G X Y2399 | X_X |
| UD0 Dummy dat | $\frac{\mathbf{G} \mathbf{X} \mathbf{R} \mathbf{X}}{\mathbf{Y}^{8} \mathbf{Y}^{16}} \mathbf{X} \mathbf{X}$ | X301 X301 Y2400 | X X X302 |
| UL7 | R B X X X Y2401 Y2409 | X X X G X Y4793 | |
| LD6 | G X R X X X Y2402 Y2410 | X X B Y4794 | X_X |
| LD1 | RXBXXX Y2407 Y2415 | X X X G X Y4799 | X_X |
| LD0 | G X R X X X Y2408 Y2416 | X X X B Y4800 | <u>X_X</u> |
| FLM (Reduction) | | | Note(1) |
| CL1 | | ∫ >0+n) × T | |
| FLM | | | |
| UD0~UD7 | | | |
| LD0~LD7 | | p9Xx600X Dummy data | XX |
| Note(1) : The int | terval of CL1 pulse must | be same including the vertical blanking period | I. |
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8.2 INTERFACE TIMING SPECIFICATION

| VDD=3.3±0.15V, VSS=0V, Vcon=1.2~2.4V, Ta=+5°C~+40°C | | | | | | | | | | | |
|---|--------|-----|-----|-----|------|--|--|--|--|--|--|
| ITEM | SYMBOL | MIN | TYP | MAX | UNIT | | | | | | |
| CL1 pulse width "H" | tWHCL1 | 150 | | | ns | | | | | | |
| Clock cycle time | tCYC | 50 | | | ns | | | | | | |
| CL2 pulse w idth | tWCL2 | 15 | | | ns | | | | | | |
| Clock set up time | tSCL1 | 110 | | | ns | | | | | | |
| Clock hold time | tHCL1 | 110 | | | ns | | | | | | |
| Clock rise fall time | tr, tf | | | 50 | ns | | | | | | |
| Data set up time | tDSU | 10 | | | ns | | | | | | |
| Data hold time | tDH | 10 | | | ns | | | | | | |
| "FLM" set up time | tFS | 120 | | | ns | | | | | | |
| "FLM" hold time | tFH | 300 | | | ns | | | | | | |
| Set up time | tFSD | 120 | | | ns | | | | | | |
| Hold time | tFHD | 120 | | | ns | | | | | | |



8.3 POWER ON / OFF SEQUENCE



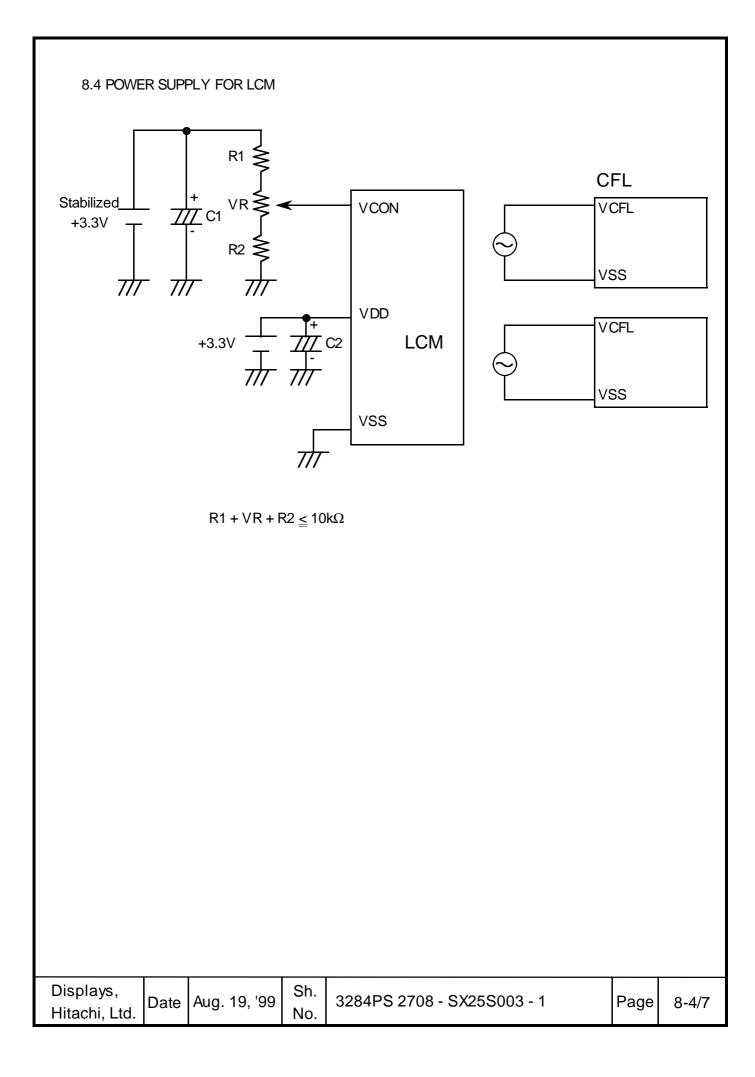
| SYMBOL | MIN | MAX | UNIT | COMMENT |
|--------|-----|-----|------|-------------|
| tDLD | 100 | | ms | |
| tCH | 0 | 200 | ms | (Note 1) |
| tLDH | 20 | | ms | |
| tDOr | | 100 | ns | |
| tDOf | | 100 | ns | (Note 2) |
| tDLCr | 0 | | ms | |
| tDLCf | 0 | | ms | |
| tDLCs | 0 | | ms | (Note 2, 3) |
| tVH | 200 | | ms | (Note 4) |

- (Note 1) Please keep the specified sequence because w rong sequence may cause permanent damage to the LCD panel.
- (Note 2) Hitachi recommends you to use DISP•OFF function. Display quality may deteriorate if you don't use DISP•OFF function.
- (Note 3) $1.2 \leq V \text{con} \leq 2.4 \text{V}$

Vcon voltage should be set up to adjusted voltage before DISP•OFF signal arises. Otherw ise, when DISP•OFF signal arises, adjusted contrast image may not be generated.

(Note 4) Please keep the specified sequeuce of DISP•OFF signal because if the tVH is short enough, LCD panel may not be restarted.

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8.5 INPUT DATA ALLOCATION TABLE

| | Data | a Signal | U D 7 | U D 6 | U D 5 | U D 4 | U D 3 | U D 2 | U D 1 | U D 0 | U D 7 | U D 6 | U D 5 | U D 4 | | U D 4 | U D 3 | U D 2 | U D 1 | U D 0 | |
|-------------------------|-------------|----------------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-----------|-------------|-------------|-------------|-------------|-------------|-------|
| | | Y | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | | 2 3 9 | 2 3 9 | 2 3 9 | 2 3 9 | 2 4 0 | |
| | х | \backslash | | | | | | | | | | | | | | 6 | 9 7 | 8 | 9 | 0 | |
| | | 1 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | | 2 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | | 3 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | PANEL | 4 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | | 5 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | UPPER | | 1 1 1 | | | | | | | | | | 1 1 1 | | | | | | | | |
| | | 298 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | | 299 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | | 300 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | | 301 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | | 302 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | 긢 | 303 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | ANE | 304 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | ς Ρ/ | 305 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | LOWER PANEL | | | 1 | | | | 1 | - | 1 1 1 | 1 | | 1 | | | | | 1 | 1 | 1 | |
| | 2 | 598 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | | 599 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | | 600 | R | G | В | R | G | В | R | G | В | R | G | В | | G | В | R | G | В | |
| | х | | 2 4 | | 4 7 | 4 7 | 4 7 | 4 7 | 4 8 | |
| | | Y | 0 1 | 0 2 | 0 3 | 0 4 | 0 5 | 0 6 | 0 7 | 0 8 | 0 9 | 1 0 | 1 1 | 1 2 | | 9 6 | 9 7 | 9 8 | 9 9 | 0 0 | |
| | Data | a Signal | L D | | L D | L D | L D | L D | L D | |
| | | - | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | 7 | 6 | 5 | 4 | | 4 | 3 | 2 | 1 | 0 | |
| | | RED GREEN BLUE | | | | | | | | | | | | | | | | | | | |
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8.6 INTERNAL PIN CONNECTION

| PIN | No. | SIGNAL | LEVEL | FUNCTION |
|----------------------|------|------------------|-----------|-------------------------------|
| 1 | | GND | - | GND |
| 2 | | CL2 | H→L | Data Shift |
| 3 | | GND | - | GND |
| 4 | | GND | - | GND |
| 5 | | CL1 | H→L | Data Latch |
| 6 | | FLM | Н | First Line Marker |
| 7 | | GND | - | GND |
| 8 | | GND | - | GND |
| 9 | | VDD | - | Pow er Supply for LCD |
| 10 |) | DISP •OFF | H/L | H:ON/L:OFF |
| 11 | 1 | GND | - | GND |
| 12 | 2 | GND | - | GND |
| 13 | 3 | GND | - | GND |
| 14 | 1 | LD7 | | |
| 15 | 5 | LD6 | I | |
| 16 | 5 | LD5 | I | |
| 17 | 7 | LD4 | H/L | Display Data (Low er Column) |
| 18 | 3 | LD3 | 11/ ⊑ | Display Data (Low er Coldmin) |
| 19 | 9 | LD2 | | |
| 20 |) | LD1 | | |
| 2′ | 1 | LD0 | | |
| 22 | 2 | GND | - | GND |
| 23 | 3 | GND | - | GND |
| 24 | 1 | GND | - | GND |
| 25 | 5 | UD0 | | |
| 26 | 6 | UD1 | | |
| 27 | 7 | UD2 | | |
| 28 | 3 | UD3 | H/L | Display Data (Upper Caluma) |
| 29 | 9 | UD4 | | Display Data (Upper Column) |
| 30 |) | UD5 | | |
| 3′ | 1 | UD6 | | |
| 32 | 2 | UD7 | | |
| 33 | 3 | GND | - | GND |
| 34 | 1 | GND | - | GND |
| 35 | 5 | GND | - | GND |
| 36 | 6 | VDD | - | Pow er Supply for LCD |
| 37 | 7 | VDD | - | Pow er Supply for LCD |
| 38 | 3 | VCON | - | Contrast Adjust |
| 39 | | N.C | - | |
| 4(| | GND | - | GND |
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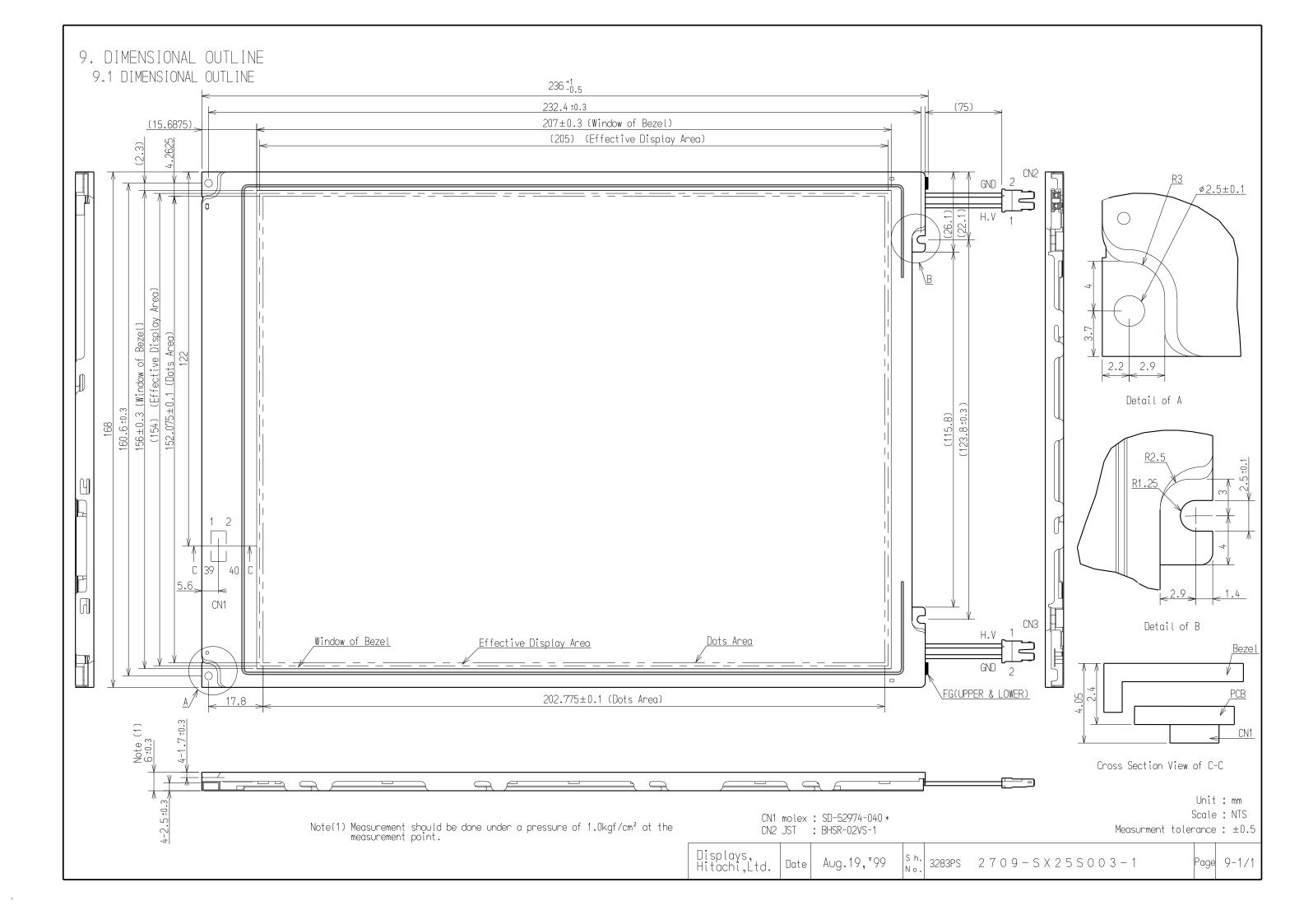
CN2 JST : BHSR-02VS-1 (Suitable Connector : JST SM02B-BHSS-1)

| PIN No. | SIGNAL | LEVEL | FUNCTION | | | | |
|---------|--------|-------|-----------------------|--|--|--|--|
| 1 | VCFL | A C | Pow er Supply for CFL | | | | |
| 2 | VSS | - | GND for CFL | | | | |

CN3 JST : BHSR-02VS-1 (Suitable Connector : JST SM02B-BHSS-1)

| PIN No. | SIGNAL | LEVEL | FUNCTION | | | | |
|---------|--------|-------|-----------------------|--|--|--|--|
| 1 | VCFL | A C | Pow er Supply for CFL | | | | |
| 2 | VSS | - | GND for CFL | | | | |

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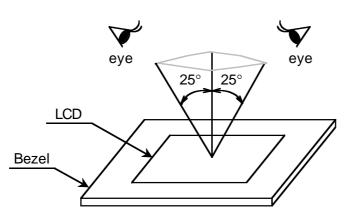


10. APPEARANCE STANDARD

10.1 A PPEARANCE INSPECTION CONDITION

Visual inspection should be done under the follow ing condition.

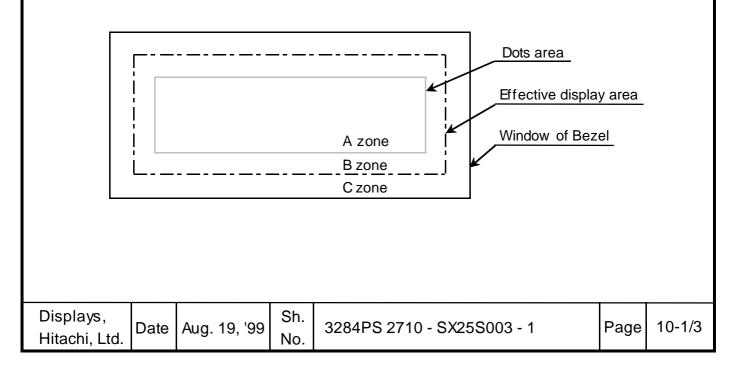
- (1) The inspection should be done in a dark room.
- (2) The CFL should be lighted with the prescribed inverter.
- (3) The distance betw een eyes of an inspector and the LCD Module is 25cm.
- (4) The view ing zone is show n the figure. View ing angle $\leq 25^{\circ}$



10.2 DEFINITION OF ZONE

A zone : The dots area specified at page 9-1/1 of this document.

- B zone : The effective display area specified at page 9-1/1 of this document.
- C zone : Area betw een the w indow of bezel line and the effective display are (B zone) line specified at page 9-1/1 of this document.



10.3 A PPEARANCE SPECIFICATION

(1) LCD A PPEARANCE

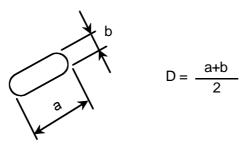
Note (1) If the problem related to this section occurs about this item, the responsible persons of both party (Customer and HITACHI) will discuss the matter detail.

| No. | ITEM | | CRITE | RIA | | APPLIED ZONE | | | |
|-----|-----------------------|--|------------------------|-----------------------|------------------------------|-----------------|--|--|--|
| | Scratches | Note (1) | Note (1) | | | | | | |
| | Dent | Same as above | Same as above | | | | | | |
| | Wrinkles in Polarizer | Same as above | | | | А | | | |
| | Bubbles | Average diameter | D (mm) | Maximum | acceptable number | | | | |
| | | D <u>≤</u> 0.2 | 2 | | ignored | | | | |
| L | | 0.2 < D <u><</u> 0.3 | 3 | | 12 | A | | | |
| | | 0.3 < D <u><</u> 0.5 | 5 | | 3 | | | | |
| | | 0.5 < D | | | none | | | | |
| | Stains, | Filamentous (Line shape) | | | | | | | |
| с | Foreign materials | Length L (mm) | Width W (mm) | | Maximum acceptable number | | | | |
| | Dark spot | L <u>≤</u> 2.0 | W <u>≤</u> 0.03 | | ignored | A,B | | | |
| | F | L <u>≤</u> 3.0 | 0.03 < W <u>≤</u> 0.05 | | 6 | | | | |
| | | L <u>≤</u> 2.5 | 0.05 < \ | <i>N</i> <u>≤</u> 0.1 | 1 | | | | |
| | | Round (Dot shape) | | | | | | | |
| D | | Average diameter D (mm) | | mum ble number | Minimum space | | | | |
| | | D < 0.2 | ign | ored | | | | | |
| | | 0.2 <u>≤</u> D < 0.3 | | 10 | 10 mm | | | | |
| | | 0.3 <u>≤</u> D < 0.4 | 5 | | 30 mm | A,B | | | |
| | | 0.4 <u>≤</u> D | n | one | | | | | |
| | | The total numberFilamentous + Round = 10 | | | | | | | |
| | | Those wiped out easily are acceptable | | | | | | | |
| | Color tone | Note (1) | Note (1) | | | | | | |
| | Color uniformity | Same as above | | | | А | | | |

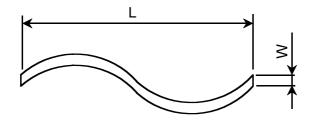
| | | Displays, Hitachi, Ltd. | Date | Aug. 19, '99 | Sh. No. | 3284PS 2710 - SX25S003 - 1 | Page | 10-2/3 |
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|--|--|----------------------------|------|--------------|------------|----------------------------|------|--------|

| No. | ITEM | | CRITERIA | | | | |
|-----|---------------------------------|---|--|---------------------------------|------------------|---|--|
| | Contrast irregularity (Spot) | Average diameter D (mm) | Contrast | Maximum acceptable number | Minimum space | | |
| | | D <u></u> ≤0.25 | Taka | ignored | | | |
| L | | 0.25 <d<u><0.35</d<u> | To be judged by | 10 | 20mm | A | |
| | | 0.35 <d<u><0.5</d<u> | HITACHI | 4 | 20mm | | |
| | | 0.5 <d<u>≤0.7</d<u> | STANDARD | 3 | 50mm | | |
| С | | 0.7 <d< td=""><td colspan="2">0.7<d< td=""><td colspan="2">none —</td></d<></td></d<> | 0.7 <d< td=""><td colspan="2">none —</td></d<> | | none — | | |
| U | Contrast irregularity (Line) | Width W (mm) | Length L (mm) | Maximum acceptable number | Minimum space | | |
| _ | (A pair of scratches) | W <u>≤</u> 0.25 | L <u>≤</u> 1.2 | 2 | 20mm | | |
| D | | W <u>≤</u> 0.2 | L <u>≤</u> 1.5 | 3 | 20mm | A | |
| | | W <u>≤</u> 0.15 | L <u>≤</u> 2.0 | 3 | 20mm | | |
| | | W <u>≤</u> 0.1 | L <u>≤</u> 3.0 | 4 | 20mm | | |
| | | The who | The w hole number 6 | | | | |
| | Rubbing Scratch | Note (1) | | | | _ | |

Note (2) Definition of Average diameter (D)



Note (3) Definition of Length (L) and Width (W)

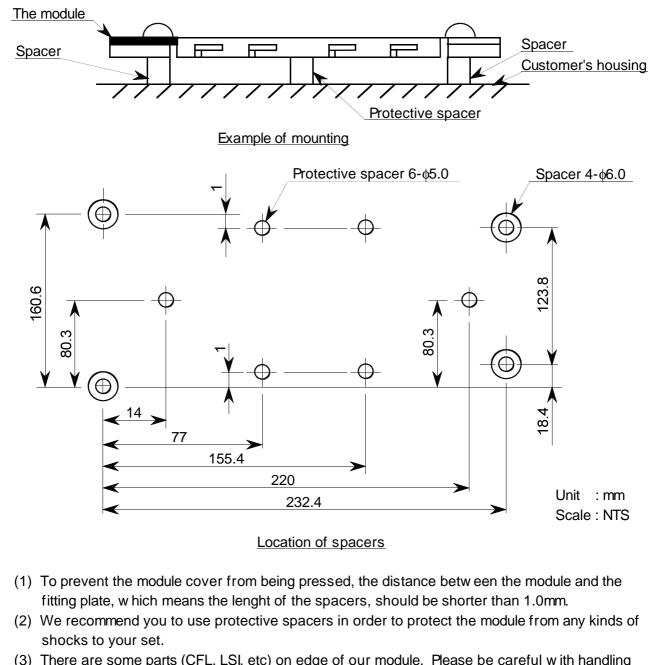


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11. PRECAUTION IN DESIGN

11.1 MOUNTING PRECAUTION

Please mount the LCD Module using mounting holes arranged in 4 corners, and please pay attention to the follow ings.



- (3) There are some parts (CFL, LSI, etc) on edge of our module. Please be careful with handling when you assemle (without any stress).
- (4) When you insert the connector to our module, please be careful with inserting it without slant.

11.2 PRECAUTIONS AGAINST ELECTROSTATIC DISCHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band etc. And don't touch I/F pins directly.

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11.3 POWER ON SEQUENCE

Input signals should not be applied to LCD module before power supply voltage is applied and reaches to specified voltage $(3.3\pm0.15V)$.

If the above sequence is not kept, C-MOS LSIs of LCD module may be damaged due to latch up phenomenon.

11.4 HANDLING PRECAUTIONS

- (1) Since the polarizer on the top, and the aluminum plate on the bottom tend to be easily damaged, they should be handled with full care so as not to get them touched, pushed or rubbed by a piece on glass, tw eezers and anything else which are harder than a pencil lead 3H.
- (2) As the adhesives used for adhering upper/low er polarizers and aluminum plate are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, tuluene, ethanole and isopropylalcohol. The following solvents are recommended for use : Normal hexane

Please contact us when it is necessary for you to use chemicals other than the above.

- Lightly w ipe to clean the dirty surface w ith absorbent cotton or other soft material like chamois, soaked in the recommended chemicals w ithout scrubbing it hardly.
 To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to w ipe it w ith absorbent cotton.
- (4) Immediately wipe off saliva or water drop attached on the display area because it may cause deformation or faded color.
- (5) Fogy dew deposited on the surface may cause a damage, stain or dirt to the polarizer. When you need to take out the LCD module from some place at low temperature for test, etc. It is required to be warmed them up to be temperature higher than room temperature before taking them out.
- (6) Touching the display area or I/F pins with bare hands or contaminating them are prohibited, because the stain on the display area and poor insulation betw een terminals are often caused by being touched with bare hands.

(Some cosmetics are detrimental to polarizers.)

- (7) In general, the glass is fragile so that it, especially on its periphery, tends to be cracked or chipped in handling. Please do not give the LCD module sharp shocks caused by falling etc.
- Maximum pressure to the surface must be less than 1.96×10⁴ Pa (0.2kgf/cm²).
 And if the pressure area is less than 1cm², maximum pressure must be less than 1.96N (0.2kgf).

| | | | | | | |
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11.5 OPERATION PRECAUTION

- Using a LCM module beyond its maximum ratings may result in its permanent destruction.
 LCM module's should usually be used under recommended operating conditions show n in chapter
 5. Exceeding any of these conditions may adversely affect its reliability.
- (2) Response time will be extremely delayed at low er temperature than the specified operating temperature range and on the other hand LCD's show s dark blue color at higher temperature. How ever those phenomena do not mean defects of the LCD module. Those phenomena will disappear in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some display patterns will be abnormally displayed.
- (4) A slight dew depositing on terminals may cause electrochemical reaction which leads to terminal open circuit. Please operate the LCD module under the relative condition of 40°C 85%RH.

11.6 STORAGE

In case of storing LCD module for a long period of time (for instance, for years) for the purpose of replacement use, the follow ing precautions necessary.

- (1) Store the LCD modules in a dark place ; do not expose them to sunlight or ultraviolet rays.
- (2) Keep the temperature betw een 10°C and 35°C at normal humidity.
- (3) Store the LCD modules in the container which is used for shipping from us.

11.7 SAFETY

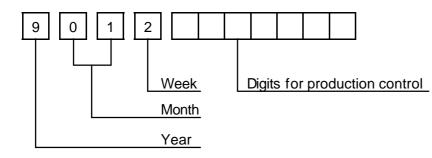
- (1) The LCD modules include Cold Cathode Fluorescent Lamp (CFL). CFL contains a small amount of mercury. Please follow local ordinances or regulations for disposal.
- (2) It is recommendable to crash dameged or unnecessary LCD's into pieces and w ash off liquid crystal by either of solvents such as acetone and ethanol, w hich should be burned up later.
- (3) When any liquid leaked out of a damaged glass cell comes in contact with your hands please wash it off well with soap and water.

| | | | | | . | |
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12. DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 4 digits for production lot and 6 or 7 digits for production control.



| Year | Figure in lot mark |
|------|-----------------------|
| 1999 | 9 |
| 2000 | 0 |
| 2001 | 1 |
| 2002 | 2 |

| Month | Figure in lot mark | Month | Figure in lot mark |
|-------|-----------------------|-------|-----------------------|
| Jan. | 01 | July | 07 |
| Feb. | 02 | Aug. | 08 |
| Mar. | 03 | Sep. | 09 |
| Apr. | 04 | Oct. | 10 |
| May | 05 | Nov. | 11 |
| June | 06 | Dec. | 12 |

| Week (day in Calender) | Figure in lot mark |
|---------------------------|-----------------------|
| 1~7 | 1 |
| 8~14 | 2 |
| 15~21 | 3 |
| 22~28 | 4 |
| 29~31 | 5 |
| 20 01 | 5 |

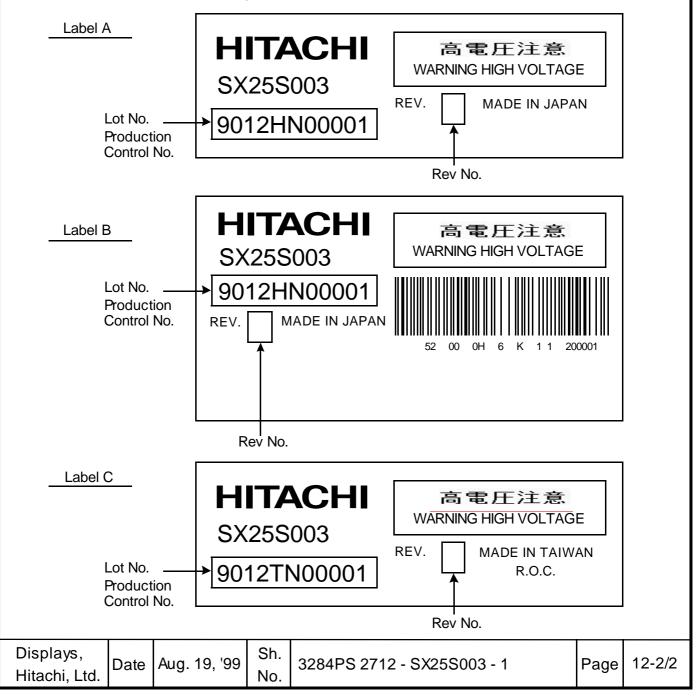
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|----------------------------|------|--------------|------------|----------------------------|------|--------|

12.2 REVISION

| REV No. | ПЕМ | LOT No. | PRODUCTION CONTROL No. |
|---------|-----|---------|------------------------|
| А | | | 00001~ |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |

12.3 LOCATION OF LOT MARK

Either Label A or Label B is being attached on the back side of LCM.



13. PRECAUTION FOR USE

(1) A limit sample should be provided by the both parities on an occasion when the both parties agree to its necessity.

Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

- (2) On the follow ing occasions, the handling of the problem should be decided through discussion and agreement betw een responsible persons of the both parties.
 - (1) When a question is arisen in the specifications.
 - (2) When a new problem is arisen which is not specified in the specifications.
 - (3) When an inspection specification change or operating condition change by customer is reported to HITACHI, and some problem is arisen in the specification due to the change.
 - (4) When a new problem is arisen at the customer's operating set for sample evaluation
- (3) Regarding the treatment for maintenance and repairing, both parties will discuss it in six month later after latest delivery of this product.

The precaution that should be observed when handling LCM have been explained above. If any points are unclear or if you have any requests, please contact Hitachi.

| | | - | | | - | |
|----------------------------|------|--------------|------------|----------------------------|------|--------|
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