

# JUNCTION FIELD EFFECT TRANSISTOR 2SK3230B

# N-CHANNEL SILICON JUNCTION FIELD EFFECT TRANSISTOR FOR IMPEDANCE CONVERTER OF ECM

### **DESCRIPTION**

The 2SK3230B is suitable for converter of ECM.

General-purpose product.

#### **FEATURES**

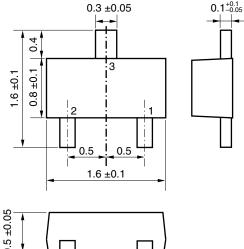
- · Low noise:
  - -108.5 dB TYP. (V<sub>DD</sub> = 2.0 V, C = 5 pF, R<sub>L</sub> = 2.2 k $\Omega$ )
- Especially suitable for audio and telephone
- Small package:

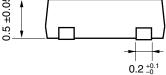
SC-89 (TUSM)

### ORDERING INFORMATION

PART NUMBER	PACKAGE		
2SK3230B	SC-89 (TUSM)		

# PACKAGE DRAWING (Unit: mm)

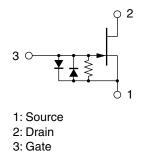




# ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage ( $V_{GS} = -1.0 \text{ V}$ )	VDSX	20	V
Gate to Drain Voltage	VgDo	-20	V
Drain Current	lσ	10	mA
Gate Current	lg	10	mA
Total Power Dissipation	Рт	100	mW
Junction Temperature	$T_j$	125	°C
Storage Temperature	Tstg	-55 to +125	°C

#### **EQUIVALENT CIRCUIT**



Caution Please take care of ESD (Electro Static Discharge) when you handle the device in this document.

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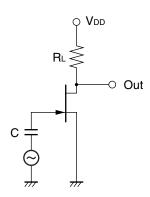
# **ELECTRICAL CHARACTERISTICS (TA = 25°C)**

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CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Cut-off Current	Ipss	V <sub>DS</sub> = 2.0 V, V <sub>GS</sub> = 0 V	90	200	430	μΑ
Gate Cut-off Voltage	V <sub>GS(off)</sub>	$V_{DS} = 2.0 \text{ V}, I_{D} = 1.0 \ \mu\text{A}$		-0.37	-1.0	V
Forward Transfer Admittance	<b>y</b> fs1	$V_{DS} = 2.0 \text{ V}, I_{D} = 30 \mu\text{A}, f = 1.0 \text{ kHz}$	300	480		μS
	<b>y</b> fs2	V <sub>DS</sub> = 2.0 V, V <sub>GS</sub> = 0 V, f = 1.0 kHz	750	1300		μS
Input Capacitance	Ciss	V <sub>DS</sub> = 2.0 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz		4.0		pF
Voltage Gain	Gv	$V_{DD}$ = 2.0 V, C = 5 pF, R <sub>L</sub> = 2.2 k $\Omega$ ,		-1.0		dB
		V <sub>IN</sub> = 10 mV, f = 1 kHz				
Noise Voltage	NV	$V_{DD} = 2.0 \text{ V, C} = 5 \text{ pF, RL} = 2.2 \text{ k}\Omega,$		-108.5		dB
		A-curve				

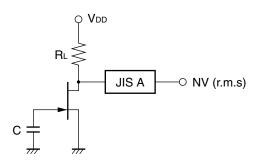
# IDSS CLASSIFICATION

MARKING	CE	CF	СН	CJ
Ibss (µA)	90 to 180	150 to 240	210 to 350	320 to 430

# **VOLTAGE GAIN TEST CIRCUIT**



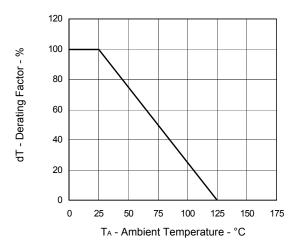
# NOISE VOLTAGE TEST CIRCUIT



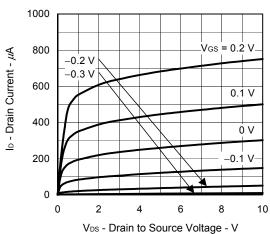
les - Gate to Source Current - µA

# TYPICAL CHARACTERISTICS (TA = 25°C)

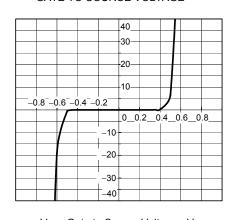
#### DERATING FACTOR OF POWER DISSIPATION



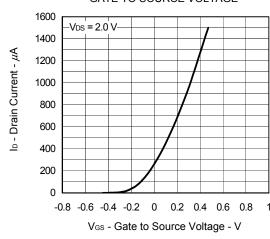
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



GATE TO SOURCE CURRENT vs. GATE TO SOURCE VOLTAGE

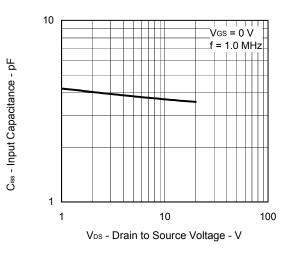


DRAIN CURRENT vs.
GATE TO SOURCE VOLTAGE

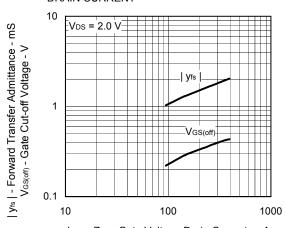


V<sub>GS</sub> - Gate to Source Voltage - V

# INPUT CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

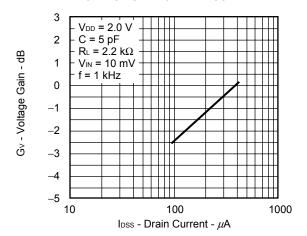


FORWARD TRANSFER ADMITTANCE AND GATE CUT-OFF VOLTAGE vs. ZERO GATE VOLTAGE DRAIN CURRENT

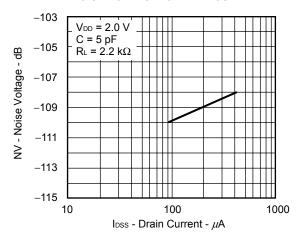


 ${\sf I}_{\sf DSS}$  - Zero Gate Voltage Drain Current -  $\mu{\sf A}$ 

# VOLTAGE GAIN vs. DRAIN CURRENT



# NOISE VOLTAGE vs. DRAIN CURRENT



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