

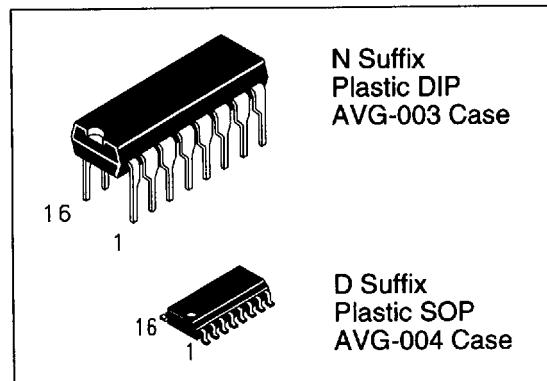
Available Q2, 1995

Dual 4-Input Multiplexer

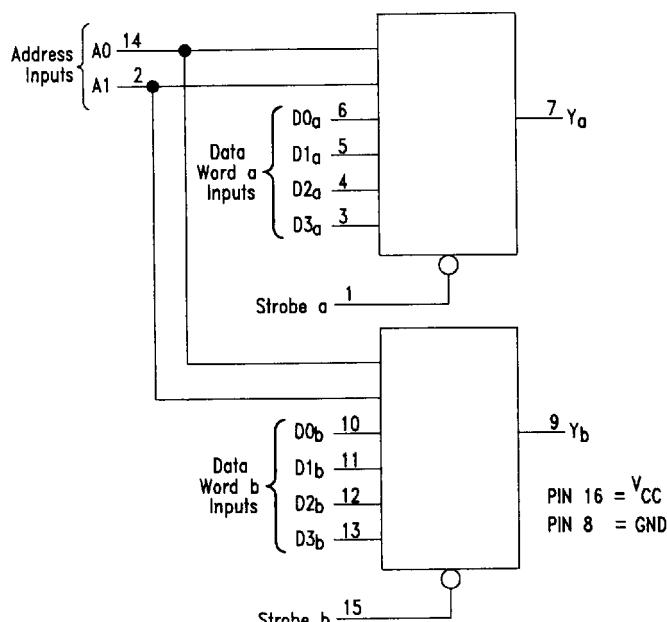
This device is a high speed, dual 4-input multiplexer with common address inputs and individual strobe inputs for each section. It can select two lines of data from four sources. The two buffered outputs present data in the true(non-inverted) form. In addition to multiplexer operation, it can act as a function generator and generate any two functions of three variables.

- Advanced very high speed CMOS
- Outputs source/sink 24 mA
- Transmission line driving 50 ohms
- ACT has TTL compatible inputs
- Operation from 2 to 6 volts guaranteed
- DC & AC Parameters guaranteed over -40 to +85°C

**DV74AC153
DV74ACT153**



LOGIC DIAGRAM



PIN ASSIGNMENT

Strobe a	1	●	16	V _{CC}
A1	2		15	Strobe b
D3 _a	3		14	A0
D2 _a	4		13	D3 _b
D1 _a	5		12	D2 _b
D0 _a	6		11	D1 _b
Y _a	7		10	D0 _b
GND	8		9	Y _b

TRUTH TABLE

Address Inputs		Inputs					Output
A0	A1	Strobe	D0	D1	D2	D3	Y
X	X	H	X	X	X	X	L
L	L	L	L	X	X	X	L
L	L	L	H	X	X	X	H
H	L	L	X	L	X	X	L
H	L	L	X	H	X	X	H
L	H	L	X	X	L	X	L
L	H	L	X	X	H	X	H
H	H	L	X	X	X	L	L
H	H	L	X	X	X	H	H

H=HIGHVoltageLevel

L=LOWVoltageLevel

X=EitherLoworHighLogicLevel

ABSOLUTE MAXIMUM RATINGS

Maximum ratings are those values beyond which damage to the device may occur.

Symbol	Parameter	AC153, ACT153	Unit
V _{CC}	DC Supply Voltage (Referenced to GND)	– 0.5 to +7.0	V
V _{IN}	DC Input Voltage (Referenced to GND)	– 0.5 to V _{CC} +0.5	V
V _{OUT}	DC Output Voltage (Referenced to GND)	– 0.5 to V _{CC} +0.5	V
I _{IN}	DC Input Current, per Pin	± 20	mA
I _{OUT}	DC Output Sink/Source Current, per Pin	± 50	mA
I _{CC}	DC V _{CC} or GND Current per Output Pin	± 50	mA
T _{STG}	Storage Temperature	– 65 to +150	°C

GUARANTEED OPERATING CONDITIONS

Symbol	Parameter	Min	Typ	Max	Unit
V _{CC}	Supply Voltage	'AC	2.0	5.0	6.0
		'ACT	4.5	5.0	5.5
V _{IN} , V _{OUT}	DC Input Voltage, Output Voltage, (Ref. to GND)	0		V _{CC}	V
t _r , t _f	Input Rise and Fall Time (Note 1) 'AC Devices	V _{CC} @ 3.0 V		150	ns/V
		V _{CC} @ 4.5 V		40	ns/V
		V _{CC} @ 5.5 V		25	ns/V
t _r , t _f	Input Rise and Fall Time (Note 2) 'ACT Devices	V _{CC} @ 4.5 V		10	ns/V
		V _{CC} @ 5.5 V		8.0	ns/V
T _A	Operating Ambient Temperature Range	–40		85	°C
C _{PD}	Power Dissipation Capacitance	V _{CC} = 5.0 V	60		pF
C _{IN}	Input Capacitance V _{CC} = 5.0 V	V _{CC} = 5.0 V	4.5		pF

1. V_{IN} from 30% to 70% V_{CC}

2. V_{IN} from 0.8 to 2.0 V

AC — 153

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	V _{CC} (V)	AC153			Unit
				T _A = +25°C	T _A = –40 to +85°C		
				Typ	Guaranteed Limits		
V _{IH}	Minimum High Level Input Voltage	V _{OUT} = 0.1V or V _{CC} – 0.1 V	3.0 4.5 5.5	1.5 2.25 2.75	2.1 3.15 3.85	2.1 3.15 3.85	V
V _{IL}	Maximum Low Level Input Voltage	V _{OUT} = 0.1V or V _{CC} – 0.1 V	3.0 4.5 5.5	1.5 2.25 2.75	0.9 1.35 1.65	0.9 1.35 1.65	V
V _{OH}	Minimum High Level Output Voltage	I _{OUT} = –50 μA	3.0 4.5 5.5	2.99 4.49 5.49	2.9 4.4 5.4	2.9 4.4 5.4	V
		V _{IN} = V _{IL} or V _{IH} I _{OH} = –24mA	3.0 4.5 5.5		2.56 3.86 4.86	2.46 3.76 4.76	V

153

AC CHARACTERISTICS

Symbol	Parameter ($C_L = 50 \text{ pF}$)	Vcc $\pm 10\%$ (V)	AC153				Unit	
			TA = +25°C		TA = -40 to +85°C			
			Min	Max	Min	Max		
tPLH	Propagation Delay An to Y	3.3	2.5	15	2.5	17.5	ns	
		5.0	2.0	11	2.0	12.5		
tPHL	Propagation Delay Strobe to Y	3.3	3.0	14.5	2.5	16.5	ns	
		5.0	2.5	11.0	2.0	12.0		
tPLH	Propagation Delay Dn to Y	3.3	2.5	13.5	2.0	16.0	ns	
		5.0	1.5	9.5	1.5	11.0		
tPHL	Propagation Delay Dn to Y	3.3	2.5	11.0	2.0	12.5	ns	
		5.0	2.0	8.0	1.5	9.0		
tPLH	Propagation Delay Dn to Y	3.3	2.5	12.5	2.0	14.5	ns	
		5.0	1.5	9.0	1.5	10.5		
tPHL	Propagation Delay Dn to Y	3.3	1.5	11.5	1.5	13.0	ns	
		5.0	1.5	8.5	1.5	10.0		

ACT — 153**DC ELECTRICAL CHARACTERISTICS**

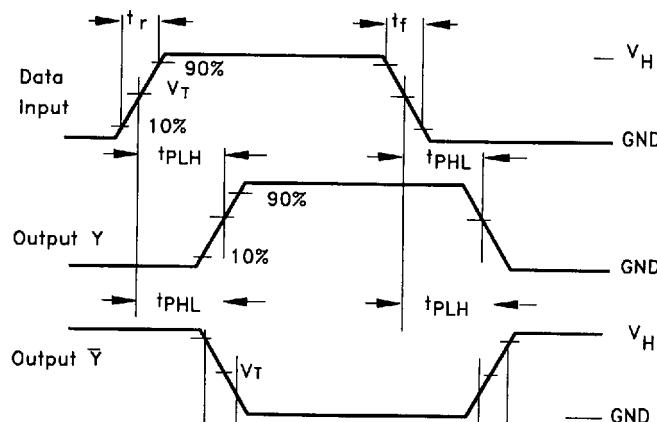
Symbol	Parameter	Conditions	Vcc (V)	ACT153			Unit
				TA = +25°C		TA = -40 to +85°C	
				Typ	Guaranteed Limits	Typ	
V _{IH}	Minimum High Level Input Voltage	V _{OUT} = 0.1V or V _{CC} - 0.1 V	4.5 5.5	1.5 1.5	2.0 2.0	2.0 2.0	V
V _{IL}	Maximum Low Level Input Voltage	V _{OUT} = 0.1V or V _{CC} - 0.1 V	4.5 5.5	1.5 1.5	0.8 0.8	0.8 0.8	V
V _{OH}	Minimum High Level Output Voltage	I _{OUT} = -50 μA	4.5 5.5	4.49 5.49	4.4 5.4	4.4 5.4	V
		V _{IN} = V _{IL} or V _{IH} I _{OH} -24mA -24 mA	4.5 5.5		3.86 4.86	3.76 4.76	V
V _{OL}	Maximum Low Level Output Voltage	I _{OUT} = 50 μA	4.5 5.5	0.001 0.001	0.1 0.1	0.1 0.1	V
		V _{IN} = V _{IL} or V _{IH} I _{OL} 24mA 24 mA	4.5 5.5		0.36 0.36	0.44 0.44	V
I _{IN}	Maximum Input Leakage Current	V _I = V _{CC} , GND	5.5		±0.1	±1.0	μA
ΔI _{CCT}	Additional Max I _{CC} /Input	V _I = V _{CC} - 2.1 V	5.5	0.6		1.5	mA

Symbol	Parameter	Conditions	Vcc (V)	ACT153		Unit	
				TA = +25°C			
				Typ	Guaranteed Limits		
I _{CC}	Maximum Quiescent Supply Current	V _{IN} = V _{CC} or GND	5.5		8.0 80	μA	

AC CHARACTERISTICS

Symbol	Parameter (C _L = 50 pF)	V _{CC} ±10% (V)	ACT153				Unit	
			TA = +25°C		TA = -40°C to +85°C			
			Min	Max	Min	Max		
t _{PLH}	Propagation Delay, A _n to Y	5.0	3.0	11.5	2.0	13.5	ns	
t _{PHL}	Propagation Delay, A _n to Y	5.0	3.0	11.5	2.5	13.5	ns	
t _{PLH}	Propagation Delay, Strobe to Y	5.0	2.0	10.5	2.0	12.5	ns	
t _{PHL}	Propagation Delay, Strobe to Y	5.0	3.0	9.5	2.5	11.0	ns	
t _{PLH}	Propagation Delay, D _n to Y	5.0	2.5	9.5	2.0	11.0	ns	
t _{PHL}	Propagation Delay, D _n to Y	5.0	2.0	9.5	2.0	11.0	ns	

SWITCHING WAVEFORMS



Input and output threshold voltage:
 $V_T = 50\% \text{ V}_{CC}$ for AC; 1.5V for ACT
 $V_H = \text{V}_{CC}$ for AC, 3V for ACT