The RF Line Wideband Linear Amplifier

... designed for amplifier applications in 50 to 100 ohm systems requiring wide bandwidth, low noise and low distortion. This hybrid provides excellent gain stability with temperature and linear amplification as a result of the push–pull circuit design.

• Specified Characteristics at V_{CC} = 24 V, T_C = 25°C:

Frequency Range — 10-400 MHz

Output Power — 1580 mW Typ @ 1 dB Compression, f = 200 MHz,

VCC = 28 V

Power Gain — 22 dB Typ @ f = 100 MHz

PEP - 650 mW Min @ -32 dB IMD

Noise Figure — 4 dB Typ @ f = 100 MHz

ITO - 46 dBm @ f = 300 MHz

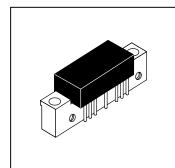
- · All Gold Metallization for Improved Reliability
- · Unconditional Stability Under All Load Conditions

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
DC Supply Voltage	VCC	28	Vdc
RF Power Input	Pin	+14	dBm
Operating Case Temperature Range	TC	-20 to +100	°C
Storage Temperature Range	T _{stg}	-40 to +100	°C

CA2842C

22 dB 10-400 MHz 1.2 WATTS WIDEBAND LINEAR AMPLIFIER



CASE 714F-03, STYLE 1 [CA (POS. SUPPLY)]

ELECTRICAL CHARACTERISTICS (T_C = 25°C, V_{CC} = 24 V, 50 Ω system unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Frequency Range	BW	10	_	400	MHz
Gain Flatness (f = 10-400 MHz)	_	_	±0.5	±1	dB
Power Gain (f = 100 MHz)	PG	21	22	23	dB
Noise Figure, Broadband (f = 100 MHz)	NF	_	4	5	dB
Power Output — 1 dB Compression (f = 10-200 MHz, V _{CC} = 28 V)	Po1 dB	1260	1580	_	mW
Power Output — 1 dB Compression (f = 200–400 MHz, V _{CC} = 28 V)	P _{o1 dB}	630	_	_	mW
Third Order Intercept (See Figure 10, f ₁ = 10-400 MHz, See Fig. 10)	ITO	42	44	_	dBm
Input/Output VSWR (f = 10-400 MHz)	VSWR	_	1.3:1	1.5:1	_
Second Harmonic Distortion (P ₀ = 100 mW, f _{2H} = 300 MHz)	d _{SO}	_	_	-50	dB
Peak Envelope Power (Two Tone Distortion Test — See Figure 10) (f = 200 MHz @ -32 dB IMD)	PEP	650	1000	_	mW
Supply Current	Icc	200	230	250	mA



TYPICAL CHARACTERISTICS

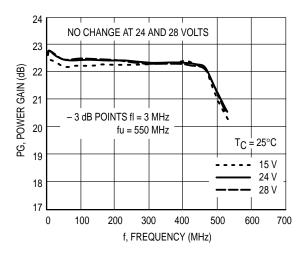


Figure 1. Power Gain versus Voltage

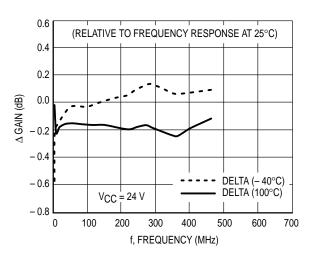


Figure 2. Relative Power Gain versus Temperature

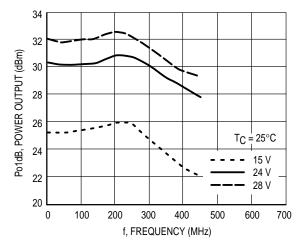


Figure 3. 1 dB Compression versus Voltage

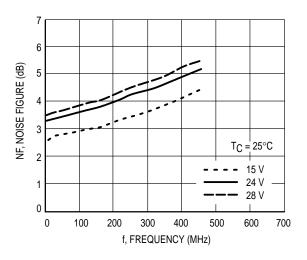


Figure 4. Noise Figure versus Voltage

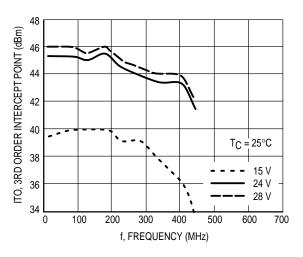


Figure 5. Third Order Intercept versus Voltage

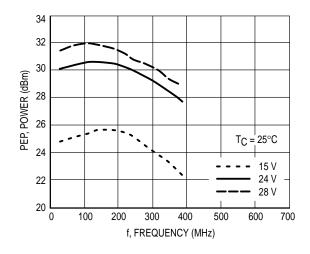
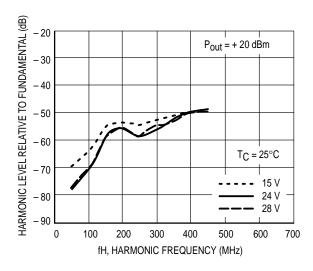


Figure 6. Peak Envelope Power versus Voltage





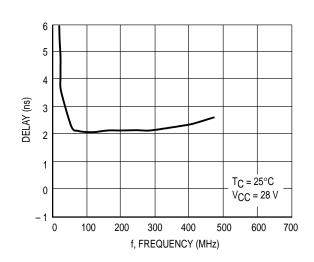


Figure 8. Group Delay versus Frequency

Biased at 24 Volts

 $T_C = 25^{\circ}C$ $Z_O = 50\Omega$

Frequency (MHz)	S11		S21		S12		S22	
	Mag	Ang	Mag	Ang	Mag	Ang	Mag	Ang
10	-15.8	62	22.8	-168	-27	15	-20.2	29
50	-26.5	20	22.5	146	-27	-25	-24	15
100	-25.5	25	22.5	111	-27.5	-56	-22.5	-16
200	-20.5	-7	22.5	26	-27.9	-117	-18.1	-73
300	-17.2	-48	22.5	-51	-28.5	-170	-16.5	-125
400	-18.8	-129	22.4	-126	-28.3	114	-22.5	156

Magnitude in dB, Phase Angle in degrees.

Table 1. S-Parameters

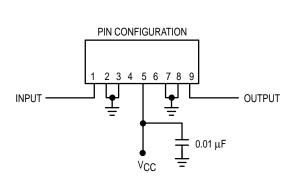
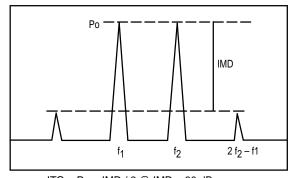


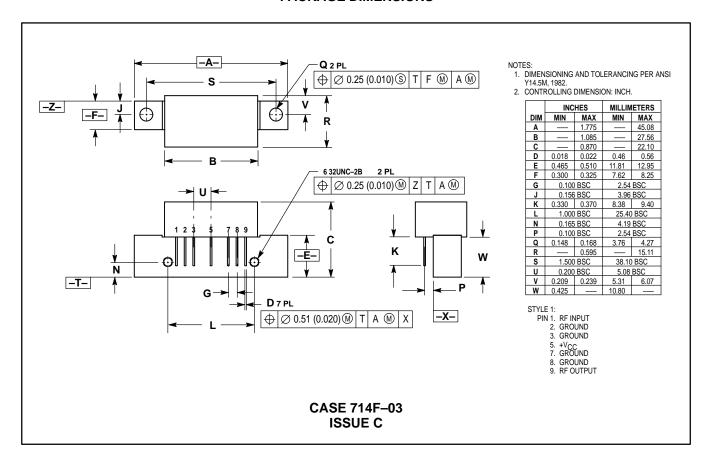
Figure 9. External Connections



ITO = Po + IMD / 2 @ IMD > 60 dB $PEP = 4 \times Po @ IMD = -32 dB$

Figure 10. Intermodulation Test

PACKAGE DIMENSIONS



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