

## Description

The S358 consists of two independent high gain Internally frequency compensated operational amplifiers designed to operate from a single power supply over a wide range of voltage.

## Features

- Input common mode voltage range includes ground.
- Internally frequency compensated for unity gain.
- Large DC voltage gain : 100dB
- Wide bandwidth for unity gain : 1 MHz
- Very low power consumption.
- Wide supply voltage range : Single : 3V ~ 30V, Dual :  $\pm 1.5 \sim \pm 15V$

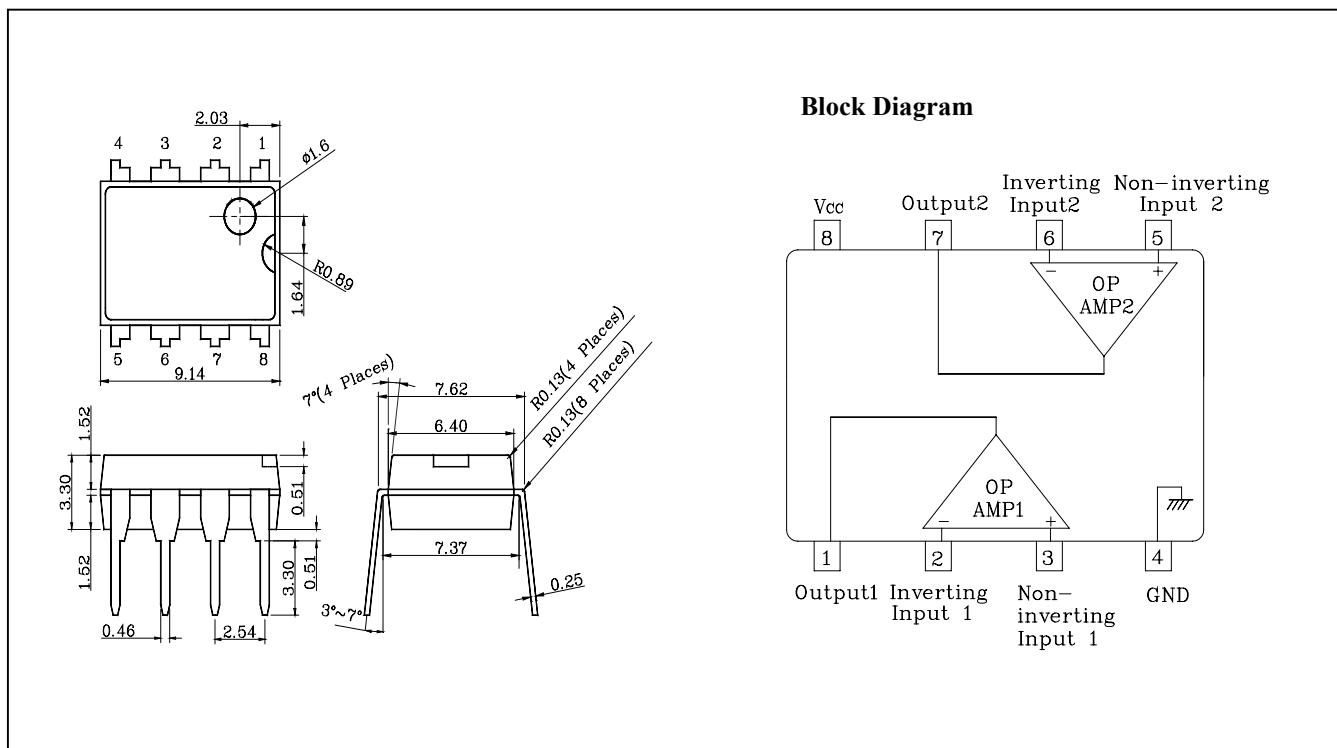
## Applications

- Transducer amplifier.
- DC gain blocks.
- Conventional operational amplifiers.

## Ordering Information

Type NO.	Marking	Package Code
S358P	S358P	DIP-8

## Outline Dimensions

**unit : mm**


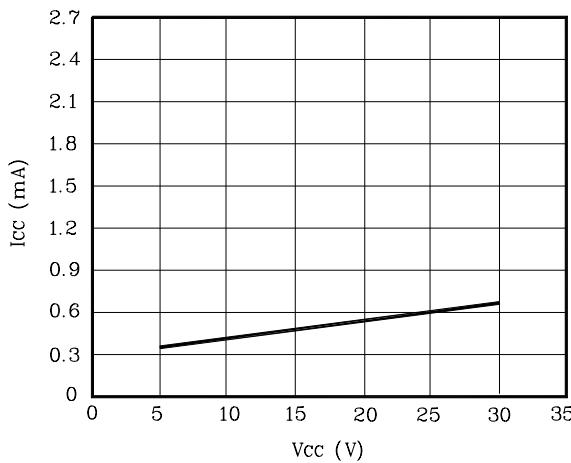
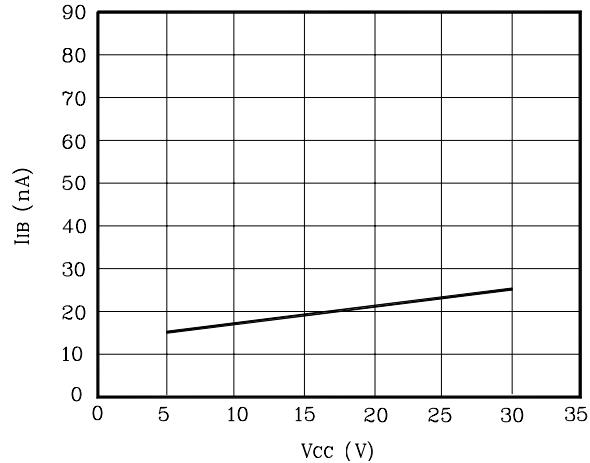
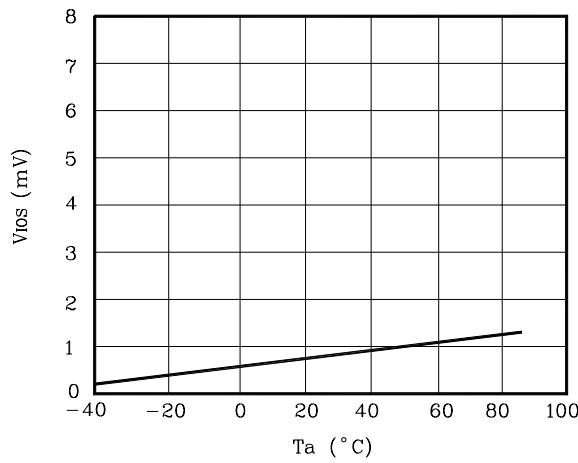
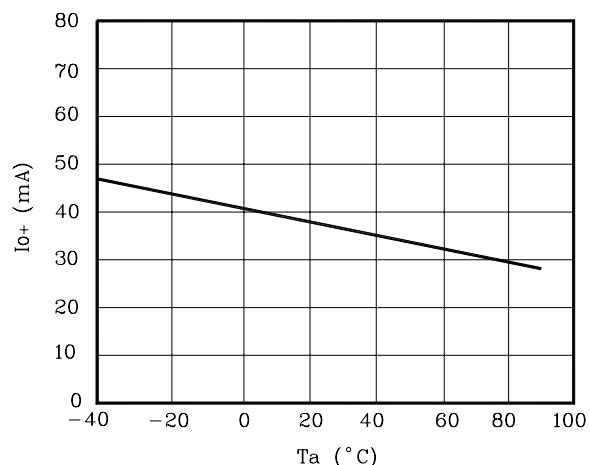
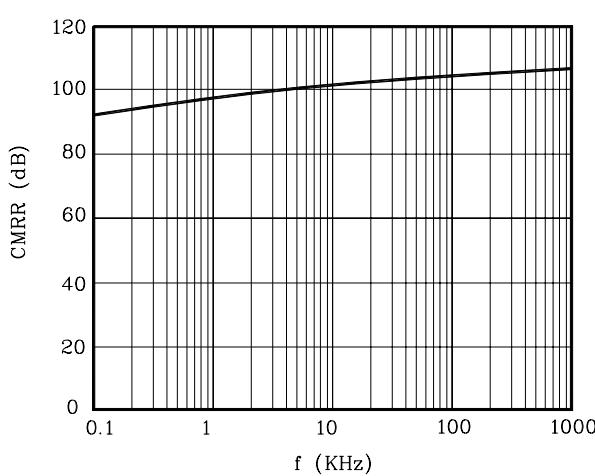
**Absolute maximum ratings**

Characteristic	Symbol	Ratings	Unit
Supply voltage	V <sub>CC</sub>	36 or $\pm 18$	V
Differential input voltage	V <sub>IND</sub>	32	V
Input voltage	V <sub>IN</sub>	-0.3 ~ +32	V
Power Dissipation	P <sub>D</sub>	570	mW
Operating temperature	T <sub>opr</sub>	-45 ~ +85	°C
Storage temperature	T <sub>stg</sub>	-55 ~ 150	°C

**Electrical Characteristics**(Unless otherwise specified, V<sub>CC</sub> = 5V and -45 °C ≤ Ta ≤ +85 °C)

Characteristic	Symbol	Test Condition		Min.	Typ.	Max.	Unit
Input offset voltage	V <sub>IOS</sub>	5V ≤ V <sub>CC</sub> ≤ 30V	(Ta=25 °C)	-	±2	±7	mV
		R <sub>g</sub> = 0Ω, 0V ≤ V <sub>IC</sub> ≤ V <sub>CC</sub> -1.5V		-	-	±9	
Input offset voltage drift	ΔV <sub>IOS</sub> /ΔT	R <sub>g</sub> = 0Ω		-	7	-	μV/ °C
Input offset current	I <sub>IOS</sub>	-		(Ta=25 °C)	-	±5	±50
					-		±150
Input offset current drift	ΔI <sub>IOS</sub> /ΔT	-		-	10	-	pA/ °C
Input bias current	I <sub>IB</sub>	-		(Ta=25 °C)	-	45	250
					-	40	500
Input common mode voltage range	V <sub>ICR</sub>	V <sub>CC</sub> = 30V		(Ta=25 °C)	0	-	V <sub>CC</sub> -1.5
					0	-	V <sub>CC</sub> -2
Supply current	I <sub>CC</sub>	V <sub>CC</sub> = 30V, R <sub>L</sub> = ∞		-	1	2	mA
		V <sub>CC</sub> = 5V, R <sub>L</sub> = ∞		-	0.7	1.2	
Large signal voltage gain	G <sub>V</sub>	V <sub>CC</sub> = 15V R <sub>L</sub> ≥ 2 KΩ		(Ta=25 °C)	25	100	V/mV
					15	-	
Output voltage swing	V <sub>OH</sub>	V <sub>CC</sub> = 30V		R <sub>L</sub> =2 KΩ	26	-	V
				R <sub>L</sub> =10 KΩ	27	28	
	V <sub>OL</sub>	V <sub>CC</sub> = 5V, R <sub>L</sub> ≤ 10 KΩ		-	3	20	mV
Common mode rejection ratio	CMRR	(Ta=25 °C)		65	90	-	dB
Power supply rejection ratio	PSRR	(Ta=25 °C)		65	100	-	dB
Output source current	I <sub>O+</sub>	V <sub>CC</sub> = 15V		(Ta=25 °C)	20	40	mA
		V <sub>IN+</sub> = 1V, V <sub>IN-</sub> = 0V			10	20	
Output sink current	I <sub>O-</sub>	V <sub>CC</sub> = 15V		(Ta=25 °C)	10	20	mA
		V <sub>IN+</sub> = 0V, V <sub>IN-</sub> = 1V			5	8	
		V <sub>OUT</sub> = 200mV,		(Ta=25 °C)	12	50	μA
		V <sub>IN+</sub> = 0V, V <sub>IN-</sub> = 1V					
Output short circuit to ground	I <sub>SC</sub>	Ta=25 °C		-	40	60	mA

## Electrical Characteristic Curves

**Fig. 1**  $I_{CC}$ - $V_{CC}$ **Fig. 2**  $I_{IB}$ - $V_{CC}$ **Fig. 3**  $V_{IOS}$ - $T_a$ **Fig. 4**  $I_O$ - $T_a$ **Fig. 5** CMRR-f**Fig. 6**  $V_{OR}$ -f