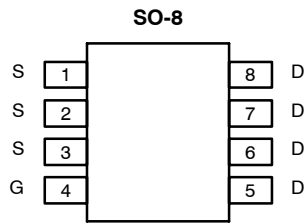


N-Channel 30-V (D-S) MOSFET

| PRODUCT SUMMARY | | |
|-----------------|---------------------------|-----------|
| V_{DS} (V) | $r_{DS(on)}$ (Ω) | I_D (A) |
| 30 | 0.0065 @ $V_{GS} = 10$ V | 23 |
| | 0.008 @ $V_{GS} = 4.5$ V | 17 |

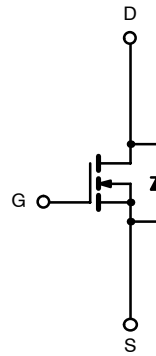
FEATURES

- TrenchFET® Power MOSFET
- 100% R_g Tested



Top View

Ordering Information: Si4404DY
Si4404DY-T1 (with Tape and Reel)



N-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED) | | | | | |
|---|----------------|--------------------------|--------------|------------------|-----|
| Parameter | Symbol | 10 secs | Steady State | Unit | |
| Drain-Source Voltage | V_{DS} | 30 | | V | |
| Gate-Source Voltage | V_{GS} | ± 20 | | | |
| Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a | I_D | $T_A = 25^\circ\text{C}$ | 23 | 15 | A |
| | | $T_A = 70^\circ\text{C}$ | 19 | 12 | |
| Pulsed Drain Current (10 μs Pulse Width) | I_{DM} | 60 | | | |
| Continuous Source Current (Diode Conduction) ^a | I_S | 2.9 | 1.3 | W | |
| Maximum Power Dissipation ^a | P_D | $T_A = 25^\circ\text{C}$ | 3.5 | | 1.6 |
| | | $T_A = 70^\circ\text{C}$ | 2.2 | 1 | |
| Operating Junction and Storage Temperature Range | T_J, T_{stg} | -55 to 150 | | $^\circ\text{C}$ | |

| THERMAL RESISTANCE RATINGS | | | | | |
|--|------------|-----------------|---------|------|--------------------|
| Parameter | Symbol | Typical | Maximum | Unit | |
| Maximum Junction-to-Ambient ^a | R_{thJA} | $t \leq 10$ sec | 29 | 35 | $^\circ\text{C/W}$ |
| | | Steady State | 67 | 80 | |
| Maximum Junction-to-Foot (Drain) | R_{thJF} | 13 | 16 | | |

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

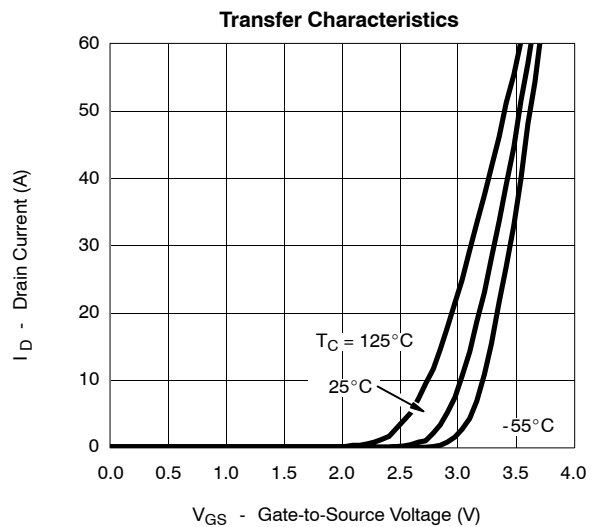
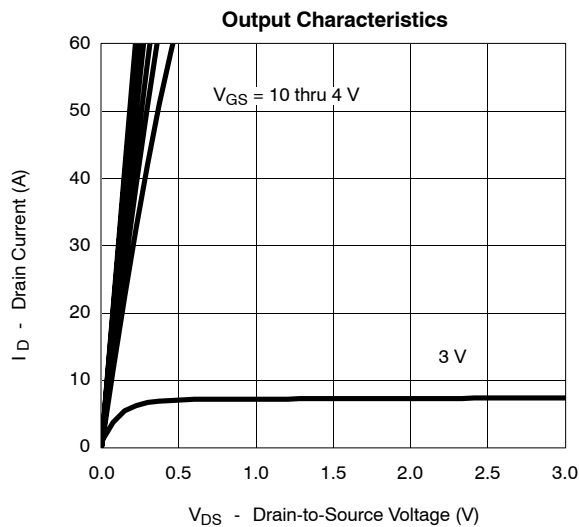
SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

| Parameter | Symbol | Test Condition | Min | Typ | Max | Unit |
|---|---------------------|--|-----|--------|--------|------|
| Static | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} = V _{GS} , I _D = 250 μA | 1.0 | | | V |
| Gate-Body Leakage | I _{GSS} | V _{DS} = 0 V, V _{GS} = ±20 V | | | ±100 | nA |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} = 30 V, V _{GS} = 0 V | | | 1 | μA |
| | | V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C | | | 5 | |
| On-State Drain Current ^a | I _{D(on)} | V _{DS} ≥ 5 V, V _{GS} = 10 V | 30 | | | A |
| Drain-Source On-State Resistance ^a | r _{DS(on)} | V _{GS} = 10 V, I _D = 23 A | | 0.0045 | 0.0065 | Ω |
| | | V _{GS} = 4.5 V, I _D = 17 A | | 0.0068 | 0.008 | |
| Forward Transconductance ^a | g _{fs} | V _{DS} = 15 V, I _D = 23 A | | 80 | | S |
| Diode Forward Voltage ^a | V _{SD} | I _S = 2.9 A, V _{GS} = 0 V | | 0.8 | 1.2 | V |
| Dynamic^b | | | | | | |
| Total Gate Charge | Q _g | V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 23 A | | 36 | 55 | nC |
| Gate-Source Charge | Q _{gs} | | | 15 | | |
| Gate-Drain Charge | Q _{gd} | | | 12 | | |
| Gate Resistance | R _g | | 1.5 | 2.2 | 3.7 | Ω |
| Turn-On Delay Time | t _{d(on)} | V _{DD} = 15 V, R _L = 15 Ω I _D ≅ 1 A, V _{GEN} = 10 V, R _G = 6 Ω | | 20 | 30 | ns |
| Rise Time | t _r | | | 15 | 23 | |
| Turn-Off Delay Time | t _{d(off)} | | | 105 | 160 | |
| Fall Time | t _f | | | 40 | 60 | |
| Source-Drain Reverse Recovery Time | t _{rr} | I _F = 2.9 A, di/dt = 100 A/μs | | 50 | 80 | |

Notes

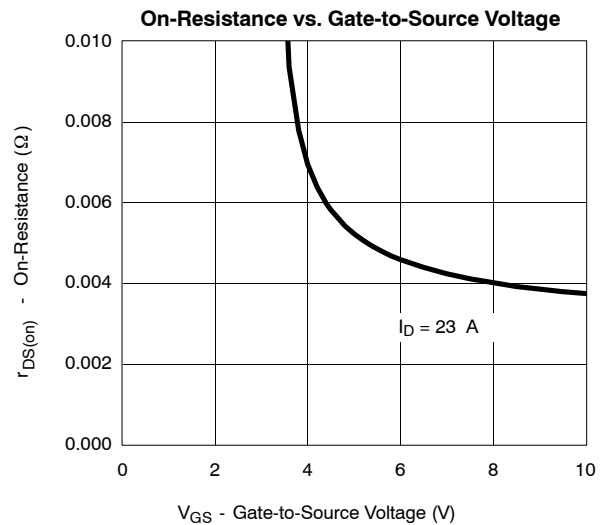
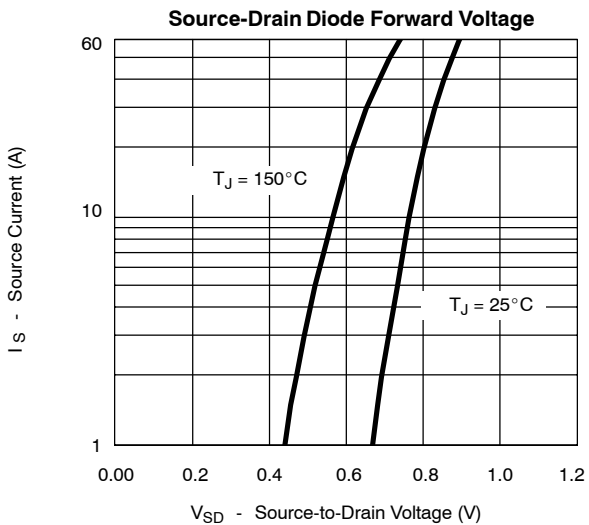
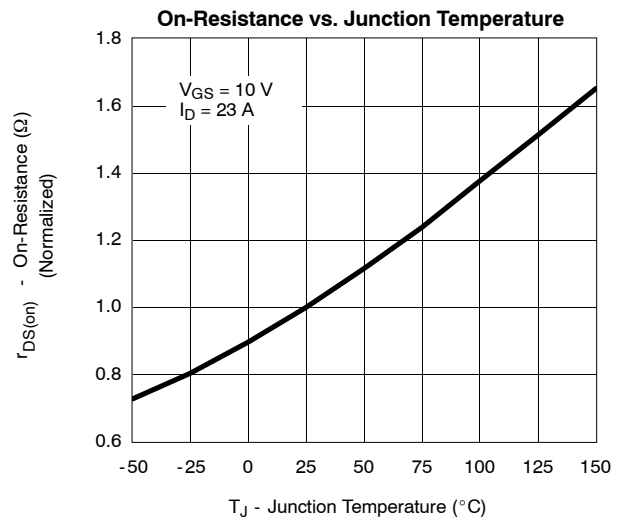
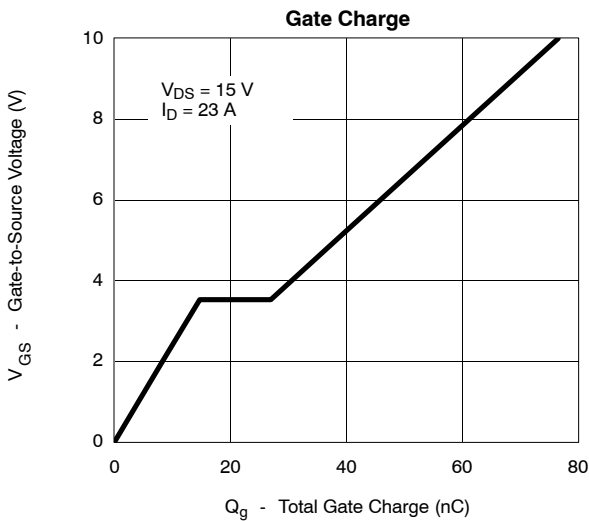
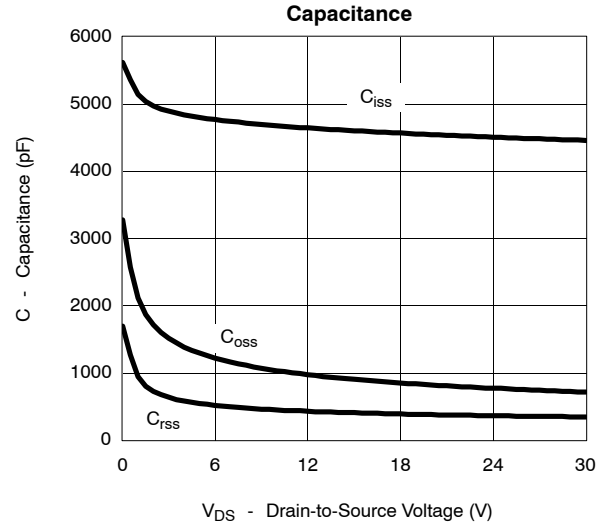
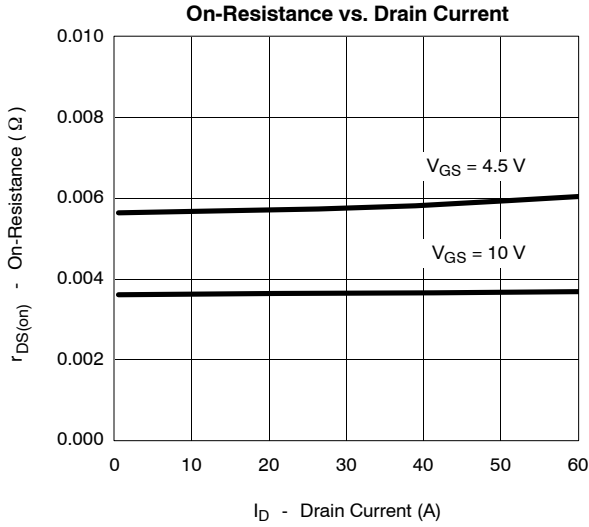
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)





TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)





TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

