

Features and Benefits

- Chopper stabilized amplifier stage
- New miniature package / thin, high reliability package
- Operation down to 3.5V
- Simingi CMOS for optimum stability, quality, and cost

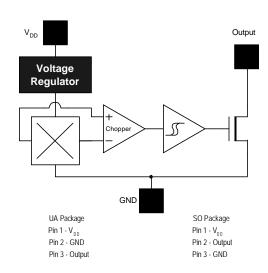
Applications

- Solid state switch
- Limit switch
- Current limit
- Interrupter

Ordering Information

Part No.	Temperature Suffix	Package	Temperature Range
US5781	E	SO or UA	-40°C to 85°C Extended
US5781	L	SO or UA	-40°C to 150°C Automotive

Functional Diagram



Description

The US5781 is a unipolar Hall effect sensor IC fabricated from mixed signal CMOS technology. It incorporates advanced chopper stabilization techniques to provide accurate and stable magnetic switch points. There are many applications for this sensor in addition to those listed above. The design, specifications and performance have been optimized for applications of solid state switches.

The output transistor will be switched on (B_{OP}) in the presence of a sufficiently strong South pole magnetic field facing the marked side of the package. Similarly, the output will be switched off (B_{RP}) in the presence of a weaker South field and remain off with "0" field. The SOT-23 device is reversed from the UA package. The SOT-23 output transistor will be switched on (BOP) in the presence of a sufficiently strong North pole magnetic field subjected to the marked face.

Note: This is a static-sensitive device; please observe ESD precautions. Reverse V_{DD} protection is not included. For reverse voltage protection, a 100 **W**resistor in series with V_{DD} is recommended.



US5781 Electrical SpecificationsDC operating parameters: $T_A = -40$ to 150° C, $V_{DD} = 12V_{DC}$ (unless otherwise specified).

Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Supply Voltage	V_{DD}	Operating	3.5		24	V
Supply Current	l _{DD}	B <b<sub>RP</b<sub>	0.5	2.5	5.0	mA
Saturation Voltage	V _{DS(on)}	$I_{OUT} = 20 \text{ mA}, B>B_{OP}$		0.3	0.5	V
Output Leakage	loff	B <b<sub>RP, V_{OUT} = 27V</b<sub>		0.01	5.0	ìΑ
Output Rise Time	tr	$V_{DD} = 12V$, $R_L = 1.1 \text{KÙ}$, $C_L = 20 \text{pf}$		0.04		ÌS
Output Fall Time	tr	$V_{DD} = 12V, R_L = 1.1 \text{KÙ}, C_L = 20 \text{pf}$		0.18		ÌS

US5781 Magnetic Specifications

Magnetic operating parameters: $T_A = -40$ to 150°C, $V_{DD} = 12$ V_{DC} (unless otherwise specified).

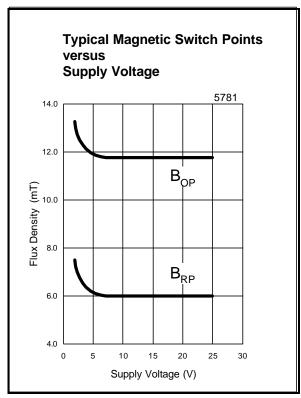
Parameter	Symbol Test Conditions	Min	Тур	Max	Units
Operating Point	B _{OP}	7.0	12.0	15.0	mT
Release Point	B_RP	3.5	7.0	10.0	mT
Hysteresis	B _{hys}	2.0	5.0	7.0	mT

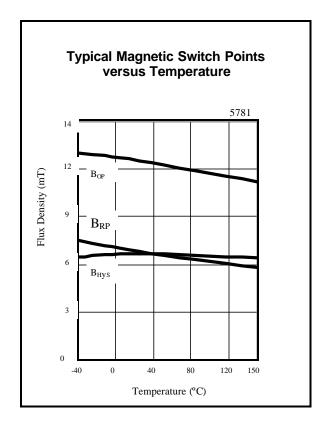
Note: 1 mT = 10 Gauss.

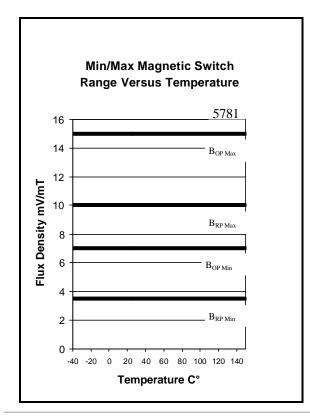
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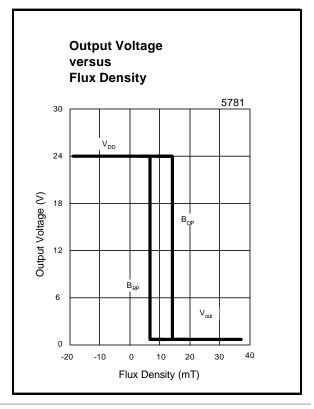


Performance Graphs



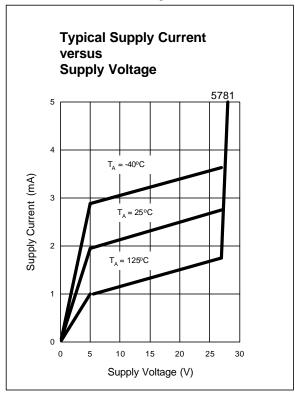


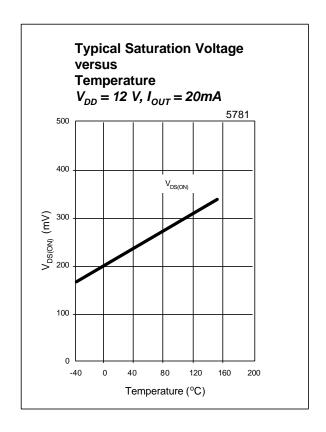


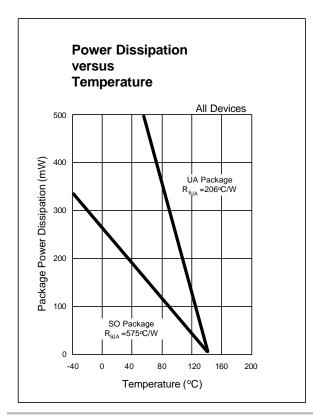


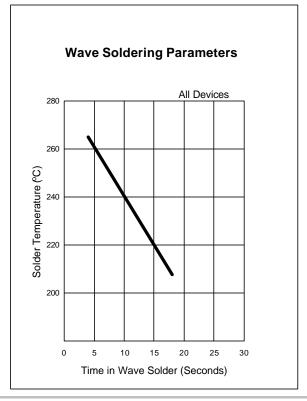


Performance Graphs











Unique Features CMOS Hall IC Technology

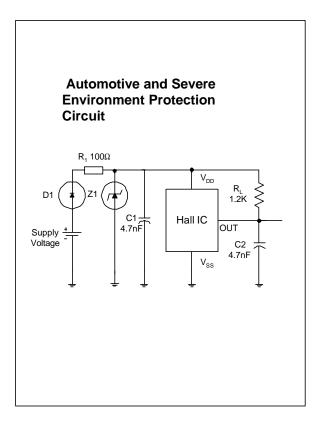
The chopper stabilized amplifier uses switched capacitor techniques to eliminate the amplifier offset voltage, which, in bipolar devices, is a major source of temperature sensitive drift. CMOS makes this advanced technique possible. The CMOS chip is also much smaller than a bipolar chip, allowing very sophisticated circuitry to be placed in less space. The small chip size also contributes to lower physical stress and less power consumption.

Installation Comments

Consider temperature coefficients of Hall IC and magnetics, as well as air gap life time variations. Observe temperature limits during wave soldering.

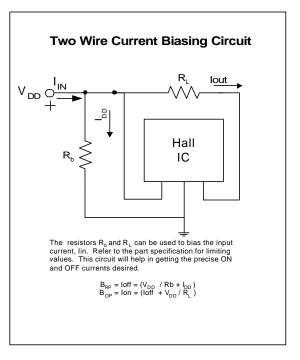
Applications

If reverse supply protection is desired, use a resistor in series with the V_{DD} pin. The resistor will limit the supply current (Fault), I_{DD} , to 50 mA. For severe EMC conditions, use the application circuit on this page.



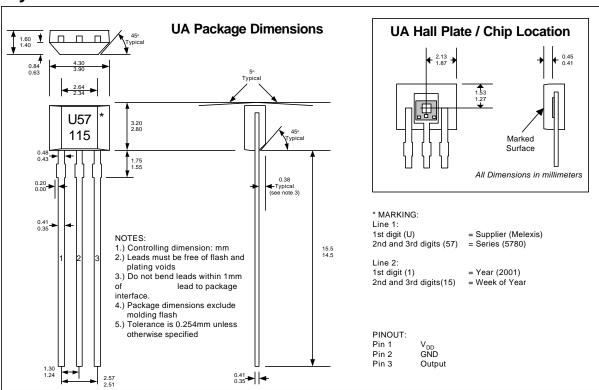
Absolute Maximum Ratings

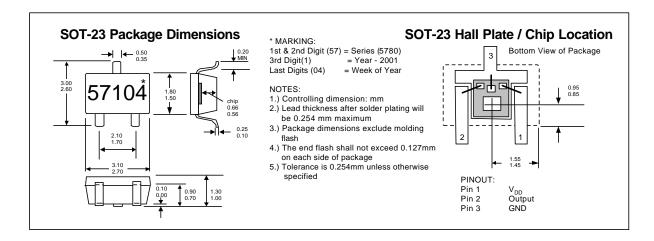
Supply Voltage (Operating), V _{DD}	3.5V to 24V		
Supply Current (Fault), IDD	50mA		
Output Voltage, V _{OUT}	3.5V to 24V		
Output Current (Fault), I _{OUT}	50mA		
Power Dissipation, P _D	100mW		
Operating Temperature Range, T _A	-40 to 150°C		
Storage Temperature Range, T _S	-65 to 150°C		
Maximum Junction Temp, T _J	175°C		
ESD Sensitivity (All Pins)	+/- 2KV		





Physical Characteristics





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