

Single-chip design for high performance and low cost

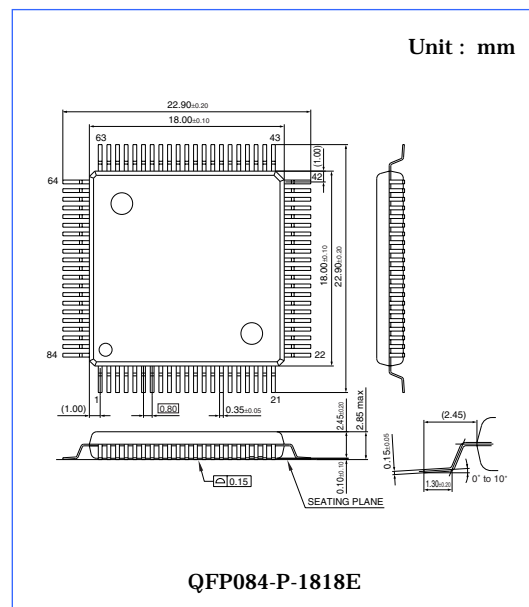
1-Chip Signal Processor for Analog TVs MN102H76G/81G

■ Overview

MN102H76G/81G is a single-chip signal processor that integrates a TV image signal processing circuit, a 3-line comb filter and a channel selection microcomputer. This product makes it possible to develop high-performance NTSC analog TV receivers at low cost.

■ Features

- Integrates a video signal processor circuit and a channel selection microcomputer into a single chip configuration. This allows video processing in a digital domain, heretofore only possible in analog. The result is a wide range of advanced software-based image adjustment functions.
- High-quality, high-performance OSD (On-Screen Display) with 3-layer configuration for 4,096-color reproduction and semi-transparent 16 gradation scale alpha blending.
- Single chip design integrates TV image signal processing circuit, 3-line comb filter and channel selection microcomputer. This lessens the need for peripheral components and trims mounting space for total cost reduction.



■ Applications

- NTSC analog TV receivers

■ Specifications

Product Name	MN102H76G/81G
Functions	16-bit microcomputer with comb filter and VCJ for NTSC system analog television receivers · 3-line comb filter, color decoder circuit, synchronous separation circuit, deflection circuit and RGB processor built-in · Alpha blend 3-layer OSD circuit and caption decoder built-in · 16-bit microcomputer
Process	CMOS 0.30 μm 3 metals
Operating Frequency	28.64 MHz
Power Supply Voltage	3.3 V
Power Consumption	700 mW
Package	84-pin QFP package (18 mm × 18 mm)

Products and specifications are subject to change without notice. Please ask for the latest Product Standards to guarantee the satisfaction of your product requirements.

Semiconductor Company, Matsushita Electric Industrial Co., Ltd.

■ Block Diagram of NTSC System

